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# **NCE2301E**

### NCE P-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE2301E uses advanced trench technology to provide excellent R<sub>DS(ON)</sub>, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications .It is ESD protested.

#### **General Features**

● V<sub>DS</sub> = -20V,I<sub>D</sub> =-2.6A

 $R_{DS(ON)}$  < 150m $\Omega$  @  $V_{GS}$ =-2.5V

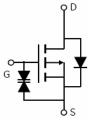
 $R_{DS(ON)}$  < 120m $\Omega$  @  $V_{GS}$ =-4.5V

ESD Rating: 2000V HBM

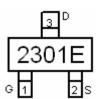
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

### **Application**

Load switch



Schematic diagram



Marking and pin assignment



SOT-23 top view

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2301E	NCE2301E	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	-20	V
Gate-Source Voltage	V <sub>G</sub> s	±10	V
Drain Current-Continuous	I <sub>D</sub>	-2.6	Α
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-13	Α
Maximum Power Dissipation	P <sub>D</sub>	1.0	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	℃

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	125	°C/W

#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V,V <sub>GS</sub> =0V	-	-	-1	μΑ



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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V,V <sub>DS</sub> =0V	-	-	±10	μΑ
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	0.40	0.7	1.0	V
Drain Sauras On State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	87	120	mΩ
Drain-Source On-State Resistance		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A	-	125	150	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-5V,I <sub>D</sub> =-2A	5		-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ - 40\/\/ -0\/	-	325	-	PF
Output Capacitance	Coss	$V_{DS}$ =-10V, $V_{GS}$ =0V, F=1.0MHz	-	63	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0WH2	-	37	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	11		nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-10 $V$ , $R_L$ =1. $5\Omega$	-	5.5		nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{GEN}$ =3 $\Omega$	-	22		nS
Turn-Off Fall Time	t <sub>f</sub>		-	8		nS
Total Gate Charge	Qg	\/ - 40\/   - 24	-	3.2		nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-10V, $I_{D}$ =-2A,	-	0.6	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =-4.5V	-	0.9	-	nC
Drain-Source Diode Characteristics						-
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-2.6A	-	-	-1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	-2.6	Α

### Notes:

- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

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### **Typical Electrical and Thermal Characteristics**

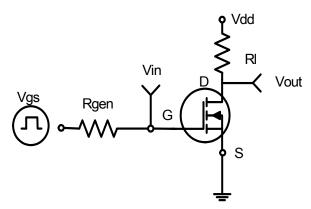
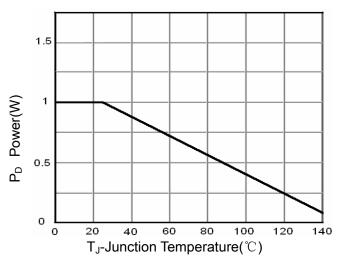
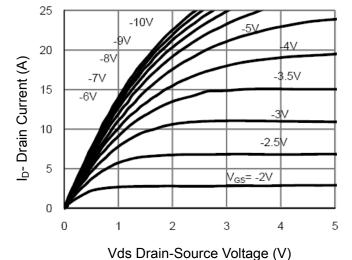


Figure 1:Switching Test Circuit



**Figure 3 Power Dissipation** 



**Figure 5 Output Characteristics** 

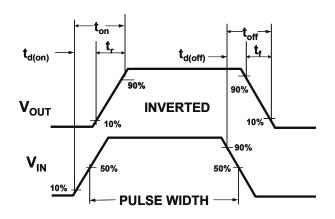


Figure 2:Switching Waveforms

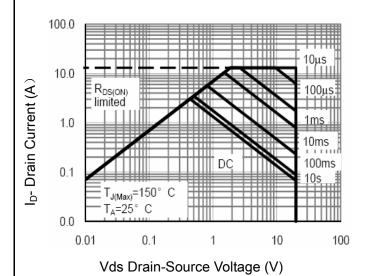


Figure 4 Safe Operation Area

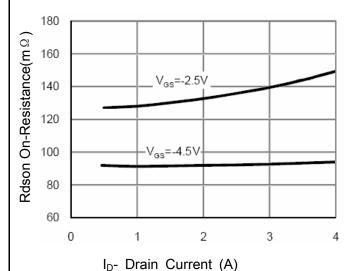
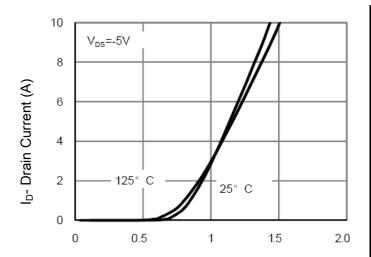


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics

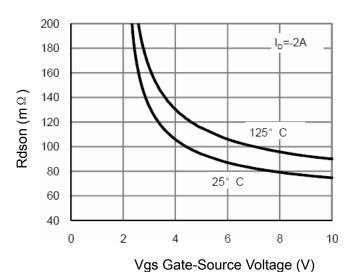


Figure 9 Rdson vs Vgs

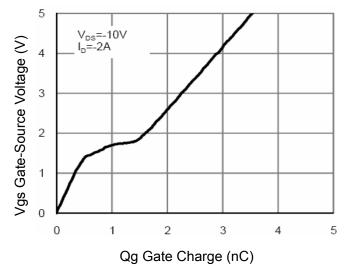


Figure 11 Gate Charge

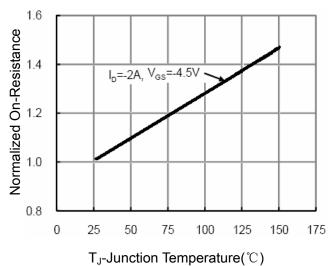


Figure 8 Drain-Source On-Resistance

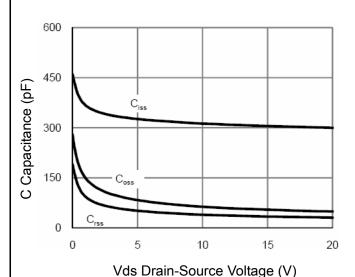


Figure 10 Capacitance vs Vds

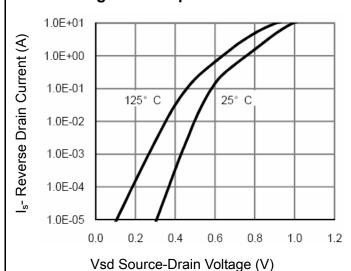
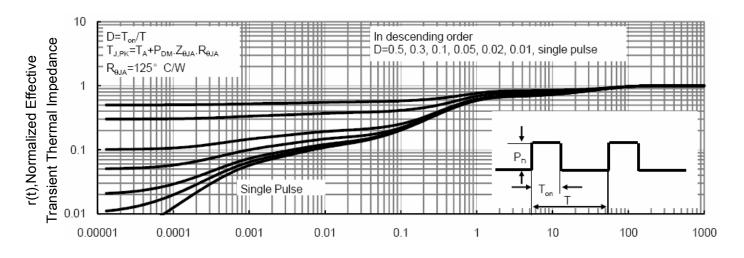


Figure 12 Source- Drain Diode Forward







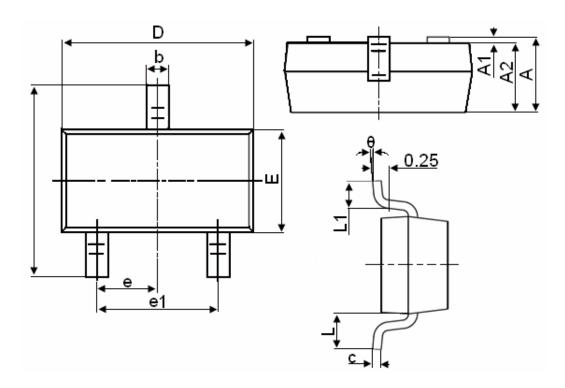
Square Wave Pluse Duration(sec)

Figure 13 Normalized Maximum Transient Thermal Impedance

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# **SOT-23 Package Information**



Symbol	Dimensions in Millimeters				
Syllibol	MIN.	MAX.			
Α	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е		0.950TYP			
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

#### **Notes**

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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