

DUAL ENHANCEMENT MODE (N-CHANNEL/P-CHANNEL)

■ DESCRIPTION

The UTC **UD4606Z** is a dual enhancement mode Power MOSFET using UTC perfect trench technology to provide customers with advanced $R_{DS(ON)}$ and low gate charge. This device has ESD protection function.

These complementary MOSFETs can be used to form a level shifted high side switch and for other applications.

■ FEATURES

* N-Channel: 30V/6.9A

$R_{DS(ON)}=22.5\text{m}\Omega$ (TYP.) ($V_{GS}=10\text{V}$)

$R_{DS(ON)}=34.5\text{m}\Omega$ (TYP.) ($V_{GS}=4.5\text{V}$)

*P-Channel: -30V/-6A

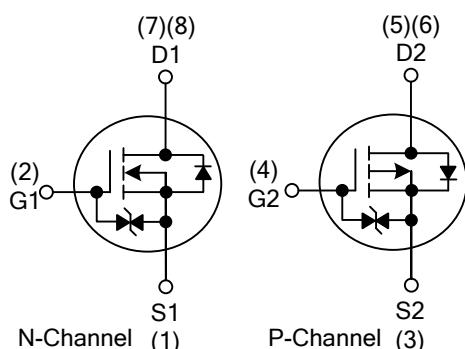
$R_{DS(ON)}=28\text{m}\Omega$ (TYP.) ($V_{GS}=-10\text{V}$)

$R_{DS(ON)}=44\text{m}\Omega$ (TYP.) ($V_{GS}=-4.5\text{V}$)

* Reliable and rugged

* ESD protection

■ SYMBOL

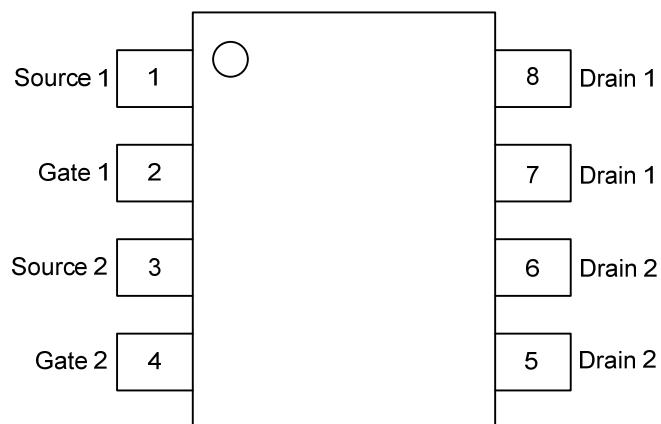


■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free Plating	Halogen Free		1	2	3	4	5	6	7	8	
UD4606ZL-S08-R	UD4606ZG-S08-R	SOP-8	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel

UD4606ZL - S08 - R	(1) Packing Type (2) Package Type (3) Halogen Free	(1) R: Tape Reel, T: Tube (2) S08: SOP-8 (3) G: Halogen Free
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■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

N-CHANNEL

PARAMETER	SYMBOL	RATINGS	UNIT
Drain to Source Voltage	V_{DSS}	30	V
Gate to Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ²	I_D	6.9	A
Pulsed Drain Current ²	I_{DM}	30	A
Total Power Dissipation	P_D	2	W
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

P-CHANNEL

PARAMETER	SYMBOL	RATINGS	UNIT
Drain to Source Voltage	V_{DSS}	-30	V
Gate to Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ²	I_D	-6	A
Pulsed Drain Current ²	I_{DM}	-30	A
Total Power Dissipation	P_D	2	W
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Note:1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Surface Mounted on 1in² pad area, t≤10sec.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Note)	$R_{\theta JA}$	110	$^\circ\text{C}/\text{W}$

Note: Surface Mounted on 1in² pad area, t≤10sec.

■ ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$, unless otherwise specified)

N-CHANNEL

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm20\text{V}$			±5	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.9	3	V
Drain-Source On-State Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6.9\text{A}$		22.5	28	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$		34.5	42	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		680		pF
Output Capacitance	C_{OSS}			102		pF
Reverse Transfer Capacitance	C_{RSS}			77		pF
SWITCHING PARAMETERS						
Total Gate Charge ²	Q_{G}	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=6.9\text{A}$		13.8		nC
Gate-Source Charge	Q_{GS}			1.82		nC
Gate-Drain Charge	Q_{GD}			3.2		nC
Turn-ON Delay Time ²	$t_{\text{D}(\text{ON})}$	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=3\Omega, R_{\text{L}}=2.2\Omega$		4.6		ns
Turn-ON Rise Time	t_{R}			4.1		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			20.6		ns
Turn-OFF Fall Time	t_{F}			5.2		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Diode Continuous Forward Current ³	I_{S}				3	A
Drain-Source Diode Forward Voltage ²	V_{SD}	$I_{\text{S}}=6.9\text{A}, V_{\text{GS}}=0\text{V}$		0.76	1	V

P-CHANNEL

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}$			-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm20\text{V}$			±5	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.2	-2	-2.4	V
Drain-Source On-State Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-6\text{A}$		28	35	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-5\text{A}$		44	58	$\text{m}\Omega$
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		920		pF
Output Capacitance	C_{OSS}			190		pF
Reverse Transfer Capacitance	C_{RSS}			122		pF
SWITCHING PARAMETERS						
Total Gate Charge ²	Q_{G}	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-6\text{A}$		18.5		nC
Gate-Source Charge	Q_{GS}			2.7		nC
Gate-Drain Charge	Q_{GD}			4.5		nC
Turn-ON Delay Time ²	$t_{\text{D}(\text{ON})}$	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}, R_{\text{G}}=3\Omega, R_{\text{L}}=2.7\Omega$		7.7		ns
Turn-ON Rise Time	t_{R}			5.7		ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			20.2		ns
Turn-OFF Fall Time	t_{F}			9.5		ns

■ ELECTRICAL CHARACTERISTICS(Cont.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Diode Continuous Forward Current ³	I _S				-4.2	A
Drain-Source Diode Forward Voltage ²	V _{SD}	I _S =-6A, V _{GS} =0V		-0.76	-1	V

Notes:

1. Pulse width limited by T_{J(MAX)}.
2. Pulse width ≤300μs, duty cycle ≤2%.
3. Surface Mounted on 1in² pad area, t≤10sec.

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