



An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

PNP EXPITAXIAL PLANAR SILICON TRANSISTORS

BC303, BC304



TO-39 Metal Can Package

PNP SILICON LOW-AND MEDIUM POWER TRANSISTORS.

ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

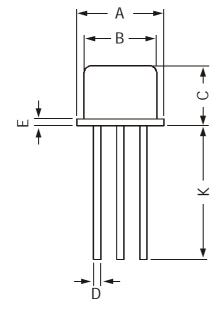
DESCRIPTION	SYMBOL	BC303	BC304	UNITS
Collector Emitter Voltage	V_{CEO}	60	45	V
Collector Base Voltage	V_{CBO}	85	60	V
Emitter Base Voltage	V_{EBO}	7.0	7.0	V
Collector Current	I_{C}	500	500	
Power Dissipation @ Ta=25°C	P_{D}	850	mW	
Junction Temperature	T_j	175		°C
StorageTemperature Range	T_{stg}	-65 to +200		°C

ELECTRICAL CHARACTERISTICS (Ta=25° C unless specified otherwise)

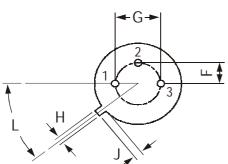
		L TEST CONDITION	BC303		BC304		
DESCRIPTION	SYMBO		MIN	MAX	MIN	MAX	UNITS
Collector Cut off Current	I _{CBO}	V_{CB} =85V, I_{E} =0		20			nA
		V_{CB} =60V, I_E =0				20	nA
DC Current Gain	h_{FE}	BC303 / BC304					
		$I_C=150$ mA, $V_{CE}=10$ V	40	240			
		BC303 / 304-5	70	140			
		$I_C=150$ mA, $V_{CE}=10$ V					
		BC303 / 304-6	120	240			
		$I_C=150$ mA, $V_{CE}=10$ V					
Collector Emitter Sat. Voltage	$V_{CE(Sat)}$	$I_C=150$ mA, $I_B=15$ mA	ALL	0.65			V
DYNAMICS CHARACTERISTICS							
Transition Frequency	f_T	I_C =50mA, V_{CE} =10V f=100MHz	ALL		0.65		MHz

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DIM	MIN	MAX
Α	8.50	9.39
В	7.74	8.50
С	6.09	6.60
D	0.40	0.53
Ε		0.88
F	2.41	2.66
G	4.82	5.33
Н	0.71	0.86
J	0.73	1.02
Κ	12.70	_
L	42 DEG	48 DEG





All dimensions are in mm

PIN CONFIGURATION

- 1. EMITTER
- 2. BASE
- 3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-39	500 pcs/polybag	540 gm/500 pcs	3" x 7.5" x 7.5"	20K	17" x 15" x 13.5"	32K	40 kgs

Notes BC303, BC304

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Data Sheet

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