



SPP3403

P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP3403 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where high-side switching , and low in-line power loss are needed in a very small outline surface mount package.

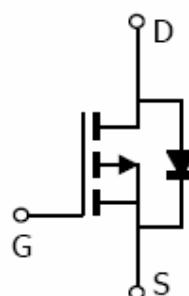
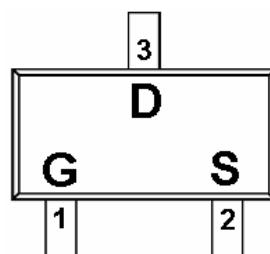
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

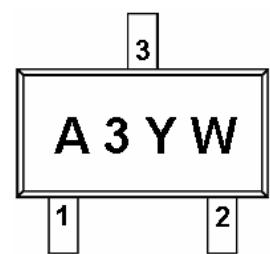
FEATURES

- ◆ -30V/-2.8A,RDS(ON)=100mΩ@VGS=- 10V
- ◆ -30V/-2.5A,RDS(ON)=110mΩ@VGS=-4.5V
- ◆ -30V/-1.5A,RDS(ON)=145mΩ@VGS=-2.5V
- ◆ -30V/-1.0A,RDS(ON)=200mΩ@VGS=-1.8V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23-3L package design

PIN CONFIGURATION (SOT-23-3L)



PART MARKING



Y : Year Code
W : Week Code



SPP3403

P-Channel Enhancement Mode MOSFET

PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP3403S23RG	SOT-23-3L	A3YW
SPP3403S23RGB	SOT-23-3L	A3YW

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPP3403S23RG : Tape Reel ; Pb – Free

※ SPP3403S23RGB : Tape Reel ; Pb – Free ; Halogen – Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	-30	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	-3.5	A
	T _A =70°C		
Pulsed Drain Current	I _{DM}	-20	A
Continuous Source Current(Diode Conduction)	I _S	-1.4	A
Power Dissipation	T _A =25°C	1.25	W
	T _A =70°C		
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	105	°C/W



SPP3403

P-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

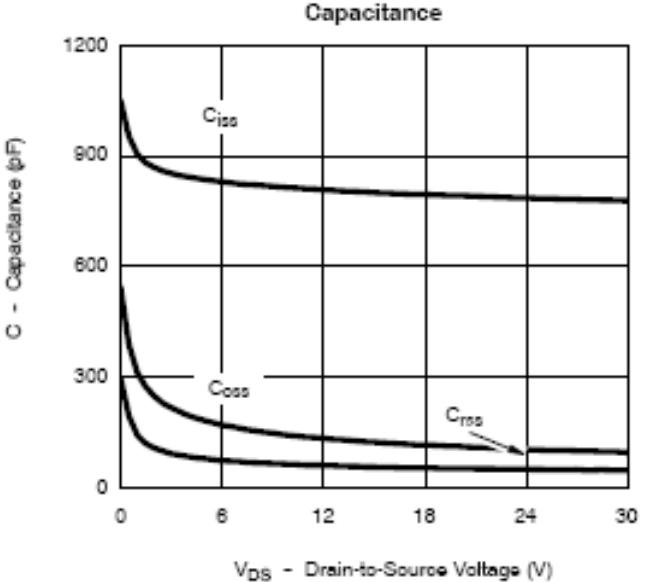
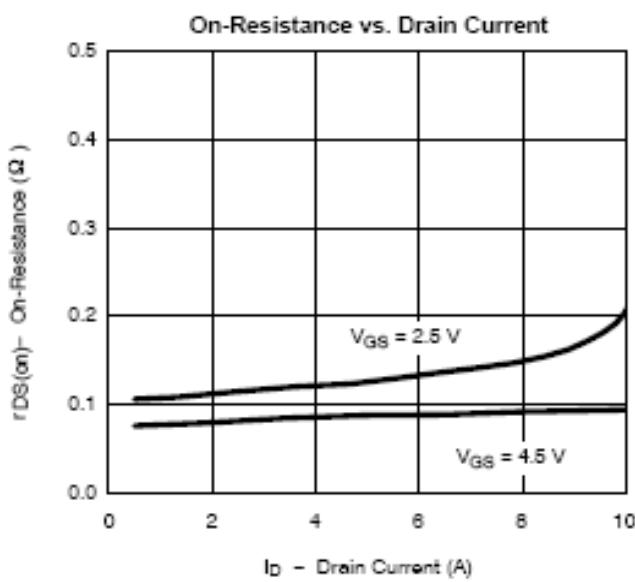
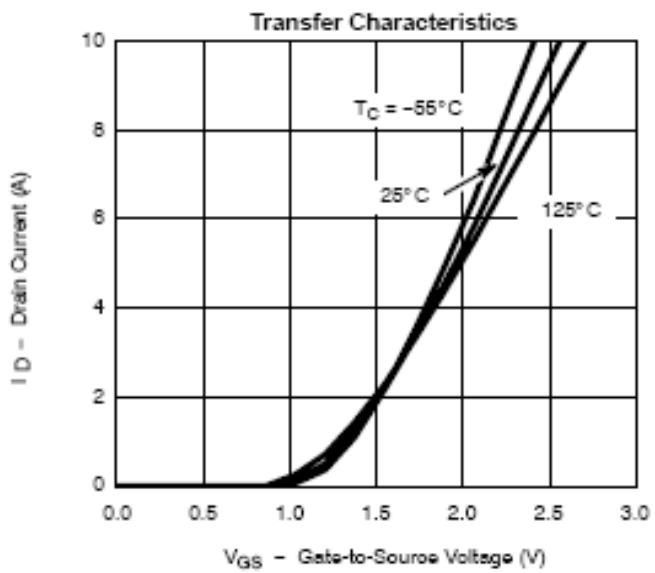
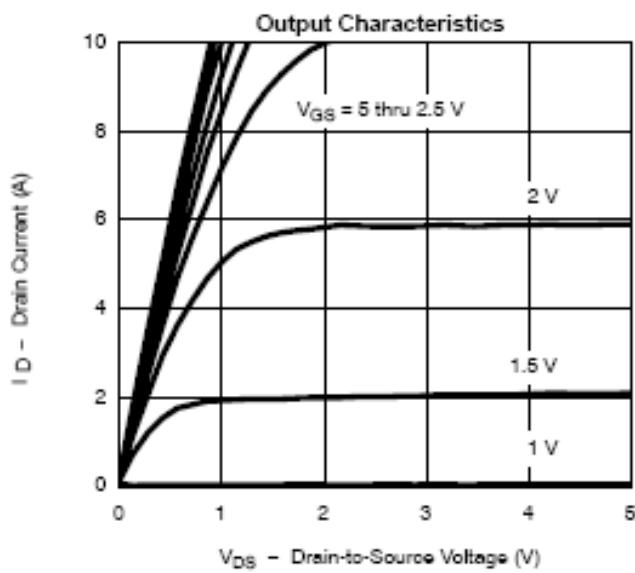
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-30			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=-250uA	-0.4		-1.0	
Gate Leakage Current	IGSS	VDS=0V, VGS=±12V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=-24V, VGS=0V			-1	uA
		VDS=-24V, VGS=0V TJ=85°C			-5	
On-State Drain Current	ID(on)	VDS= -5V, VGS =-4.5V	-4			A
Drain-Source On-Resistance	RDS(on)	VGS=-10V, ID=-2.8A		0.080	0.100	Ω
		VGS=-4.5V, ID=-2.5A		0.100	0.110	
		VGS=-2.5V, ID=-1.5A		0.130	0.145	
		VGS=-1.8V, ID=-1.0A		0.160	0.200	
Forward Transconductance	gfs	VDS=-10V, ID=-2.8A		4		S
Diode Forward Voltage	VSD	IS=-1.2A, VGS =0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Qg	VDS=-15V , VGS=-4.5V ID=-2.0A		5.8		nC
Gate-Source Charge	Qgs			0.8		
Gate-Drain Charge	Qgd			1.5		
Input Capacitance	Ciss	VDS=-15V , VGS=0V f=1MHz		380		pF
Output Capacitance	Coss			55		
Reverse Transfer Capacitance	Crss			40		
Turn-On Time	td(on)	VDD=-15V , RL=15Ω ID=-1.0A , VGEN=-10V RG=3Ω		6		ns
	tr			3.9		
Turn-Off Time	td(off)			40		
	tf			15		



SPP3403

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

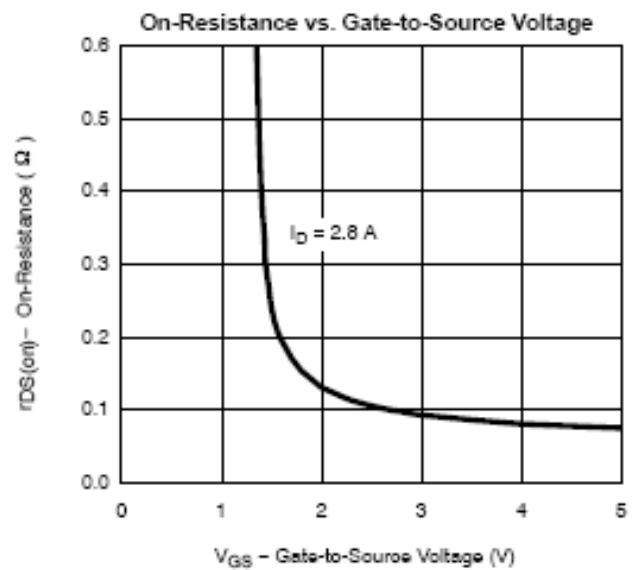
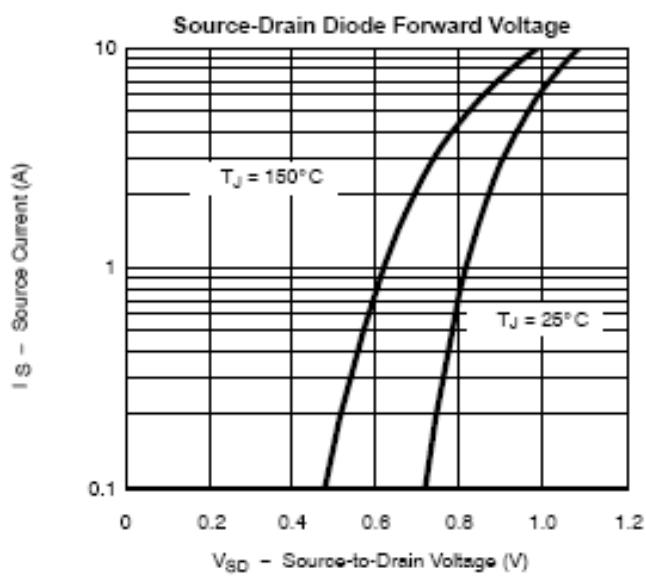
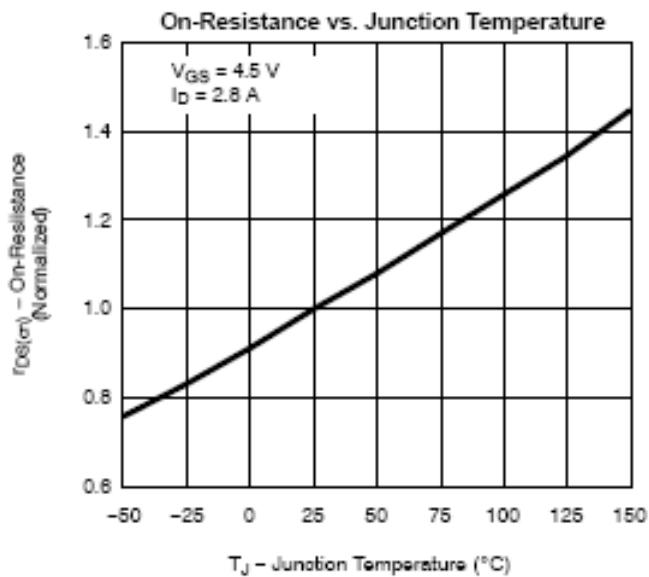
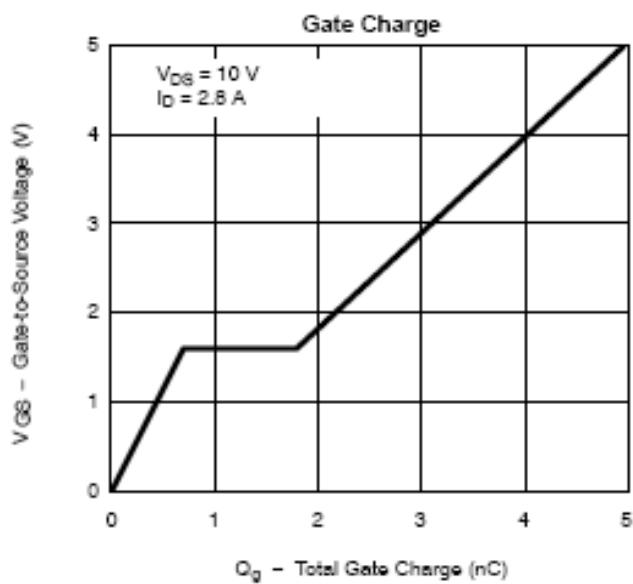




SPP3403

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

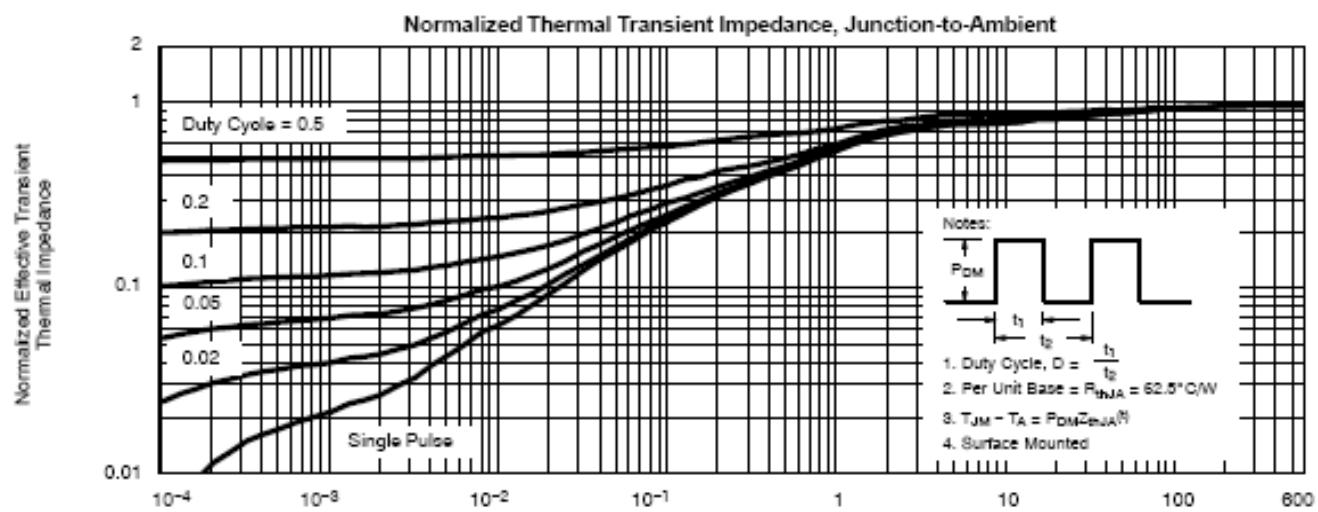
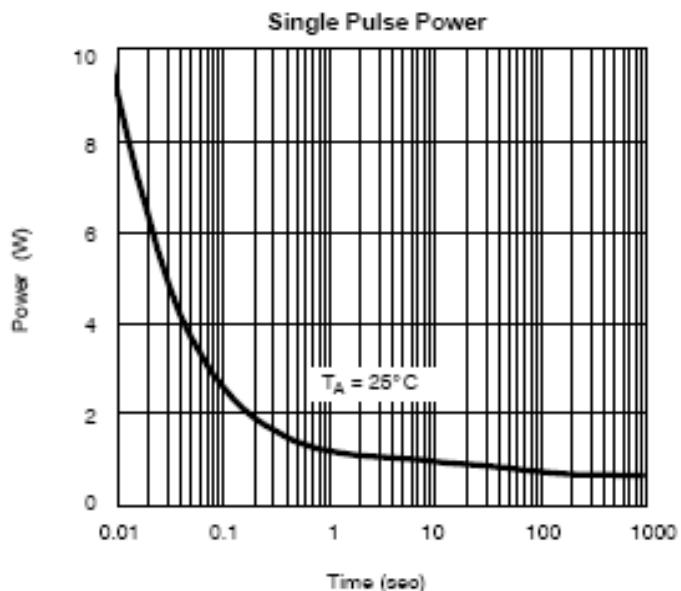
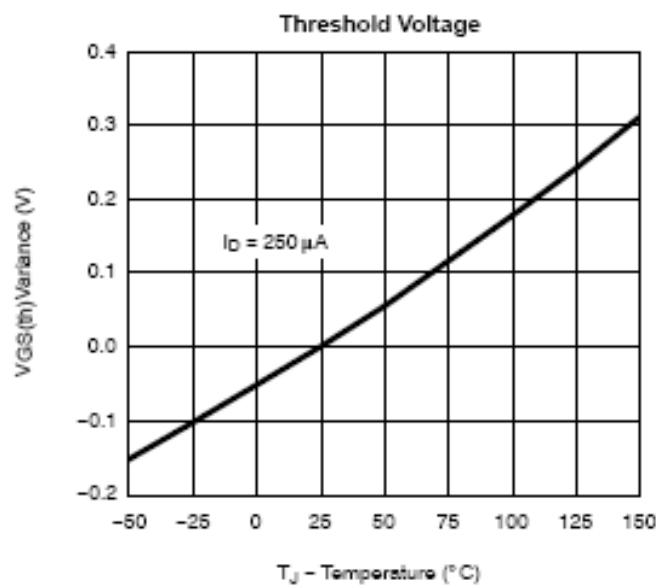




SPP3403

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

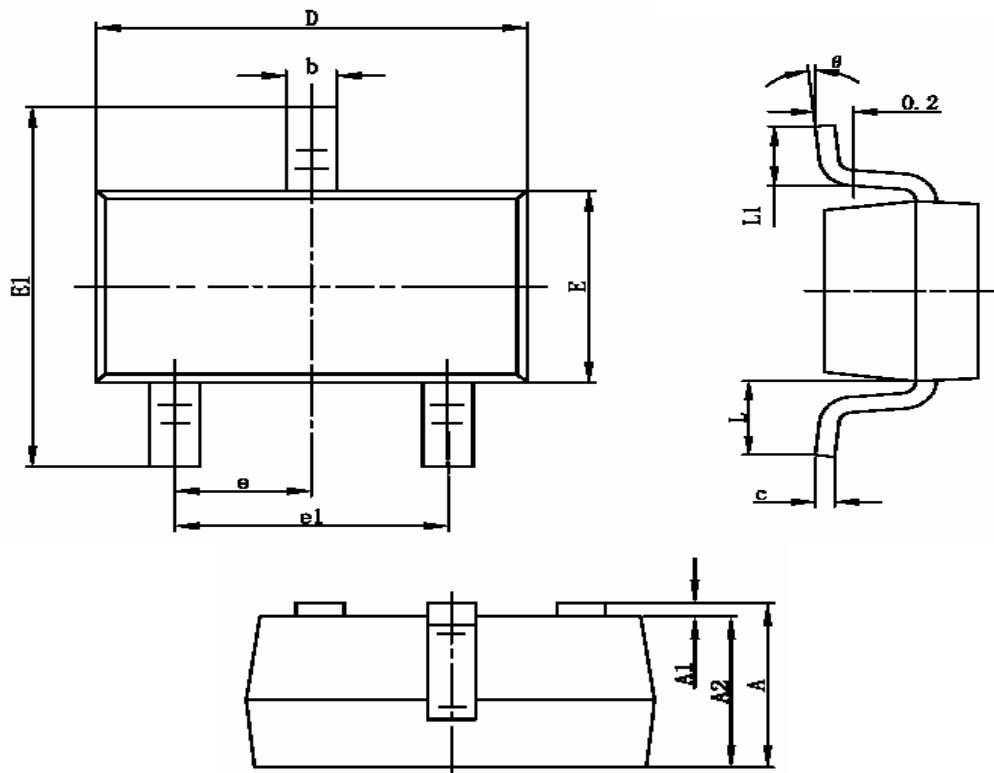




SPP3403

P-Channel Enhancement Mode MOSFET

SOT-23-3L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.700REF		0.028REF	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



SPP3403

P-Channel Enhancement Mode MOSFET

Information provided is alleged to be exact and consistent. SYNC Power Corporation presumes no responsibility for the penalties of use of such information or for any violation of patents or other rights of third parties which may result from its use. No license is granted by allegation or otherwise under any patent or patent rights of SYNC Power Corporation. Conditions mentioned in this publication are subject to change without notice. This publication surpasses and replaces all information previously supplied. SYNC Power Corporation products are not authorized for use as critical components in life support devices or systems without express written approval of SYNC Power Corporation.

©The SYNC Power logo is a registered trademark of SYNC Power Corporation

©2004 SYNC Power Corporation – Printed in Taiwan – All Rights Reserved

SYNC Power Corporation

9F-5, No.3-2, Park Street

NanKang District (NKSP), Taipei, Taiwan 115

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

©<http://www.syncpower.com>