

Digital Transistors (Built-in Resistors)

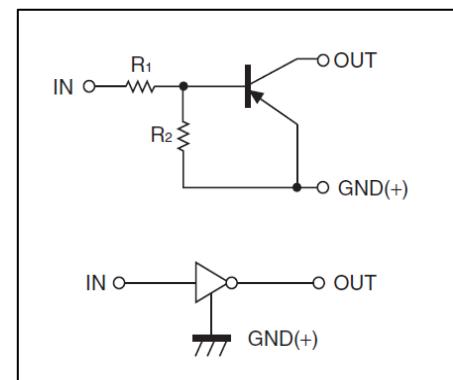
DTA124EM/DTA124EE/DTA124EUA DTA124EKA /DTA124ECA/DTA124ESA

DIGITAL TRANSISTOR (PNP)

FEATURES

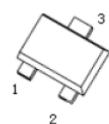
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

• Equivalent Circuit



PIN CONNECTIONS and MARKING

DTA124EM

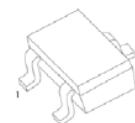


MARKING:15

SOT-723

1. IN
2. GND
3. OUT

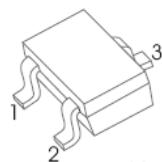
DTA124EE



SOT-523

1. IN
2. GND
3. OUT

DTA124EUA

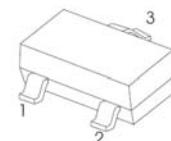


MARKING:15

SOT-323

1. IN
2. GND
3. OUT

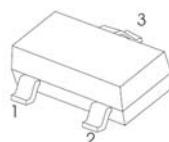
DTA124EKA



SOT-23-3L

1. IN
2. GND
3. OUT

DTA124ECA



MARKING:15

SOT-23

1. IN
2. GND
3. OUT

DTA124ESA



TO-92S

1. GND
2. OUT
3. IN

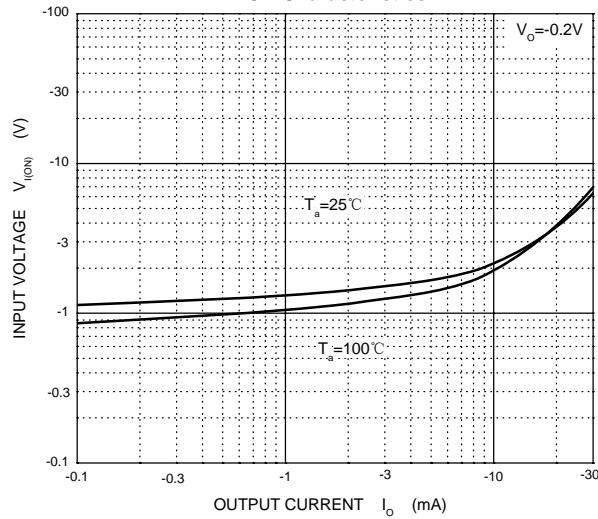
MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits(DTA124E□)						Unit
		M	E	UA	KA	CA	SA	
V _{CC}	Supply Voltage	-50						V
V _{IN}	Input Voltage	-40~+10						V
I _O	Output Current	-30						mA
I _{CM}	Peak Collector Current	-100						mA
P _D	Power Dissipation	100	150	200	200	200	300	mW
T _j	Junction Temperature	150						°C
T _{stg}	Storage Temperature	-55~+150						°C

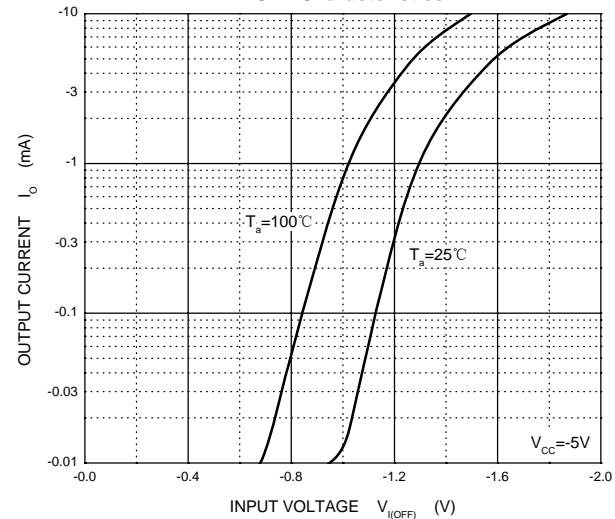
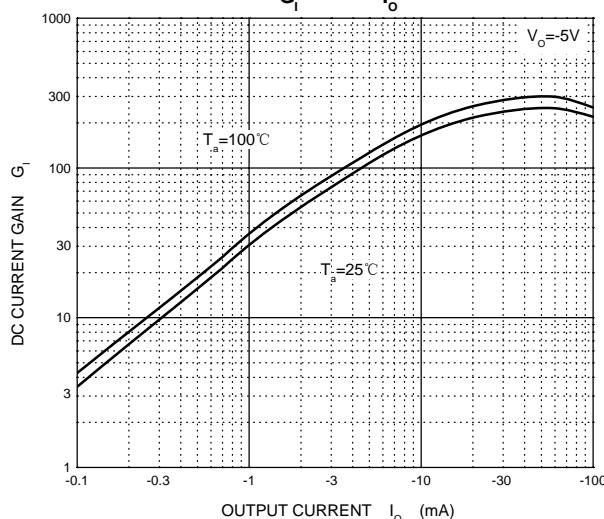
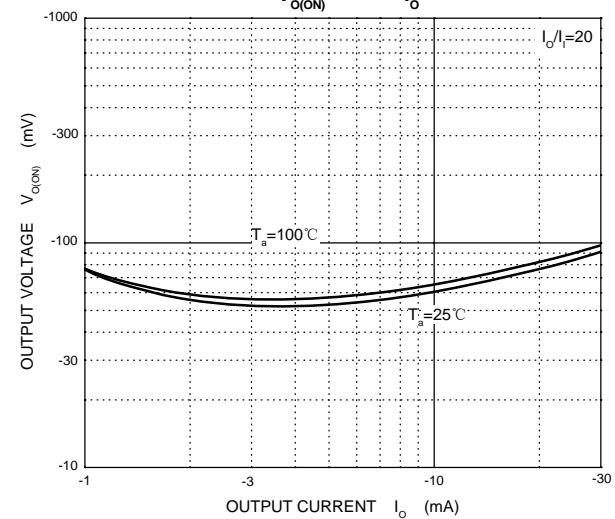
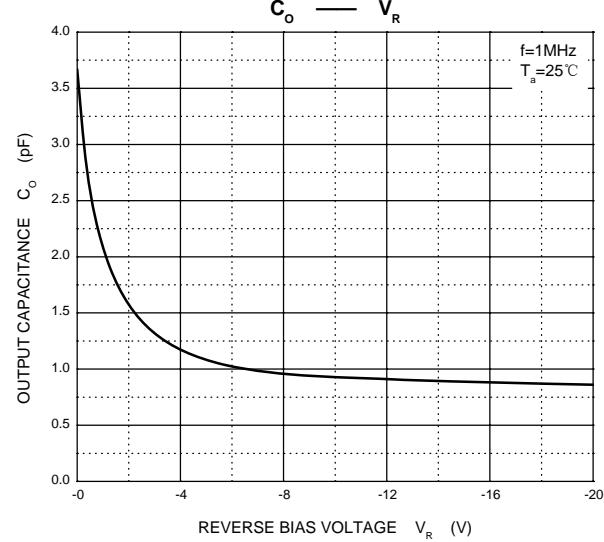
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =-5V,I _O =-100μA	-0.5			V
	V _{I(on)}	V _O =-0.2V,I _O =-5 mA			-3	V
Output voltage	V _{O(on)}	I _O /I _I =-10mA/-0.5mA			-0.3	V
Input current	I _I	V _I =-5V			-0.36	mA
Output current	I _{O(off)}	V _{CC} =-50V,V _I =0			-0.5	μA
DC current gain	G _I	V _O =-5V,I _O =-5mA	56			
Input resistance	R _I		15.4	22	28.6	kΩ
Resistance ratio	R ₂ /R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =-10V,I _O =-5mA,f=100MHz		250		MHz

ON Characteristics



OFF Characteristics

 $G_I \text{ --- } I_o$  $V_{O(ON)} \text{ --- } I_o$  $C_o \text{ --- } V_R$  $P_d \text{ --- } T_a$ 