TOSHIBA Diode Silicon Epitaxial Planar Type

# **1SS301**

### **Ultra High Speed Switching Applications**

Unit: mm

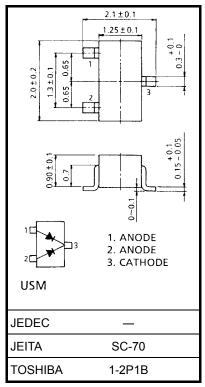
• Small package : SC-70

 $\begin{array}{ll} \bullet & \text{Low forward voltage} & : V_{F} \, (3) = 0.9 \; V \, (\text{typ.}) \\ \bullet & \text{Fast reverse recovery time} \colon t_{rr} = 1.6 \; \text{ns (typ.}) \\ \bullet & \text{Small total capacitance} & : C_{T} = 0.9 \; \text{pF (typ.)} \\ \end{array}$ 

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	$V_{RM}$	85	V	
Reverse voltage	V <sub>R</sub>	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	300 (*)	mA	
Average forward current	Io	100 (*)	mA	
Surge current (10 ms)	I <sub>FSM</sub>	2 (*)	Α	
Power dissipation	Р	100	mW	
Junction temperature	Tj	125	°C	
Storage temperature	T <sub>stg</sub>	-55~125	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.



Weight: 0.006 g (typ.)

operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

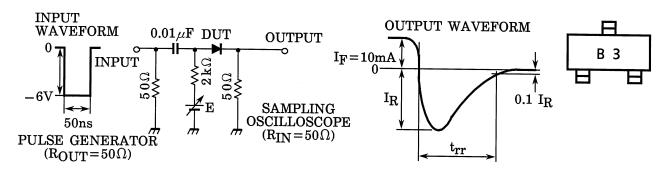
\*: Unit rating. Total rating = unit rating × 1.5

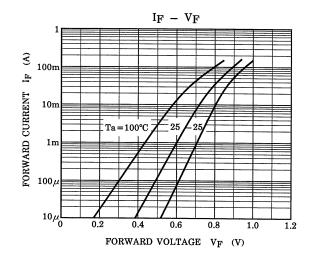
#### **Electrical Characteristics (Ta = 25°C)**

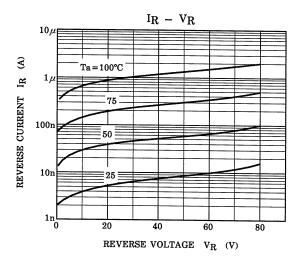
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 1 mA	ı	0.60	-	
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 10 mA	ı	0.72	ı	V
	V <sub>F (3)</sub>	_	I <sub>F</sub> = 100 mA	ı	0.90	1.20	
Reverse current	I <sub>R (1)</sub>	_	V <sub>R</sub> = 30 V	-	_	0.1	μА
	I <sub>R (2)</sub>	_	V <sub>R</sub> = 80 V	ı	ı	0.5	
Total capacitance	СТ	_	V <sub>R</sub> = 0, f = 1 MHz		0.9	3.0	pF
Reverse recovery time	t <sub>rr</sub>	_	I <sub>F</sub> = 10 mA, Fig.1	_	1.6	4.0	ns

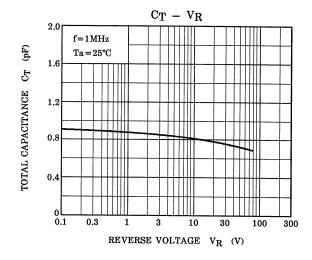
Fig.1 Reverse Recovery Time (trr) Test Circuit

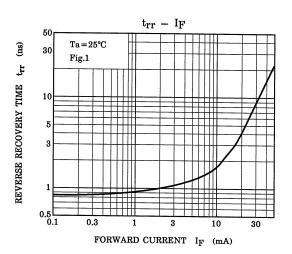
## Marking











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