### TOSHIBA PHOTOCOUPLER PHOTO RELAY

## **TLP199D**

# MEASUREMENT INSTRUMENTS LOGIC IC TESTERS / MEMORY TESTERS BOARD TESTERS / SCANNERS

The TOSHIBA TLP199D consist of a gallium arsenide infrared emitting diode optically coupled to a photo-MOS FET in a plastic SOP package. Its characteristics include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measurement instruments.

### **Features**

• 6 pin SOP (2.54SOP6) : 2.1 mm high, 2.54 mm pitch

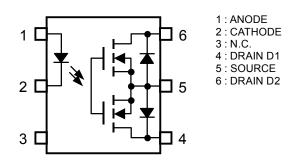
• 1-Form-A

Peak Off-State Voltage : 200 V (min)
 Trigger LED Current : 3 mA (max)
 On-State Current : 50 mA (max)
 On-State Resistance : 50 ohm (max)
 Output Capacitance : 20 pF (max)
 Isolation Voltage : 1500 Vrms (min)

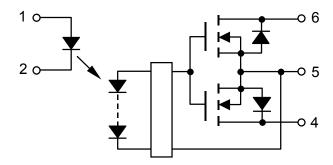
# Unit: mm 6 5 4 6.3±0.25 1 2 3 6.3±0.25 0.4±0.1 JEDEC JEITA TOSHIBA 11–7C1

Weight: 0.13 g (typ.)

### **Pin Configuration (Top View)**



### **Schematic**



2007-10-01

### Absolute Maximum Ratings (Ta = 25°C)

	CHARACTERISTI	SYMBOL	RATING	UNIT		
	Forward Current	lF	50	mA		
Ω	Forward Current Derating (Ta	ΔI <sub>F</sub> /°C	-0.5	mA/°C		
LED	Reverse Voltage		V <sub>R</sub>	5	V	
	Junction Temperature	Tj	125	°C		
	Off-State Output Terminal Vo	$V_{OFF}$	200	V		
	On-State Current	A Connection		50	mA	
DETECTOR		B Connection	I <sub>ON</sub>	50		
		C Connection		100		
	On-State Current Derating	A Connection	Δl <sub>ON</sub> /°C	-0.5	mA/°C	
		B Connection		-0.5		
	(Ta ≧ 25°C)	C Connection		-1.0		
	Junction Temperature		Tj	125	°C	
Storage Temperature Range			T <sub>stg</sub>	-55~125	°C	
Operating Temperature Range			$T_{opr}$	<b>−40~85</b>	°C	
Lead Soldering Temperature (10 s)			T <sub>sol</sub>	260	°C	
Isolat	tion Voltage (AC, 1 minute, R.F	$BV_S$	1500	Vrms		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

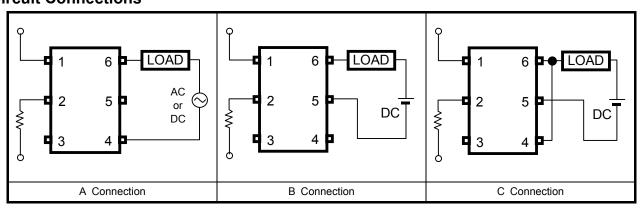
(NOTE1): Device considered a two-terminal device: LED side pins are shorted together, and DETECTOR side pins are shorted together.

### **Recommended Operating Conditions**

CHARACTERISTIC	SYMBOL	Min	Тур.	Max	UNIT
Supply Voltage	$V_{DD}$	_	_	160	V
Forward Current	lF	5	7.5	15	mA
On-State Current	I <sub>ON</sub>	_	_	50	mA
Operating Temperature	T <sub>opr</sub>	-20	_	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

### **Circuit Connections**



### Individual Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
	Forward Voltage	$V_{F}$	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 5 V			10	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz		30		pF
DETECTOR	Off-State Current	l <sub>OFF</sub>	V <sub>OFF</sub> = 160 V	_	-	1	nA
	Capacitance	C <sub>OFF</sub>	V = 0, f = 1 MHz	_	15	20	pF

### **Coupled Electrical Characteristics (Ta = 25°C)**

CHARAC	TERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Trigger LED Curre	nt	I <sub>FT</sub>	I <sub>ON</sub> = 50 mA	_	1	3	mA
Return LED Current		I <sub>FC</sub>	I <sub>OFF</sub> = 100 μA	0.1	_	_	mA
	A Connection		$I_{ON} = 50 \text{ mA}, I_F = 5 \text{ mA}$	_	40	50	
On-State Resistance	B Connection	R <sub>ON</sub>	$I_{ON} = 50 \text{ mA}, I_F = 5 \text{ mA}$	_	30	40	Ω
	C Connection		$I_{ON} = 100 \text{ mA}, I_F = 5 \text{ mA}$	_	15	_	

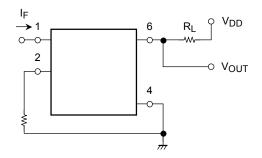
### Isolation Characteristics (Ta = 25°C)

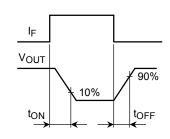
CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Capacitance Input to Output	CS	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≦ 60%	5 × 10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation Voltage	BVS	AC, 1 second (in oil)	_	3000	_	VIIIIS
		DC, 1 minute (in oil)	_	3000	_	Vdc

### **Switching Characteristics (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Turn-on Time		$R_L = 200 \Omega$ (NOTE 2	) —	_	0.5	ms
Turn-off Time	toff	$V_{DD} = 10 \text{ V}, I_F = 5 \text{ mA}$	_		0.2	1115

(NOTE 2): SWITCHING TIME TEST CIRCUIT





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