

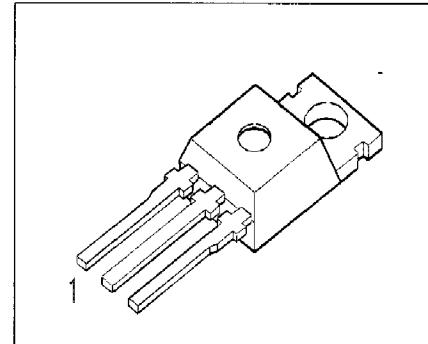
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BUZ 72 L

- N channel
- Enhancement mode
- Avalanche-rated
- Logic Level



Pin 1	Pin 2	Pin 3
G	D	S

Type	V _{DS}	I _D	R _{DS(on)}	Package
BUZ 72 L	100 V	10 A	0.2 Ω	TO-220 AB

Maximum Ratings

Parameter	Symbol	Values	Unit
Continuous drain current $T_C = 25^\circ\text{C}$	I _D	10	A
Pulsed drain current $T_C = 25^\circ\text{C}$	I _{Dpuls}	40	
Avalanche current, limited by $T_{j\max}$	I _{AR}	10	
Avalanche energy, periodic limited by $T_{j\max}$	E _{AR}	7.9	mJ
Avalanche energy, single pulse $I_D = 10 \text{ A}, V_{DD} = 25 \text{ V}, R_{GS} = 25 \Omega$ $L = 885 \mu\text{H}, T_j = 25^\circ\text{C}$	E _{AS}	59	
Gate source voltage	V _{GS}	± 14	V
Gate-source peak voltage, aperiodic	V _{gs}	± 20	
Power dissipation $T_C = 25^\circ\text{C}$	P _{tot}	40	W
Operating temperature	T _j	-55 ... + 150	°C
Storage temperature	T _{stg}	-55 ... + 150	
Thermal resistance, chip case	R _{thJC}	≤ 3.1	K/W
Thermal resistance, chip to ambient	R _{thJA}	75	
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56	



Quality Semi-Conductors

BUZ 72 L

Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Static Characteristics

Drain- source breakdown voltage $V_{GS} = 0 \text{ V}$, $I_D = 0.25 \text{ mA}$, $T_j = 25^\circ\text{C}$	$V_{(\text{BR})\text{DSS}}$	100	-	-	V
Gate threshold voltage $V_{GS}=V_{DS}$, $I_D = 1 \text{ mA}$	$V_{GS(\text{th})}$	1.2	1.6	2	
Zero gate voltage drain current $V_{DS} = 100 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_j = 25^\circ\text{C}$ $V_{DS} = 100 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_j = 125^\circ\text{C}$	I_{DSS}	-	0.1	1	μA
Gate-source leakage current $V_{GS} = 20 \text{ V}$, $V_{DS} = 0 \text{ V}$	I_{GSS}	-	10	100	nA
Drain-Source on-resistance $V_{GS} = 5 \text{ V}$, $I_D = 5 \text{ A}$	$R_{DS(\text{on})}$	-	0.12	0.2	Ω

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Electrical Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

Dynamic Characteristics

Transconductance $V_{DS} \geq 2 * I_D * R_{DS(on)max}$, $I_D = 5 \text{ A}$	g_{fs}	5	7.5	-	S
Input capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{iss}	-	680	900	pF
Output capacitance $V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	C_{oss}	-	180	250	
Storage time $V_{HT} > 1 \text{ V}$, $V_{ET} > 136 \text{ V}$, $I_E > 12 \text{ mA}$	C_{st}		1.1	1.261	
Turn-on polarization time $V_{EE} > 141 \text{ V}$, $V_{HT} > 6 \text{ V}$, $I_E > 14 \text{ mA}$ $R_{HT} > 161 \text{ k}\Omega$	$t_{e(po)}$				ot
Turn-off time $V_{EE} > 141 \text{ V}$, $V_{HT} > 6 \text{ V}$, $I_E > 14 \text{ mA}$ $R_{HT} > 161 \text{ k}\Omega$	t_s		1.96	1.241	
Turn-off polarization time $V_{EE} > 141 \text{ V}$, $V_{HT} > 6 \text{ V}$, $I_E > 14 \text{ mA}$ $R_{HT} > 161 \text{ k}\Omega$	$t_{e(po)}$		1.211	1.241	
Gate turn-off time $V_{EE} > 141 \text{ V}$, $V_{HT} > 6 \text{ V}$, $I_E > 14 \text{ mA}$ $R_{HT} > 161 \text{ k}\Omega$	t_g		1.66	1.81	