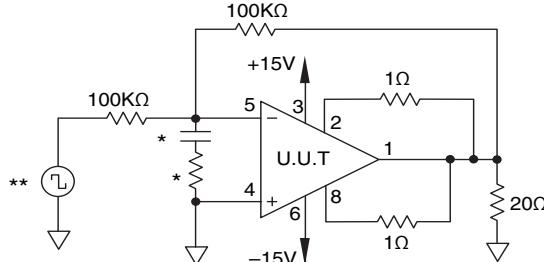


TABLE 4 GROUP A INSPECTION
PA07M/883

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SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1	Quiescent Current	I_Q	25°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	30	mA	
1	Input Offset Voltage	V_{OS}	25°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	2	mV	
1	Input Offset Voltage	V_{OS}	25°C	$\pm 12V$	$V_{IN} = 0, A_v = 100$	4.3	mV	
1	Input Offset Voltage	V_{OS}	25°C	$\pm 50V$	$V_{IN} = 0, A_v = 100$	3.5	mV	
1	Input Bias Current, +IN	$+I_B$	25°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
1	Input Bias Current, -IN	$-I_B$	25°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
1	Input Offset Current	I_{OS}	25°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
3	Quiescent Current	I_Q	-55°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	46	mA	
3	Input Offset Voltage	V_{OS}	-55°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	4.4	mV	
3	Input Offset Voltage	V_{OS}	-55°C	$\pm 12V$	$V_{IN} = 0, A_v = 100$	6.7	mV	
3	Input Offset Voltage	V_{OS}	-55°C	$\pm 50V$	$V_{IN} = 0, A_v = 100$	5.9	mV	
3	Input Bias Current, +IN	$+I_B$	-55°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
3	Input Bias Current, -IN	$-I_B$	-55°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
3	Input Offset Current	I_{OS}	-55°C	$\pm 35V$	$V_{IN} = 0$	50	pA	
2	Quiescent Current	I_Q	125°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	30	mA	
2	Input Offset Voltage	V_{OS}	125°C	$\pm 35V$	$V_{IN} = 0, A_v = 100$	5	mV	
2	Input Offset Voltage	V_{OS}	125°C	$\pm 12V$	$V_{IN} = 0, A_v = 100$	7.3	mV	
2	Input Offset Voltage	V_{OS}	125°C	$\pm 50V$	$V_{IN} = 0, A_v = 100$	6.5	mV	
2	Input Bias Current, +IN	$+I_B$	125°C	$\pm 35V$	$V_{IN} = 0$	10	nA	
2	Input Bias Current, -IN	$-I_B$	125°C	$\pm 35V$	$V_{IN} = 0$	10	nA	
2	Input Offset Current	I_{OS}	125°C	$\pm 35V$	$V_{IN} = 0$	10	nA	
4	Output Voltage, $I_o = 5A$	V_o	25°C	$\pm 15.3V$	$R_L = 2.07\Omega$	10.3	V	
4	Output Voltage, $I_o = 90mA$	V_o	25°C	$\pm 50V$	$R_L = 500\Omega$	45	V	
4	Output Voltage, $I_o = 2A$	V_o	25°C	$\pm 29V$	$R_L = 12\Omega$	24	V	
4	Current Limits	I_{CL}	25°C	$\pm 19V$	$R_L = 12\Omega, R_{CL} = 1\Omega$.54	.86	A
4	Stability/Noise	E_N	25°C	$\pm 35V$	$R_L = 100\Omega, A_v = 1, C_L = 1nF$		1	mV
4	Slew Rate	SR	25°C	$\pm 35V$	$R_L = 500\Omega$	2.5	10	V/ μ s
4	Open Loop Gain	A_{OL}	25°C	$\pm 35V$	$R_L = 500\Omega, F = 15Hz$	89		dB
4	Common Mode Rejection	CMR	25°C	$\pm 34.5V$	$R_L = 500\Omega, F = DC, V_{CM} = \pm 24.5V$	80		dB
6	Output Voltage, $I_o = 5A$	V_o	-55°C	$\pm 15.3V$	$R_L = 2.07\Omega$	10.3	V	
6	Output Voltage, $I_o = 90mA$	V_o	-55°C	$\pm 50V$	$R_L = 500\Omega$	45	V	
6	Output Voltage, $I_o = 2A$	V_o	-55°C	$\pm 29V$	$R_L = 12\Omega$	24	V	
6	Stability/Noise	EN	-55°C	$\pm 35V$	$R_L = 100\Omega, A_v = 1, C_L = 1nF$		1	mV
6	Slew Rate	SR	-55°C	$\pm 35V$	$R_L = 500\Omega$	2.5	10	V/ μ s
6	Open Loop Gain	A_{OL}	-55°C	$\pm 35V$	$R_L = 500\Omega, F = 15Hz$	89		dB
6	Common Mode Rejection	CMR	-55°C	$\pm 34.5V$	$R_L = 500\Omega, F = DC, V_{CM} = \pm 24.5V$	80		dB
5	Output Voltage, $I_o = 3A$	V_o	125°C	$\pm 11.3V$	$R_L = 2.07\Omega$	6.3	V	
5	Output Voltage, $I_o = 90mA$	V_o	125°C	$\pm 50V$	$R_L = 500\Omega$	45	V	
5	Output Voltage, $I_o = 2A$	V_o	125°C	$\pm 29V$	$R_L = 12\Omega$	24	V	
5	Stability/Noise	E_N	125°C	$\pm 35V$	$R_L = 100\Omega, A_v = 1, C_L = 1nF$		1	mV
5	Slew Rate	SR	125°C	$\pm 35V$	$R_L = 500\Omega$	1.25	10	V/ μ s
5	Open Loop Gain	A_{OL}	125°C	$\pm 35V$	$R_L = 500\Omega, F = 15Hz$	89		dB
5	Common Mode Rejection	CMR	125°C	$\pm 34.5V$	$R_L = 500\Omega, F = DC, V_{CM} = \pm 24.5V$	80		dB

BURN IN CIRCUIT


* These components are used to stabilize device due to poor high frequency characteristics of burn in board.

** Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.