# NPN Transistor with Zener Diode

### **Features**

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

## **Typical Applications**

- Driving Circuit
- Switching Applications

## **MAXIMUM RATINGS - NPN TRANSISTOR**

| Rating                         | Symbol           | Value | Unit |
|--------------------------------|------------------|-------|------|
| Collector - Emitter Voltage    | V <sub>CEO</sub> | 40    | V    |
| Collector - Base Voltage       | V <sub>CBO</sub> | 60    | V    |
| Emitter - Base Voltage         | V <sub>EBO</sub> | 6.0   | V    |
| Collector Current - Continuous | I <sub>C</sub>   | 600   | mA   |
| Collector Current - Peak       | I <sub>CM</sub>  | 900   | mA   |

## **MAXIMUM RATINGS - ZENER DIODE**

| Rating                                   | Symbol         | Value | Unit |
|--|----------------|-------|------|
| Forward Voltage @ I <sub>F</sub> = 10 mA | V <sub>F</sub> | 0.9   | V    |

## THERMAL CHARACTERISTICS

| Rating   | Symbol                            | Max         | Unit |
|--|-----------------------------------|-------------|------|
| Total Device Dissipation FR-5 Board,<br>(Note 1) @ T <sub>A</sub> = 25°C | P <sub>D</sub>                    | 380         | mW   |
| Thermal Resistance from Junction–to–Ambient                              | $R_{\theta JA}$                   | 328         | °C/W |
| Junction and Storage<br>Temperature Range                                | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150 | °C   |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1

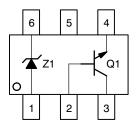
1. FR-4 Minimum Pad.



## ON Semiconductor®

http://onsemi.com

## NPN Transistor with Zener Diode





SC-74 CASE 318F

## **MARKING DIAGRAM**



M60 = Device Code
M = Date Code\*

• Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

| Device      | Package            | Shipping <sup>†</sup> |
|-------------|--------------------|-----------------------|
| NSM6056MT1G | SC-74<br>(Pb-Free) | 3000/Tape & Reel      |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

## NPN TRANSISTOR – ELECTRICAL CHARACTERISTICS ( $T_A = 25$ °C unless otherwise noted)

| Cha                                    | Symbol   | Min                  | Max                         | Unit                    |                    |
|--|--|----------------------|-----------------------------|-------------------------|--------------------|
| OFF CHARACTERISTICS                    |  | •                    | •                           | •                       |                    |
| Collector - Emitter Breakdown Voltage  | V <sub>(BR)CEO</sub>   | 40                   | _                           | Vdc                     |                    |
| Collector - Base Breakdown Voltage     | V <sub>(BR)CBO</sub>   | 60                   | -                           | Vdc                     |                    |
| Emitter - Base Breakdown Voltage       | $(I_E = 0.1 \text{ mAdc}, I_C = 0)$  | V <sub>(BR)EBO</sub> | 6.0                         | -                       | Vdc                |
| Base Cutoff Current                    | (V <sub>CE</sub> = 35 Vdc, V <sub>EB</sub> = 0.4 Vdc)  | I <sub>BEV</sub>     | -                           | 0.1                     | μAdc               |
| Collector Cutoff Current               | (V <sub>CE</sub> = 35 Vdc, V <sub>EB</sub> = 0.4 Vdc)  | I <sub>CEX</sub>     | -                           | 0.1                     | μAdc               |
| ON CHARACTERISTICS (Note 3)            |  |                      | •                           | •                       | ,                  |
| DC Current Gain                        | $ \begin{aligned} &(I_{C}=0.1 \text{ mAdc, V}_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=1.0 \text{ mAdc, V}_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=10 \text{ mAdc, V}_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=150 \text{ mAdc, V}_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=500 \text{ mAdc, V}_{CE}=2.0 \text{ Vdc}) \end{aligned} $ | h <sub>FE</sub>      | 20<br>40<br>80<br>100<br>40 | -<br>-<br>-<br>300<br>- | -                  |
| Collector - Emitter Saturation Voltage | V <sub>CE(sat)</sub>   |                      | 0.4<br>0.75                 | Vdc                     |                    |
| Base - Emitter Saturation Voltage      | $(I_C = 150 \text{ mAdc}, I_B = 15 \text{ mAdc})$<br>$(I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc})$   | V <sub>BE(sat)</sub> | 0.75                        | 0.95<br>1.2             | Vdc                |
| SMALL-SIGNAL CHARACTERISTIC            | es es  | •                    | •                           | •                       | •                  |
| Current - Gain - Bandwidth Product     | $(I_C = 20 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 100 \text{ MHz})$  | f <sub>T</sub>       | 250                         | _                       | MHz                |
| Collector-Base Capacitance             | $(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$   | C <sub>cb</sub>      | -                           | 6.5                     | pF                 |
| Emitter-Base Capacitance               | $(V_{EB} = 0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$   | C <sub>eb</sub>      | _                           | 30                      | pF                 |
| Input Impedance                        | (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)   | h <sub>ie</sub>      | 1.0                         | 15                      | kΩ                 |
| Voltage Feedback Ratio                 | (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 kHz)   | h <sub>re</sub>      | 0.1                         | 8.0                     | X 10 <sup>-4</sup> |
| Small - Signal Current Gain            | $(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$   | h <sub>fe</sub>      | 40                          | 500                     | -                  |
| Output Admittance                      | h <sub>oe</sub>  | 1.0                  | 30                          | μmhos                   |                    |
| SWITCHING CHARACTERISTICS              |  |                      |                             |                         |                    |
| Delay Time                             | t <sub>d</sub>   | -                    | 15                          | -                       |                    |
| Rise Time                              | $(V_{CC} = 30 \text{ Vdc}, V_{EB} = 2.0 \text{ Vdc}, I_{C} = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$  | t <sub>r</sub>       | -                           | 20                      | ns                 |
| Storage Time                           | $(V_{CC} = 30 \text{ Vdc}, I_{C} = 150 \text{ mAdc},$  | t <sub>s</sub>       | -                           | 225                     |                    |
| Fall Time                              | $I_{B1} = I_{B2} = 15 \text{ mAdc}$  | t <sub>f</sub>       | _                           | 30                      | ns                 |

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

## **ZENER DIODE – ELECTRICAL CHARACTERISTICS** ( $V_F$ = 0.9 Max @ $I_F$ = 10 mA for all types)

|             | Test              | Zener Vo | Itage VZ | Z <sub>ZK</sub> I <sub>Z</sub><br>= 0.5 | Z <sub>ZT</sub><br>I <sub>Z</sub> = IZT<br>@ 10% | Ma<br>IR @ |     | d <sub>VZ</sub> /dt<br>@ l <sub>ZT1</sub> | ` ' | C pF Max @                      |
|-------------|-------------------|----------|----------|---|--|------------|-----|---|-----|---------------------------------|
| Device      | Current<br>Izt mA | Min      | Max      | mA Ω<br>Max                             | Mod Ω<br>Max                                     | μА         | V   | Min                                       | Max | V <sub>R</sub> = 0<br>f = 1 MHz |
| NSM6056MT1G | 5.0               | 5.49     | 5.73     | 200                                     | 40   | 1.0        | 2.0 | -2.0                                      | 2.5 | 200                             |

## TYPICAL ELECTRICAL CHARACTERISTICS - NPN TRANSISTOR

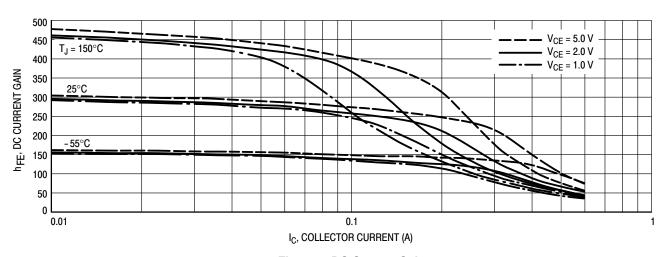


Figure 1. DC Current Gain

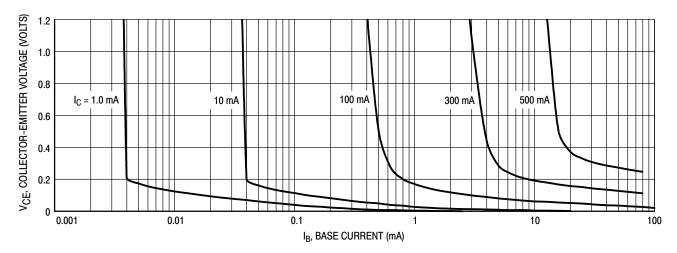


Figure 2. Collector Saturation Region

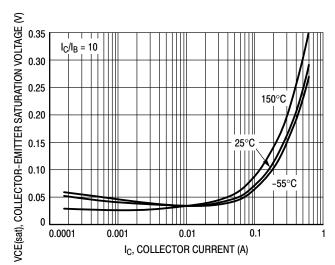


Figure 3. Collector-Emitter Saturation Voltage vs. Collector Current

## TYPICAL ELECTRICAL CHARACTERISTICS - NPN TRANSISTOR

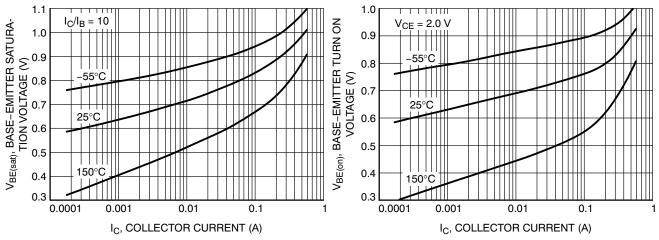


Figure 4. Base-Emitter Saturation Voltage vs.
Collector Current

Figure 5. Base-Emitter Turn On Voltage vs.
Collector Current

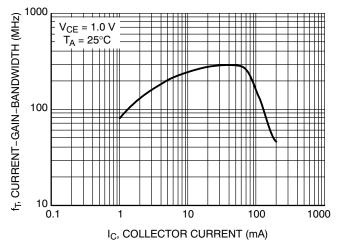


Figure 6. Current-Gain-Bandwidth Product

## TYPICAL ELECTRICAL CHARACTERISTICS - ZENER DIODE

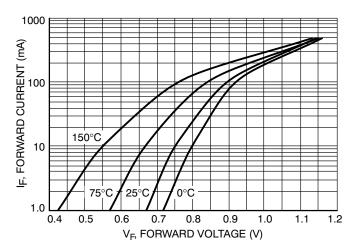
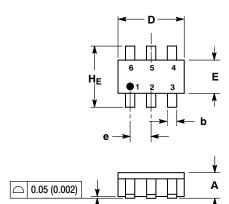
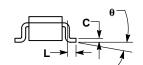


Figure 7. Typical Forward Voltage

## PACKAGE DIMENSIONS

SC-74 CASE 318F-05 **ISSUE M** 



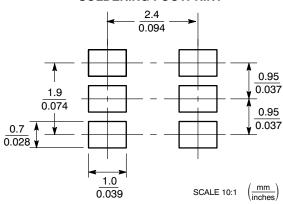


#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 318F-01, -02, -03 OBSOLETE. NEW STANDARD 318F-04.

|     | MILLIMETERS |      |      |       | INCHES |       |
|-----|-------------|------|------|-------|--------|-------|
| DIM | MIN         | NOM  | MAX  | MIN   | NOM    | MAX   |
| Α   | 0.90        | 1.00 | 1.10 | 0.035 | 0.039  | 0.043 |
| A1  | 0.01        | 0.06 | 0.10 | 0.001 | 0.002  | 0.004 |
| b   | 0.25        | 0.37 | 0.50 | 0.010 | 0.015  | 0.020 |
| С   | 0.10        | 0.18 | 0.26 | 0.004 | 0.007  | 0.010 |
| D   | 2.90        | 3.00 | 3.10 | 0.114 | 0.118  | 0.122 |
| E   | 1.30        | 1.50 | 1.70 | 0.051 | 0.059  | 0.067 |
| е   | 0.85        | 0.95 | 1.05 | 0.034 | 0.037  | 0.041 |
| L   | 0.20        | 0.40 | 0.60 | 0.008 | 0.016  | 0.024 |
| HE  | 2.50        | 2.75 | 3.00 | 0.099 | 0.108  | 0.118 |
| θ   | 0°          | _    | 10°  | 0°    | _      | 10°   |

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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