TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

# **TPC8051-H**

## Switching Regulator Applications Motor Drive Applications DC-DC Converter Applications

- · Small footprint due to a small and thin package
- · High-speed switching
- Small gate charge: Q<sub>SW</sub> = 16 nC (typ.)
- Low drain-source ON-resistance:

 $R_{DS(ON)} = 6.3 \text{ m}\Omega \text{ (typ.)}$ 

- High forward transfer admittance: |Y<sub>fs</sub>| = 45 S (typ.)
- Low leakage current:  $I_{DSS} = 10 \,\mu A \,(max) \,(V_{DS} = 80 \,V)$
- Enhancement mode:  $V_{th}$  = 1.3 to 2.3 V ( $V_{DS}$  = 10 V,  $I_D$  = 1.0 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

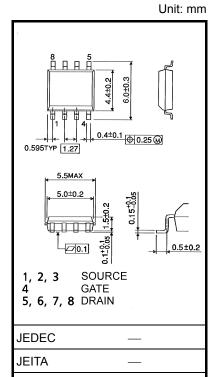
Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	80	V	
Drain-gate voltage (R	$R_{GS} = 20 \text{ k}\Omega$	$V_{DGR}$	80	V	
Gate-source voltage		$V_{GSS}$	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	13	Α	
Drain current	Pulsed (Note 1)	$I_{DP}$	52	^	
Drain power dissipati	on (t = 10 s) (Note 2a)	$P_{D}$	1.9	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	$P_{D}$	1.0	W	
Single-pulse avalance	he energy (Note 3)	E <sub>AS</sub>	110	mJ	
Avalanche current		I <sub>AR</sub>	13	Α	
Repetitive avalanche	energy rc=25°C) (Note 4)	E <sub>AR</sub>	0.06	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

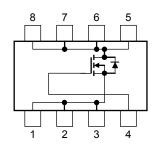


Weight: 0.085g (typ.)

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### **Circuit Configuration**

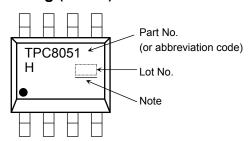
2-6J1B



#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	125	°C/W

### Marking (Note 5)



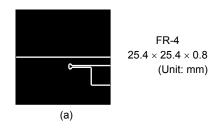
Note: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

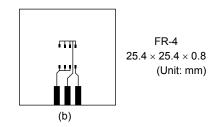
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3:  $V_{DD}=24~V,~T_{ch}=25^{\circ}C$  (initial),  $L=1~mH,~R_{G}=25~\Omega,~I_{AR}=13~A$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)



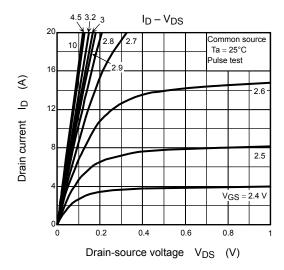
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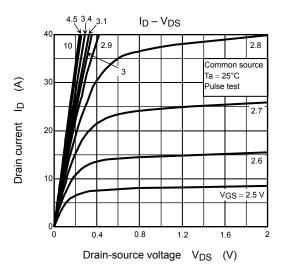
# **Electrical Characteristics (Ta = 25°C)**

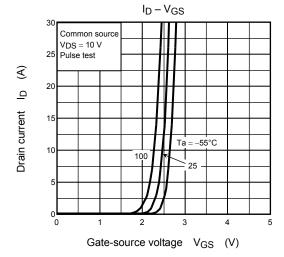
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source breakdown voltage		V <sub>(BR) DSS</sub>	$I_D = 10$ mA, $V_{GS} = 0$ V	80	_	_	V
Dialii-source brea	akuowii voitage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	60	80 — —		v
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 1.0 \text{ mA}$	1.3	_	2.3	٧
Drain-source ON-	rosistanco	Pro (oti)	$V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$	_	6.7	10.1	mΩ
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6.5 A	60 — —  1.3 — 2.3  — 6.7 10.1  — 6.3 9.7  22.5 45 —  — 5800 7540  — 150 210  — 520 —  — 1.0 1.5  — 3.4 —	11152		
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6.5 A	22.5	45	_	S
Input capacitance		C <sub>iss</sub>		_	5800	7540	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	150	210	
Output capacitance		Coss		_	520	_	
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	1.0 1.5		Ω
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> 10 V	_	3.4	_	ns
	Turn-on time	t <sub>on</sub>		_	14	_	
	Fall time	t <sub>f</sub>		_	6.7	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} \approx 40 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \mu\text{s}$	_	67	_	
Total gate charge		0	$V_{DD} \approx 64 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$	_	85	_	
(gate-source plus	gate-drain)	Qg	$V_{DD} \approx 64 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 13 \text{ A}$	_ 43		_	
Gate-source charge 1		Q <sub>gs1</sub>		_	14	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>	$V_{DD} \approx 64 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$		10	_	
Gate switch charg	ge	Q <sub>SW</sub>	1	_	16	_	

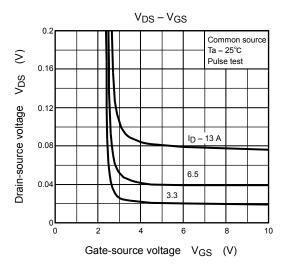
# **Source-Drain Ratings and Characteristics (Ta = 25°C)**

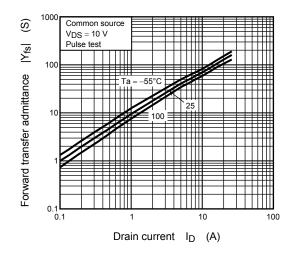
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Peak forward current	Pulse	(Note 1)	I <sub>FP</sub>	_	_	_	52	Α
Forward voltage (diode)			$V_{DSF}$	I <sub>DR</sub> = 13 A, V <sub>GS</sub> = 0 V		_	-1.2	V

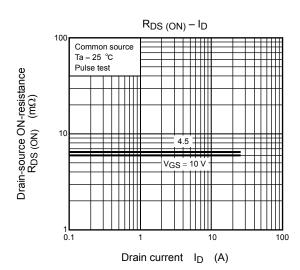


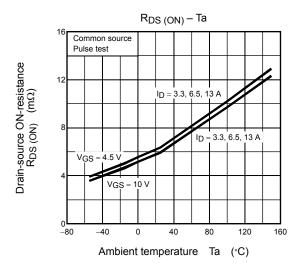


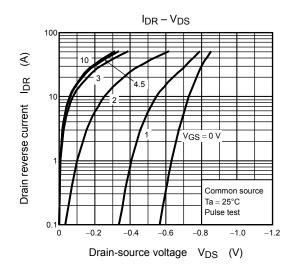


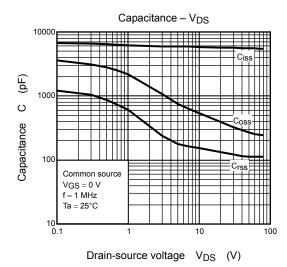


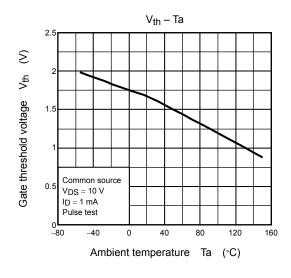


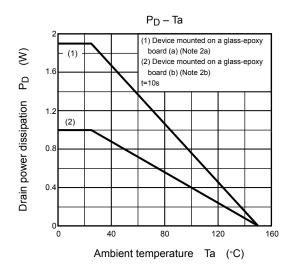


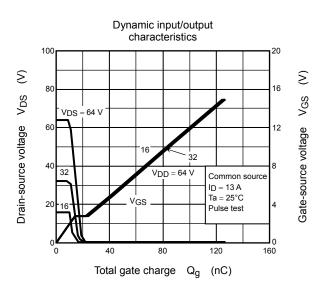




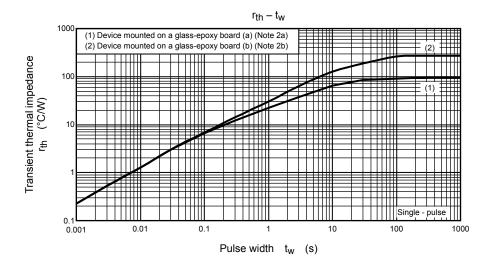


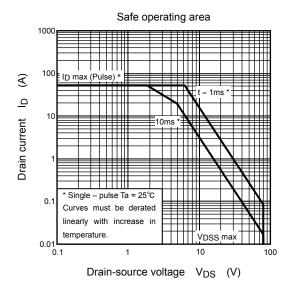






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