TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

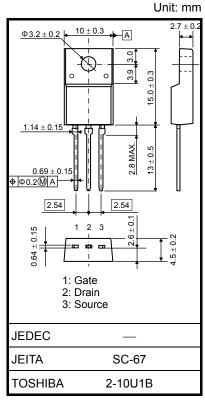
# TK13A55DA

#### Switching Regulator Applications

- Low drain-source ON-resistance:  $RDS(ON) = 0.32 \Omega(typ.)$
- High forward transfer admittance:  $|Y_{fs}| = 6.0 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 550 \ V)$
- Enhancement mode:  $V_{th} = 2.0$  to 4.0 V ( $V_{DS} = 10 \text{ V}$ ,  $I_D = 1 \text{ mA}$ )

<b>.</b> ,								
Characte	ristics	Symbol	Rating	Unit				
Drain-source voltage		V <sub>DSS</sub>	550	V				
Gate-source voltage		V <sub>GSS</sub>	±30	V				
Drain current	DC (Note 1)	۱ <sub>D</sub>	12.5	А				
	Pulse (Note 1)	I <sub>DP</sub>	50	~				
Drain power dissipati	on (Tc = 25°C)	PD	45	W				
Single pulse avalance	he energy (Note 2)	E <sub>AS</sub>	310	mJ				
Avalanche current		I <sub>AR</sub>	12.5	А				
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	4.5	mJ				
Channel temperature	•	T <sub>ch</sub>	150	°C				
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C				

#### Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Thermal Characteristics

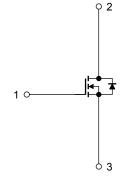
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W	

Note 1:Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}(\text{initial}), \text{ L} = 3.42 \text{ mH}, \text{ R}_{G} = 25 \Omega, \text{ I}_{AR} = 12.5 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-05

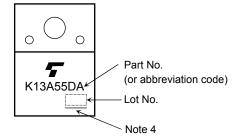
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = 550 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	550			V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$	_	0.32	0.48	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6.3 \text{ A}$	1.5	6.0		S
Input capacitance		C <sub>iss</sub>		_	1800		
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	9		pF
Output capacitance		C <sub>oss</sub>			190		
Switching time	Rise time	tr	$V_{GS} \downarrow I_D = 6.3 \text{ A } V_{OUT} I_D = 6.3 \text{ A } V_{OUT} I_D = 6.3  A$		40	_	- ns
	Turn-on time	t <sub>on</sub>			80	_	
	Fall time	t <sub>f</sub>			15	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, t <sub>w</sub> = 10 µs	_	110	_	
Total gate charge		Qg		_	38	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD}\approx 400$ V, $V_{GS}=10$ V, $I_{D}=12.5$ A		24	_	nC
Gate-drain charge		Q <sub>gd</sub>	]		14	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	12.5	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	50	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 12.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12.5 A, V <sub>GS</sub> = 0 V,	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	14	_	μC

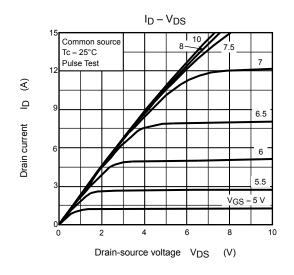
## Marking

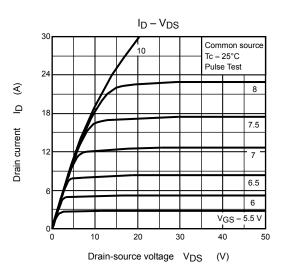


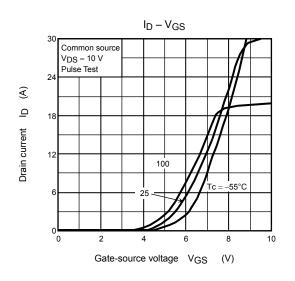
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

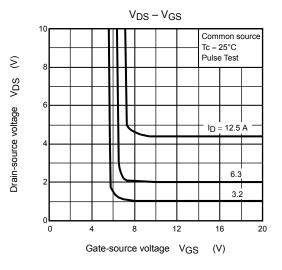
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

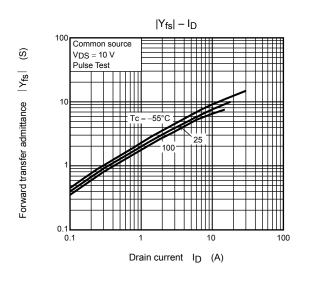
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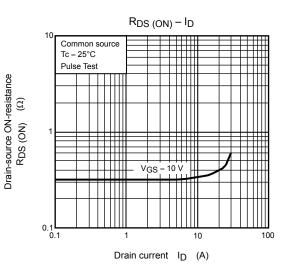




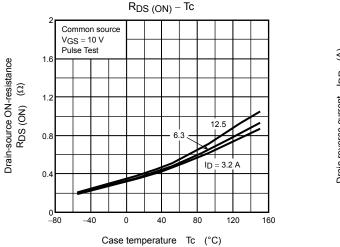


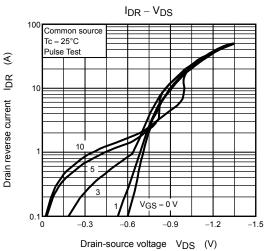


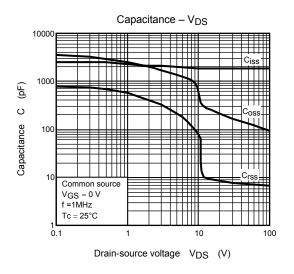


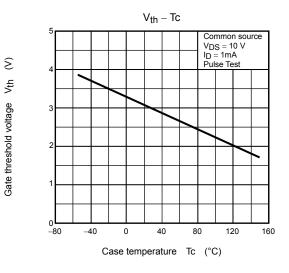


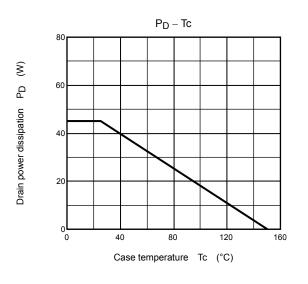
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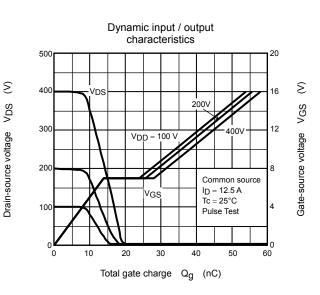


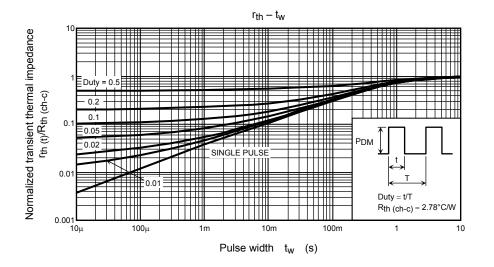




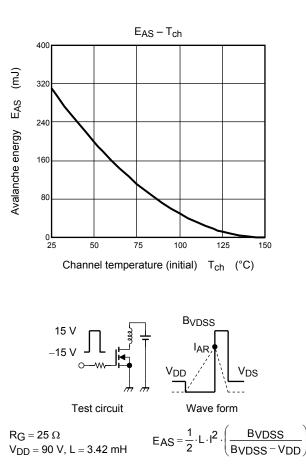








SAFE OPERATING AREA 100 ID max (pulse) \* 100 In max (continuous) 1111 10 1 ms € Drain current ID DC OPERATION  $Tc = 25^{\circ}C$ 1111 0.1 0.01 \*: Single pulse Tc=25°C Curves must be derated linearly with increase in 0.001 temperature. VDSS max 10 100 1000 Drain-source voltage  $V_{DS}$  (V)



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