TOSHIBA Transistor Silicon NPN Epitaxial Type

TPCP8510

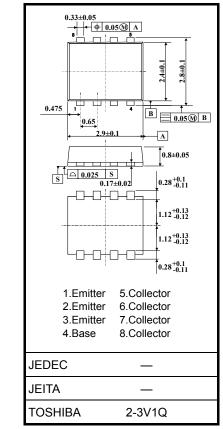
High-Speed, High-Voltage Switching Applications DC-DC Converter Applications

• High DC current gain: h_{FE} = 120 to 300 (I_C = 0.1 A)

Absolute Maximum Ratings (Ta = 25°C)

- Low collector-emitter saturation: V_{CE (sat)} = 0.14 V (max)
- High-speed switching: t_f = 0.2 μs (typ)

Characteristic Symbol Rating Unit 180 V Collector-base voltage VCBO 150 V VCEX Collector-emitter voltage 120 V VCEO 7 V Emitter-base voltage VEBO DC (Note 1) 1.0 Ιc Collector current А Pulse (Note 1) ICP 2.0 Base current 0.1 А I_B 2.25 t = 10s Collector power P_C (Note 2) w dissipation DC 1.1 Junction temperature 150 °C Тj Storage temperature range T_{stg} -55 to 150 °C



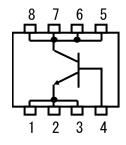
Weight: 0.017 g (typ.)

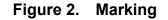
Note 1: Please use devices on condition that the junction temperature is below 150°C.

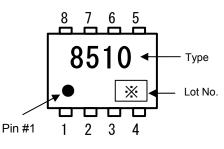
- Note 2: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Figure 1. Circuit configuration (top view)





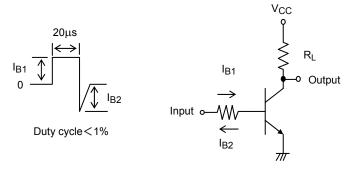


Unit: mm

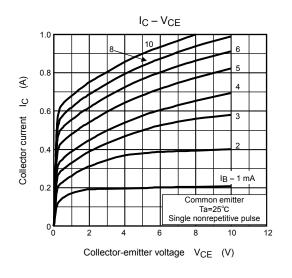
Electrical Characteristics (Ta = 25°C)

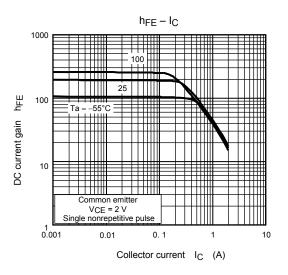
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	V _{CB} = 180 V, I _E = 0	_	_	100	nA
Emitter cut-off current		I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	nA
Collector-base breakdown voltage		V (BR) CBO	I _C = 1 mA, I _B = 0	180	—	_	V
Collector-emitter breakdown voltage		V (BR) CEO	I _C = 10 mA, I _B = 0	120	—	_	V
DC current gain		h _{FE (1)}	V _{CE} = 2 V, I _C = 0.1 A	120	—	300	
		h _{FE (2)}	V _{CE} = 2 V, I _C = 0.3 A	60	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	I _C = 0.3 A, I _B = 0.01 A	_	_	0.14	V
Base-emitter saturation voltage		V _{BE (sat)}	I _C = 0.3 A, I _B = 0.01 A	_	_	1.1	V
Switching time	Rise time	t _r	See Figure 3 circuit diagram	_	0.1	_	
	Storage time	t _{stg}	$V_{CC} \approx 72 \text{ V}, \text{ R}_{L}$ = 240 Ω	_	1.5	_	μs
	Fall time	t _f	I _{B1} = I _{B2} = 10 mA	_	0.2	_	

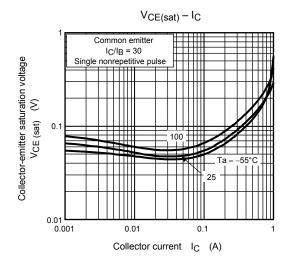
Figure 3. Switching Time Test Circuit

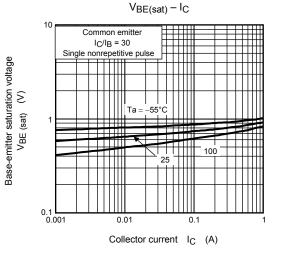


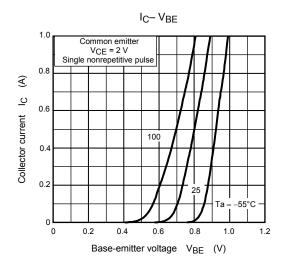
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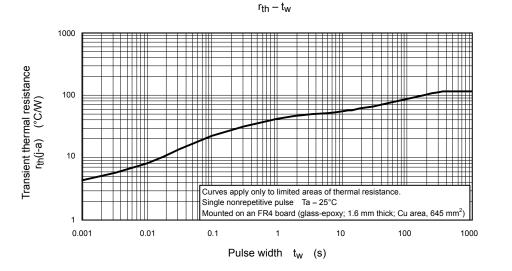












Safe operating area 10 10ms% 1ms% 100µs% IC max (pulse)% . 10μs≫ IC max (continuous) 1 € DC operation . Ta = 25°C <u>ں</u> Collector current 10s፠* 0.1 100ms%* +++0.01 *: Single nonrepetitive pulse Ta = 25°C Note that the curves for 100 ms, 10 s and DC operation will be different when the devices aren't mounted on an FR4 board (glass-epoxy, 1.6 mm thick, Cu area: 645 mm⁵). Single-device operation These characteristic curves must be derated linearly with increase in temperature. VCEO max 0.001 1000 0.1 10 1 100 Collector-emitter voltage VCE (V)

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