TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

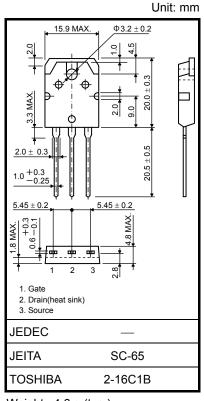
# **TK12J55D**

#### Switching Regulator Applications

- Low drain-source ON-resistance:  $RDS(ON) = 0.48 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 6.0 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 550 \ V)$
- Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

Characteristics			Symbol	Rating	Unit			
Drain-source voltage			V <sub>DSS</sub>	550	V			
Gate-source voltage			V <sub>GSS</sub>	±30	V			
Drain current	DC (No	ote 1)	۱ <sub>D</sub>	12	А			
	Pulse (No	ote 1)	I <sub>DP</sub>	48	A			
Drain power dissipation (Tc = $25^{\circ}$ C)			PD	190	W			
Single pulse avalanche energy (Note 2)			E <sub>AS</sub>	317	mJ			
Avalanche current			I <sub>AR</sub>	12	А			
Repetitive avalanche energy (Note 3)			E <sub>AR</sub>	19	mJ			
Channel temperature			T <sub>ch</sub>	150	°C			
Storage temperature range			T <sub>stg</sub>	-55 to 150	°C			

#### Absolute Maximum Ratings (Ta = 25°C)



Weight : 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

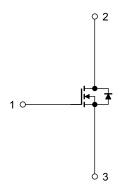
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.658	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$  (initial), L = 3.8 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 12 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Start of commercial production 2009-01

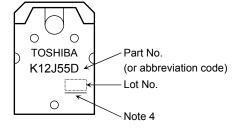
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off current		I <sub>DSS</sub>	$V_{DS} = 550 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	in-source breakdown voltage V		$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	550			V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	I-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	_	0.48	0.57	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	1.5	6.0	_	S
Input capacitance		C <sub>iss</sub>		_	1550	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	7	_	pF
Output capacitance		C <sub>oss</sub>	1	_	165	_	
Switching time	Rise time	tr	$V_{GS}$ $0 V$ $J_{D} = 6 A$ $V_{OUT}$ $C$ $R_{L} = 33 \Omega$ $V_{DD} \approx 200 V$	_	25		- ns
	Turn-on time	t <sub>on</sub>			60		
	Fall time	t <sub>f</sub>		_	15	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> = 10 µs	_	110	_	
Total gate charge		Qg		_	28	—	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 12 \text{ A}$	_	18	—	nC
Gate-drain charge		Q <sub>gd</sub>	1	_	10	—	

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	12	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	48	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V,	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs		13	_	μC

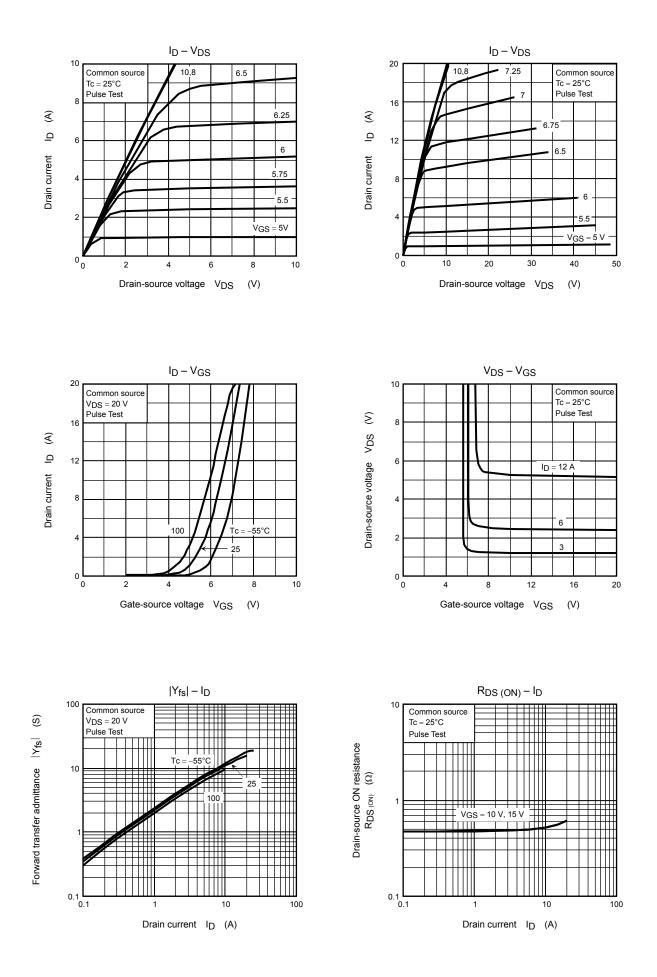
### Marking



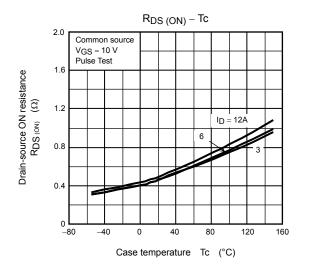
Note 4: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

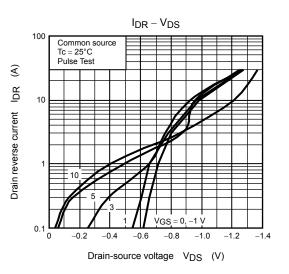
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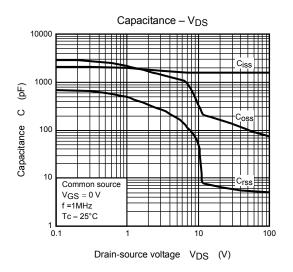
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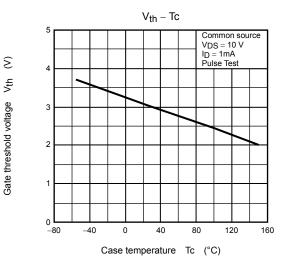


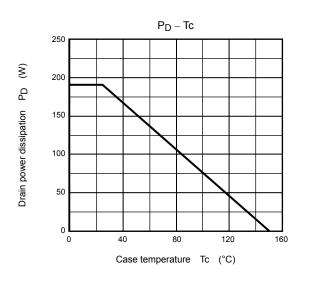
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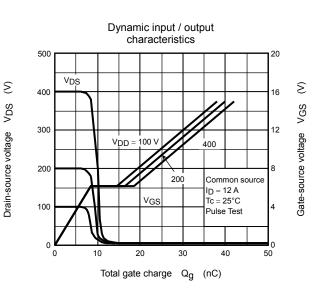


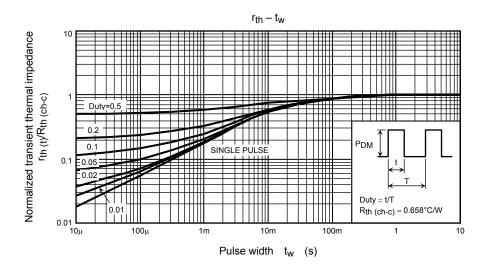


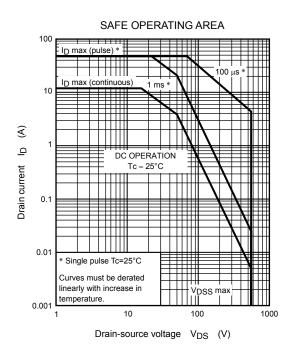


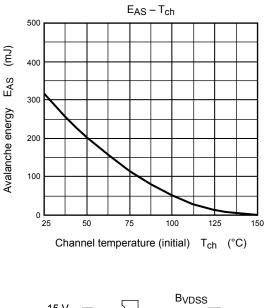


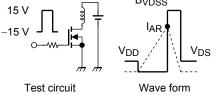












 $\begin{array}{l} \mathsf{R}_{G} = 25 \ \Omega \\ \mathsf{V}_{DD} = 90 \ \mathsf{V}, \ \mathsf{L} = 3.8 \ \mathsf{mH} \end{array} \qquad \mathsf{E}_{AS} = \frac{1}{2} \cdot \mathsf{L} \cdot \mathsf{I}^{2} \cdot \left( \frac{\mathsf{B}_{VDSS}}{\mathsf{B}_{VDSS} - \mathsf{V}_{DD}} \right) \end{array}$ 

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