

Aerospace 40 A - 200 V fast recovery rectifier

Features

- Very small conduction losses
- Negligible switching losses
- High surge current capability
- High avalanche energy capability
- Hermetic package
- Target radiation qualification:
 - 150 krad (Si) low dose rate
 - 1 Mrad high dose rate
- Package mass: 10 g
- ESCC qualified

Description

Packaged in a hermetic TO-254AA, this device is intended for use in medium voltage, high frequency switching mode power supplies, high frequency DC to DC converters, and other aerospace applications.

The complete ESCC specification for this device is available from the European space agency web site. ST guarantees full compliance of qualified parts with such ESCC detailed specifications.

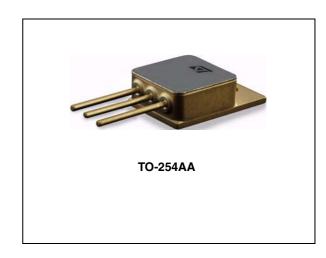


Figure 1. Device configuration

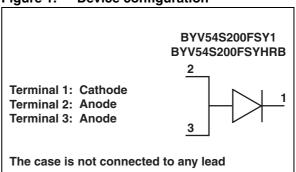


Table 1. Device summary⁽¹⁾

Order code	ESCC detailed specification	Quality level	Lead finish	EPPL	I _{F(AV)}	V _{RRM}	T _{j(max)}	V _{F(max)}
BYV54S200FSY1	-	Engineering model	Gold	-	40 A	200 V	150 °C	1.10 V
BYV54S200FSYHRB	5103/031/05	Flight part	Solder dip	Y	40 /	200 V	130 0	1.10 V

^{1.} Contact ST sales office for information about the specific conditions for products in die form and QML-Q versions.

Characteristics BYV54HR

1 Characteristics

Table 2. Absolute maximum ratings

Symbol	Characteristic	Value	Unit
I _{FSM}	Forward surge current ⁽¹⁾	400	Α
V_{RRM}	Repetitive peak reverse voltage ⁽²⁾	200	V
Io	Average output rectified current (50% duty cycle):(3)	40	Α
I _{F(RMS)}	Forward rms current	60	Α
T _{OP}	Operating case temperature range ⁽⁴⁾	-55 to +150	°C
T _J	Junction temperature	+150	°C
T _{STG}	Storage temperature range ⁽⁴⁾	-55 to +150	°C
T _{SOL}	Soldering temperature ⁽⁵⁾	+260	°C

- 1. Sinusoidal pulse of 10 ms duration
- 2. Pulsed, duration 5 ms, F = 50 Hz
- 3. For $T_{case} > +99$ °C, derate linearly to 0 A at +150°C.
- 4. For devices with hot solder dip lead finish all testing performed at $T_{amb} > +125$ °C are carried out in a 100% inert atmosphere.
- 5. Duration 10 seconds maximum at a distance of not less than 1.5 mm from the device body and the same lead shall not be resoldered until 3 minutes have elapsed.

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th (j-c)}	Junction to case (1)	1.0	°C/W

1. Package mounted on infinite heatsink.

BYV54HR Characteristics

Table 4. Electrical measurements at ambiant temperature (per diode), $T_{amb} = 22 \pm 3$ °C

Cymhal	Characteristic	MIL-STD-750	Test conditions ⁽¹⁾	Limits		Units
Symbol	Characteristic	test method	rest conditions.	Min.	Max.	Oints
I _R	Reverse current	4016	DC method, V _R = 200 V	-	50	μΑ
V _{F1} ⁽²⁾	Forward voltage	4011	Pulse method, I _F = 20 A	-	0.95	V
V _{F2} ⁽²⁾	Forward vollage	4011	Pulse method, I _F = 30 A	-	1.1	V
V_{BR}	Breakdown voltage	4021	I _R = 100 μA	200	-	٧
С	Capacitance	4001	V _R = 10 V, F = 1 MHz	-	400	pF
t _{rr}	Reverse recovery time	4031	$I_F = 1 \text{ A}, V_R = 30 \text{ V},$ $dI_F/dt = -50 \text{ A}/\mu\text{s}$	-	60	ns
$Z_{\text{th(j-c)}}^{(3)}$	Relative thermal impedance, junction to case	3101	$I_H = 15 \text{ to } 40 \text{ A}, t_H = 50 \text{ ms}$ $I_M = 50 \text{ mA}, t_{md} = 100 \mu\text{s}$	Calculat	te ΔV _F ⁽⁴⁾	°C/W

- 1. Testing performed with both anode terminals 2 and 3 tied ttogehter
- 2. Pulse width \leq 680 μ s, duty cycle \leq 2%
- 3. Performed only during screening tests parameter drift values (initial measurements for HTRB), go-no-go.
- The limits for ΔVF shall be defined by the manufacturer on every lot in accordance with MIL-STD-750 Method 3101 and shall guarantee the R_{th(j-c)} limits specified in maximum ratings.

Table 5. Electrical measurements at high and low temperatures (per diode)

Symbol	Characteristic	MIL-STD-750 test method	Test conditions ⁽¹⁾	Limits		Units
Symbol	Cilaracteristic		rest conditions.	Min.	Max.	Oints
I _R	Reverse current	4016	$T_{case} = +125 (+0, -5) ^{\circ}C$ DC method, $V_{R} = 200 V$	-	40	mA
V _{F1} ⁽²⁾ For	Forward voltage		T _{case} = +125 (+0, -5) °C pulse method, I _F = 20 A	-	0.85	V
		4011	$T_{case} = -55 (+0, -5) ^{\circ}C$ pulse method, $I_F = 20 A$	-	1.15	V
			T_{case} = +125 (+0, -5) °C pulse method, I _F = 30 A		1.0	

Read and record measurements shall be performed on a sample of 5 components with 0 failures allowed. Alternatively a 100% inspection may be performed.

To evaluate the conduction losses use the following equation:

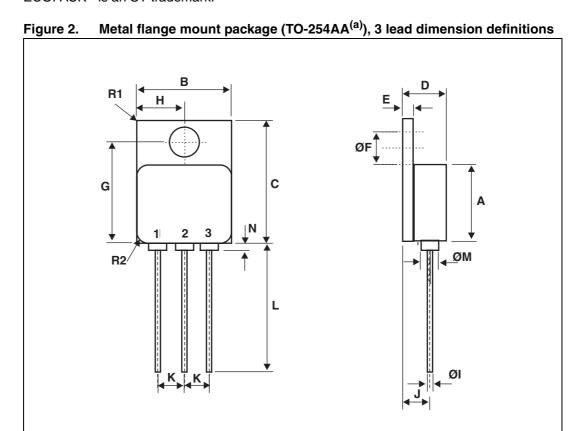
$$P = 0.74 \times I_{F(AV)} + 1.00 \times I_{F}^{2}(RMS)$$

^{2.} Pulse width \leq 680 μ s, duty cycle \leq 2%

Package information BYV54HR

2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.



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a. The terminal identification is specified by the device configuration. See *Figure 1* for terminal connections

Table 6. Metal flange mount package (TO-254), 3-lead dimension values

Deference	Dimension in	n millimetres	Dimlension	n in inches	
Reference	Min.	Max.	Min.	Max.	
А	13.59	13.84	0.535	0.545	
В	13.59	13.84	0.535	0.545	
С	20.07	20.32	0.790	0.800	
D	6.3	6.7	0.248	0.264	
E	1	3.9	0.039	0.154	
ØF	3.5	3.9	0.138	0.154	
G	16.89	17.4	0.665	0.685	
Н	6.86	BSC	0.270 BSC		
ØI ⁽¹⁾	0.89	1.14	0.035	0.045	
J	3.81	BSC	0.150 BSC		
K	3.81	BSC	0.150 BSC		
L	12.95	14.5	0.510	0.571	
ØM	3.05 Typ.		0.120	Тур.	
N	-	0.71	-	0.028	
R1 ⁽²⁾	-	1	-	0.039	
R2 ⁽³⁾	1.65 Typ.		0.0	065	

^{1. 3} locations

^{2.} Radius of heatsink flange corner - 4 locations

^{3.} Radius of body corner - 4 locations

Ordering information BYV54HR

3 Ordering information

Table 7. Ordering information⁽¹⁾

Order code	ESCC detailed specification	Package	Lead finish	Comment	Marking	Mass	EPPL	Packing
BYV54S200FSY1	-	TO-254AA	Gold	Cinalo dio	BYV54S200FSY1 + BeO	10.0	-	Strip
BYV54S200FSYHRB	5103/031/05	10-254AA	Solder dip	Single die	510303105 + BeO	10 g	Υ	pack

^{1.} Contact ST sales office for information about the specific conditions for products in die form and QML-Q versions.

BYV54HR Revision history

4 Revision history

Table 8. Document revision history

Date	Revision	Changes
08-Jul-2010	1	First issue.

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