

Hi-Rel PNP bipolar transistor 80 V - 5 A

Datasheet - production data

Features

BV _{CEO}	80 V
I _C (max)	5 A
H _{FE} at 10 V - 150 mA	> 70
Operating temperature range	-65°C to +200°C

- Hi-Rel PNP bipolar transistor
- Linear gain characteristics
- ESCC qualified
- European preferred part list EPPL
- Radiation level: lot specific total dose contact marketing for specified level

Description

The 2N5153HR is a silicon planar epitaxial PNP transistor in TO-39, TO-257 and SMD.5 packages. It is specifically designed for aerospace Hi-Rel applications and ESCC qualified according to the 5204-002 specification. In case of conflict between this datasheet and ESCC detailed specification, the latter prevails.

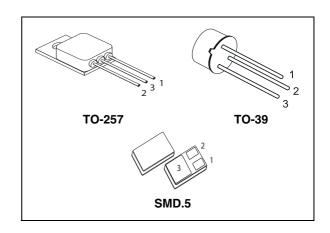


Figure 1. Internal schematic diagram

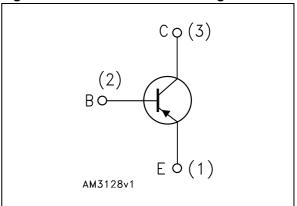


Table 1. Device summary

Order codes	Packages	Lead finish	Marking	Туре	EPPL	Packaging
2N5153HR	TO-39	Gold Solder Dip	520400201 520400202	ESCC Flight		Strip pack
2N5153SHR	SMD.5	Gold	520400206	ESCC Flight	Yes	Strip pack
2N5153ESYHR	TO-257	Gold Solder Dip	520400204 520400205	ESCC Flight		Strip pack
2N5153T1	TO-39	Gold	2N5153T1	Engineering model		Strip pack
2N5153S1	SMD.5	Gold	2N5153S1	Engineering model		Strip pack
2N5153ESY	TO-257	Gold	2N5153ESY	Engineering model		Strip pack

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Electrical ratings 2N5153HR

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-100	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-80	V
V _{EBO}	Emitter-base voltage (I _C = 0)	-5.5	V
I _C	Collector current	-5	Α
P _{TOT}	Total dissipation at $T_{amb} \le 25~^{\circ}C$ for 2N5153HR for 2N5153ESYHRB for Root part number 2 $T_C \le 25~^{\circ}C$ for 2N5153HR for 2N5153HR for 2N5153ESYHRB for Root part number 2	1 3.3 3.3 10 35 35	W W W
T _{STG}	Storage temperature	-65 to 200	°C
TJ	Max. operating junction temperature	200	°C

Table 3. Thermal data for through-hole packages

Symbol	mbol Parameter		TO-39	TO-257	Unit
R _{thJC}	Thermal resistance junction-case	max	17.5	5	°C/W
R_{thJA}	Thermal resistance junction-ambient	max	175	53	°C/W

Table 4. Thermal data for SMD package

Symbol	Parameter	SMD.5	Unit
R_{thJC}	Thermal resistance junction-case max	5	°C/W

2 Electrical characteristics

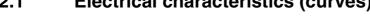
 T_{case} = 25 °C unless otherwise specified

Table 5. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (I _E = 0)	V _{CB} = - 60 V V _{CB} = - 60 V T _{amb} = 150 °	С		-1 -10	μ Α μ Α
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = - 4 V V _{EB} = - 5.5 V			-1 -1	μA mA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = - 40 V			-50	μΑ
V _{(BR)CEO} (1)	Collector-emitter breakdown voltage (I _B = 0)	I _C = - 100 mA	-80			V
V _{CE(sat)} (1)	Collector-emitter saturation voltage	I _C = - 5 A I _B = - 0.5 A			-1.5	V
V _{BE(sat)} (1)	Base-emitter saturation voltage	$I_C = -2.5 \text{ A}$ $I_B = -0.25 \text{ A}$ $I_C = -5 \text{ A}$ $I_B = -0.5 \text{ A}$			-1.45 -2.2	V V
h _{FE} ⁽¹⁾	DC current gain	$\begin{split} & _{C} = -50 \text{ mA} & V_{CE} = -5 \text{ V} \\ & _{C} = -2.5 \text{ A} & V_{CE} = -5 \text{ V} \\ & _{C} = -5 \text{ A} & V_{CE} = -5 \text{ V} \\ & _{C} = -2.5 \text{ A} & V_{CE} = -5 \text{ V} \\ & _{C} = -5 \text{ C} & V_{CE} = -5 \text{ V} \\ \end{split}$	70 40		200	
h _{fe}	AC forward current transfer ratio	V _{CE} = - 5 V I _C = - 500 m f = 20 MHz	A 3.5			
C _{OBO}	Output capacitance	$I_E = 0$ $V_{CB} = -10$ $f = 1 \text{ MHz}$	V		250	pF
t _{on}	Turn-on time	$V_{CC} = -30 \text{ V}$ $V_{BB} = -4 \text{ V}$ $V_{in} \cong -51 \text{ V}$ $I_C = 5 \text{ A}$ $I_{B1} = -I_{B2} = -0.5 \text{ A}$,		0.5	μs
t _{off}	Turn-off time	$V_{CC} = -30 \text{ V}$ $V_{BB} = -4 \text{ V}$ $V_{in} \cong -51 \text{ V}$ $I_{C} = -5 \text{ A}$ $I_{B1} = -I_{B2} = -0.5 \text{ A}$,		1.3	μs

^{1.} Pulsed duration = 300 μ s, duty cycle $\leq 1.5\%$

Electrical characteristics (curves) 2.1



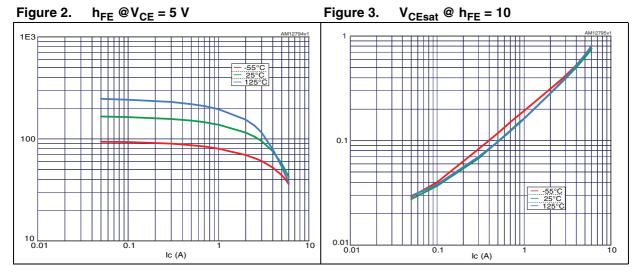
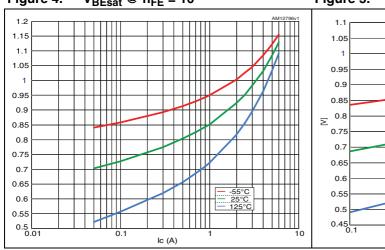
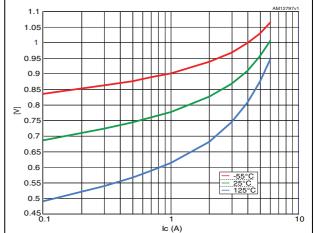


Figure 4. **V**_{BEsat} @ h_{FE} = 10

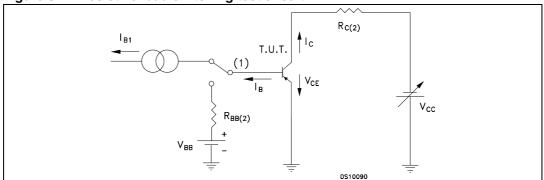


 $V_{BEON} @ V_{CE} = 5 V$ Figure 5.



Test circuit 2.2

Figure 6. Resistive load switching test circuit



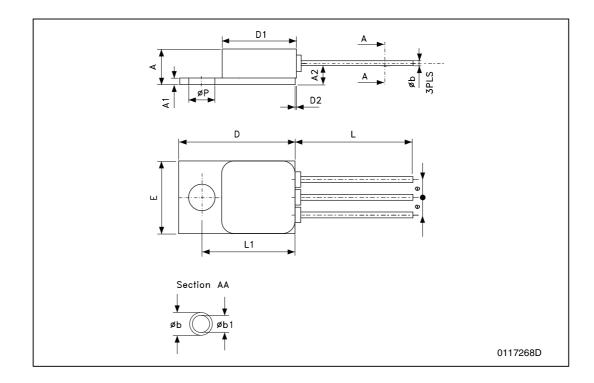
- Fast electronic switch
- 2. Non-inductive resistor

3 Package mechanical data

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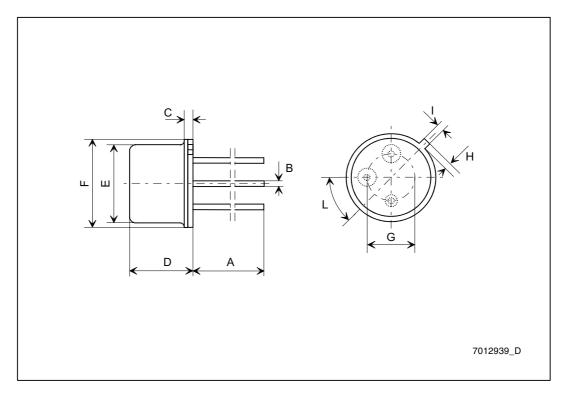
	_		
TO-257	macha	nical	data

DIM		mm.	
DIM.	Min.	Тур.	Max.
А	4.83		5.08
A1	0.89		1.14
A2		3.05	
b	0.64		1.02
b1	0.64	0.76	0.89
D	16.38		16.89
D1	10.41		10.92
D2			0.97
е		2.54	
E	10.41		10.67
L	12.70		19.05
L1	13.39		13.64
Р	3.56		3.81



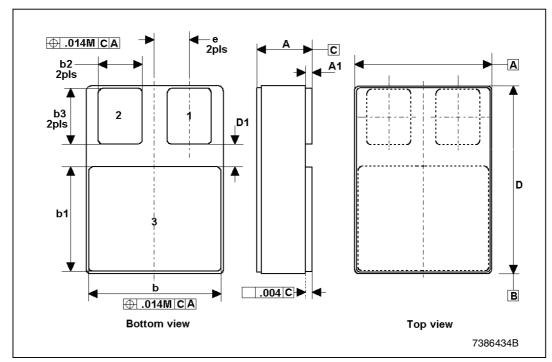
TO-39 mechanical data

DIM.		mm.	
DIW.	Min.	Тур.	Max.
А	12.70		
В			0.49
D			6.60
E			8.51
F			9.40
G		5.08	
Н			1.02
ı			0.86
L		45°	



SMD.	5 ma	cha	nical	l data
SIVID.	o ille	CHa	nica	ı uata

Dim.	mm.			
Dilli.	Min.	Тур.	Max.	
А	2.84	3.00	3.15	
A1	0.25	0.38	0.51	
b	7.13	7.26	7.39	
b1	5.58	5.72	5.84	
b2	2.28	2.41	2.54	
b3	2.92	3.05	3.18	
D	10.03	10.16	10.28	
D1		0.76		
E	7.39	7.52	7.64	
е		1.91		



2N5153HR Revision history

4 Revision history

Table 6. Document revision history

Date	Revision	Changes	
10-Dec-2008	1	Initial release	
08-Jan-2010	08-Jan-2010 2 Modified <i>Table 1 on page 1</i>		
12-Sep-2012	3	Added: Section 2.1: Electrical characteristics (curves) on page 4	

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