N -Channel Logic Level Enhancement Mode Power MOSFET

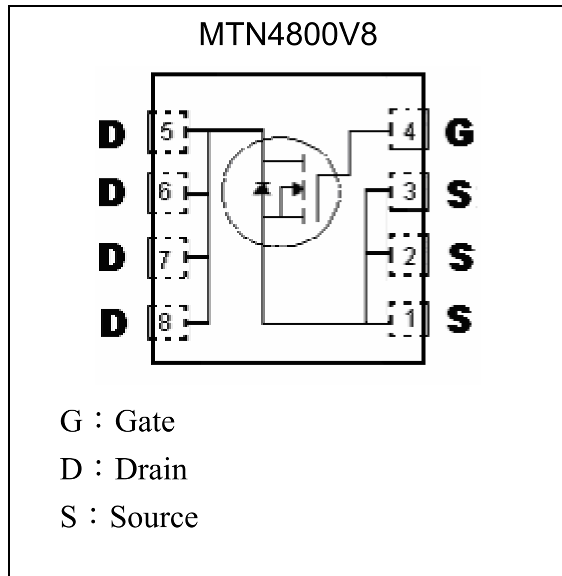
MTN4800V8

Parameter	Condition	Value
BV_{DSS}		30V
I_D		18A
$R_{DSON(TYP)}$	V_{GS}=10V, I_D=10A	14mΩ
	V_{GS}=4.5V, I_D=8A	22mΩ

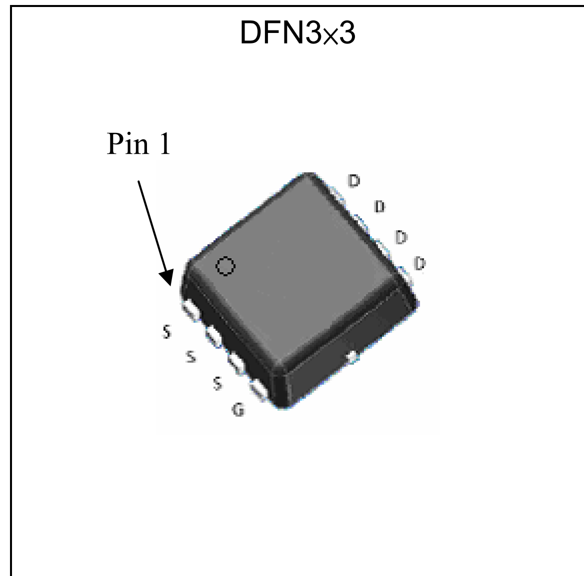
Features

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating package

Equivalent Circuit

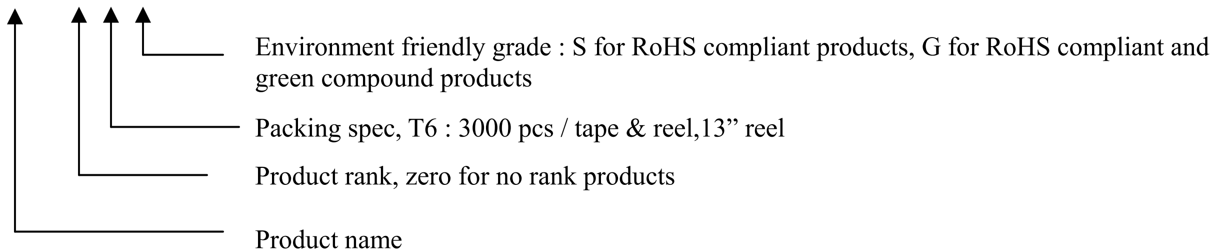


Outline



Ordering Information

Device	Package	Shipping
MTN4800V8-0-T6-G	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel



Absolute Maximum Ratings (T_C=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	
Continuous Drain Current @ T_C=25°C	I_D	18	A
Continuous Drain Current @ T_C=100°C	I_D	11.4	
Pulsed Drain Current *1	I_{DM}	72	
Total Power Dissipation @T_C=25℃	P_D	8	W
Total Power Dissipation @T_A=25℃		2.5	
Operating Junction and Storage Temperature Range	Tj, Tstg	-55~+150	°C

Note : *1. Pulse width limited by maximum junction temperature
*2. Duty cycle ≤ 1%

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	16	°C/W
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	50 *	°C/W

* Surface mounted on a 1 in² pad of 2oz copper.

Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	30	-	-	V	V_{GS}=0V, I_D=250μA
$\Delta BV_{DSS}/\Delta Tj$	-	0.02	-	V/℃	Reference to 25℃, I_D=1mA
$V_{GS(th)}$	1	1.7	2.5	V	V_{DS}=V_{GS}, I_D=250μA
I_{GSS}	-	-	±100	nA	V_{GS}=±20V, V_{DS}=0V
I_{DSS}	-	-	1	μA	V_{DS}=30V, V_{GS}=0V, Tj=25℃
	-	-	25	μA	V_{DS}=30V, V_{GS}=0V, Tj=125℃
*$R_{DS(ON)}$	-	14	20	mΩ	I_D=10A, V_{GS}=10V
	-	22	28		I_D=8A, V_{GS}=4.5V
*G_{FS}	-	14	-	S	V_{DS}=5V, I_D=10A
Dynamic					
Ciss	-	697	-	pF	V_{DS}=25V, V_{GS}=0, f=1MHz
Coss	-	61	-		
Crss	-	54	-		
$t_{d(ON)}$	-	10	-	ns	V_{DS}=15V, I_D=1A, V_{GS}=10V, R_G=6Ω, R_D=15Ω
t_r	-	5	-	ns	
$t_{d(OFF)}$	-	18	-	ns	
t_f	-	14	-	ns	
Qg	-	9	-	nC	V_{DS}=15V, I_D=10A, V_{GS}=10V,
Qgs	-	2.3	-	nC	
Qgd	-	3.4	-	nC	

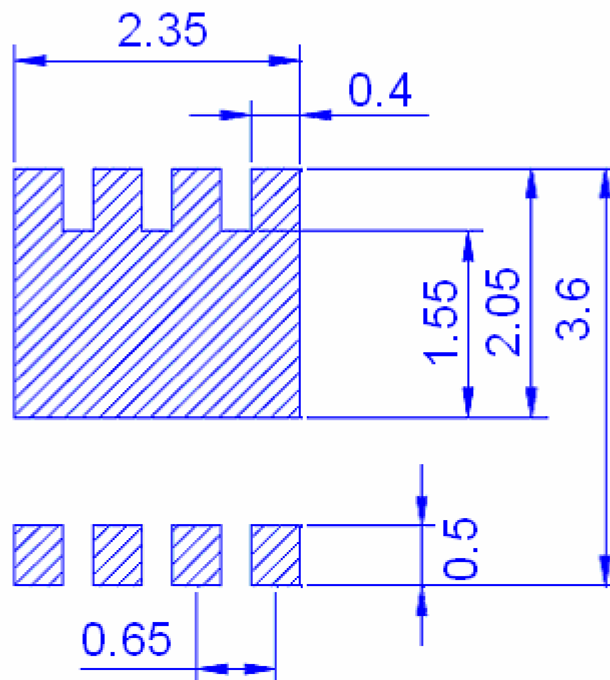
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Source Drain Diode

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
$*I_S$	-	-	4	A	
$*I_{SM}$	-	-	16		
$*V_{SD}$	-	0.8	1.3	V	I_S=4A,V_{GS}=0V
$*T_{rr}$	-	9	-	ns	I_S=4A,V_{GS}=0V, dI/dt=100A/µs
Q_{rr}	-	20	-	nC	

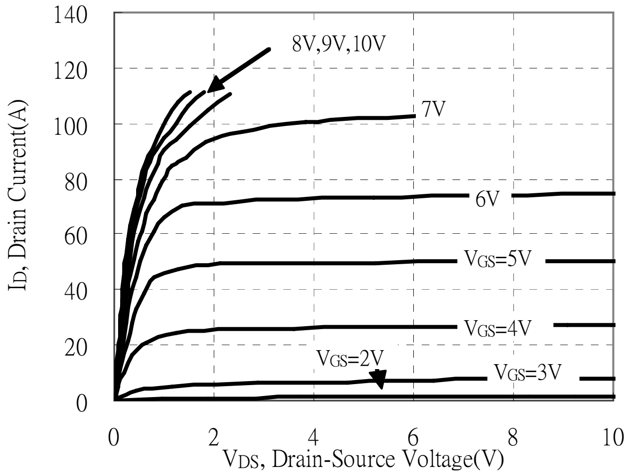
*Pulse Test : Pulse Width ≤300µs, Duty Cycle≤2%

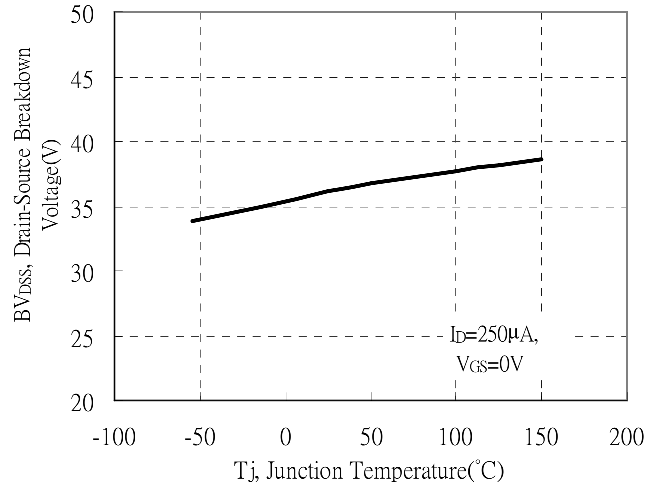
Recommended Soldering Footprint

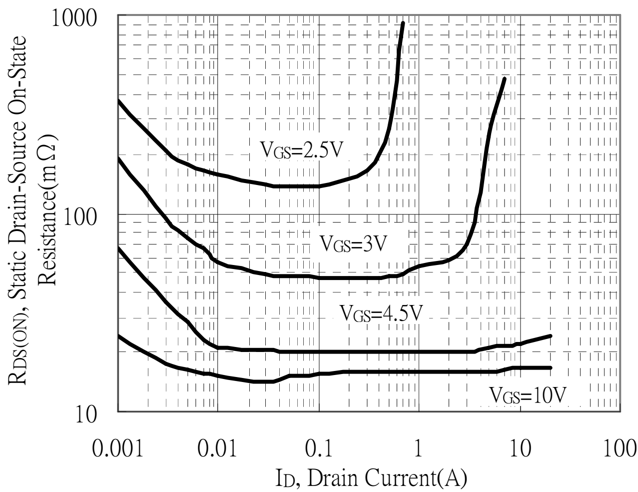


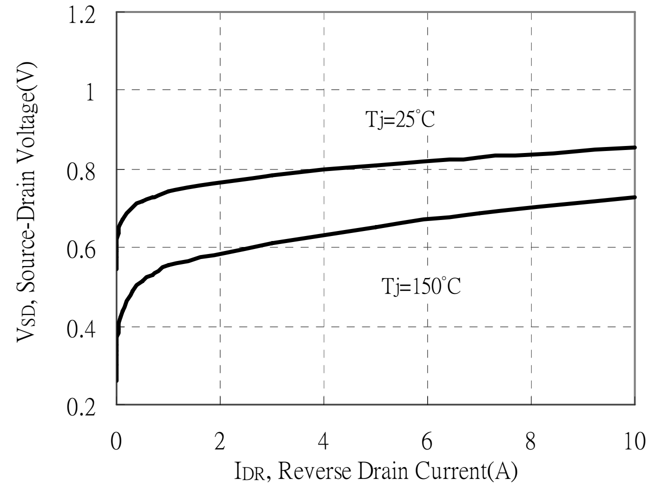
unit : mm

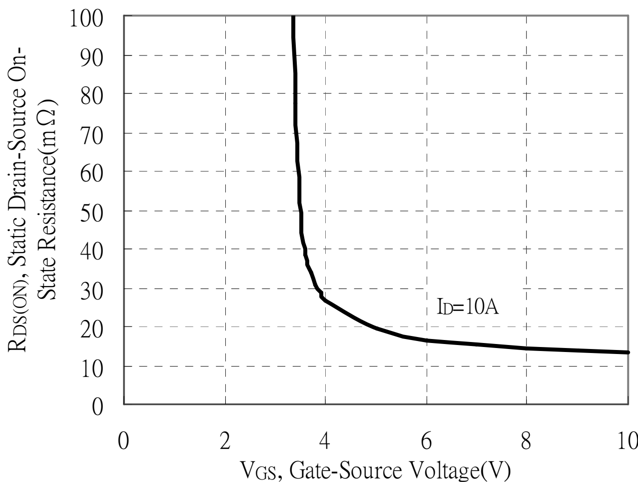
Typical Characteristics

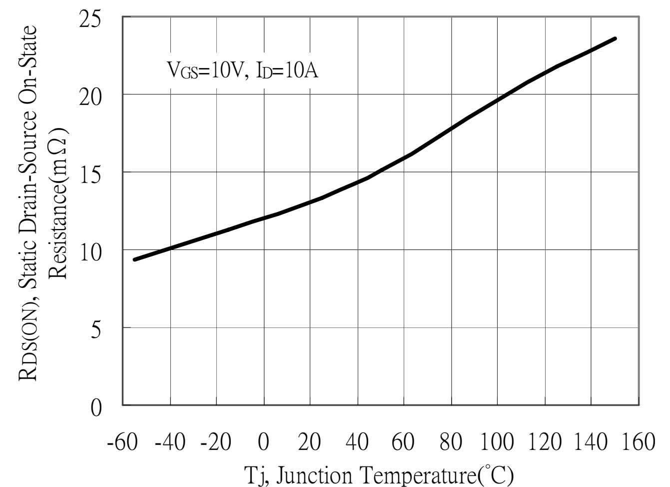




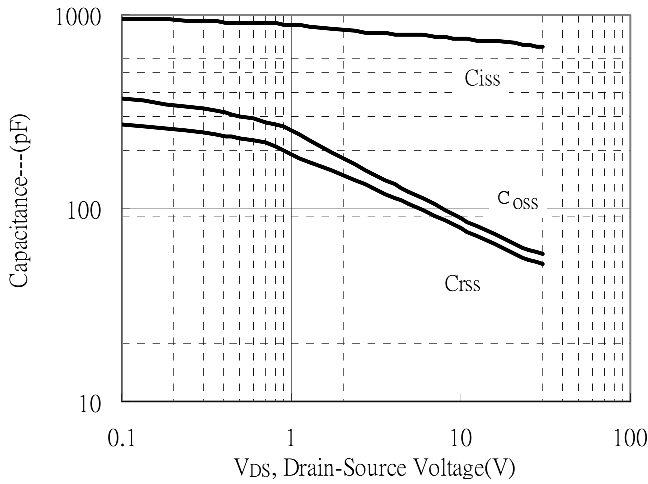


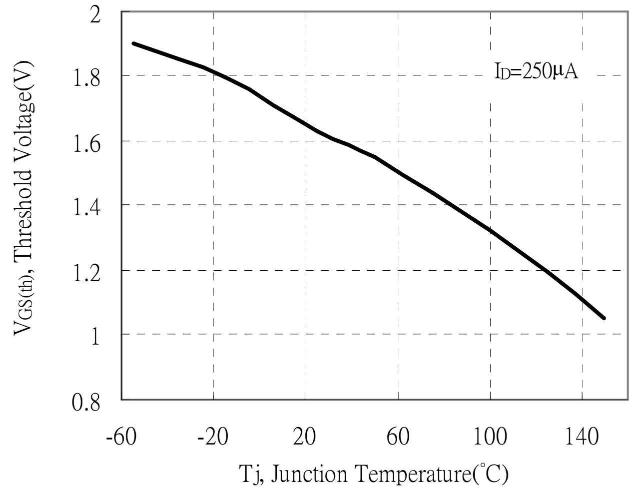


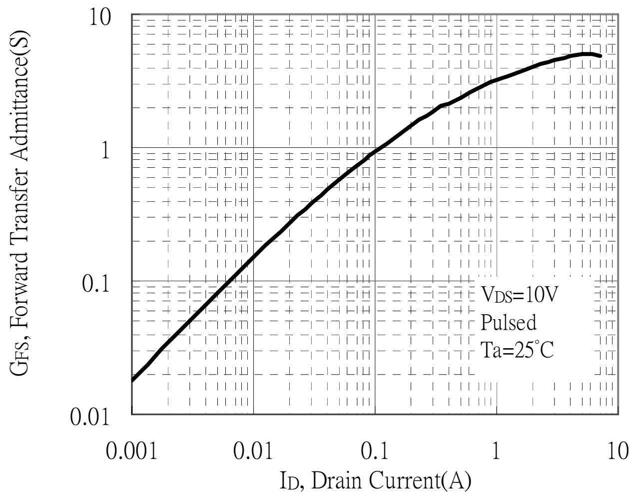


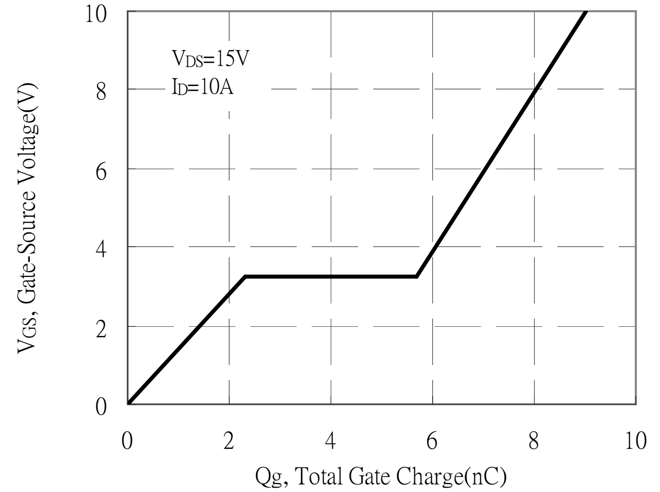


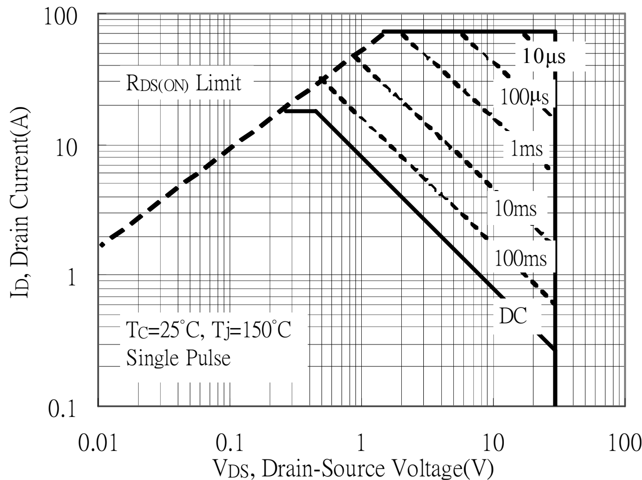
Typical Characteristics(Cont.)

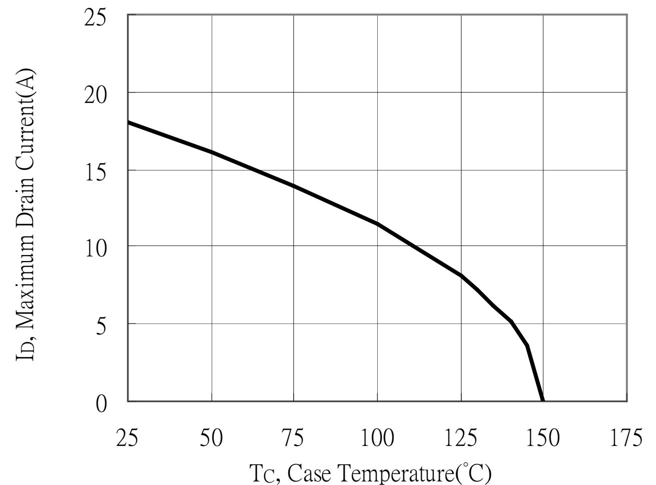




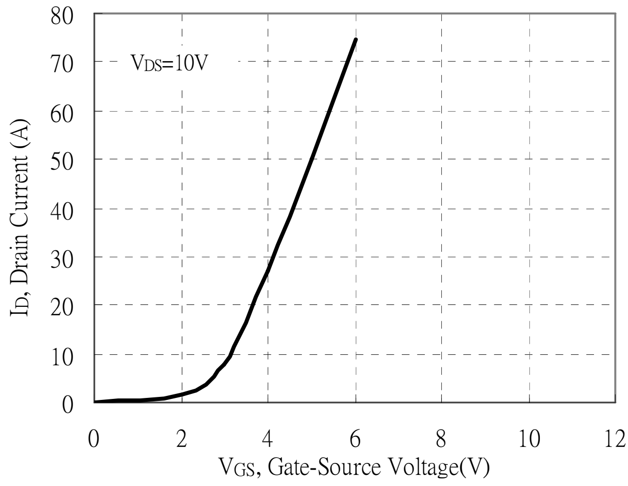


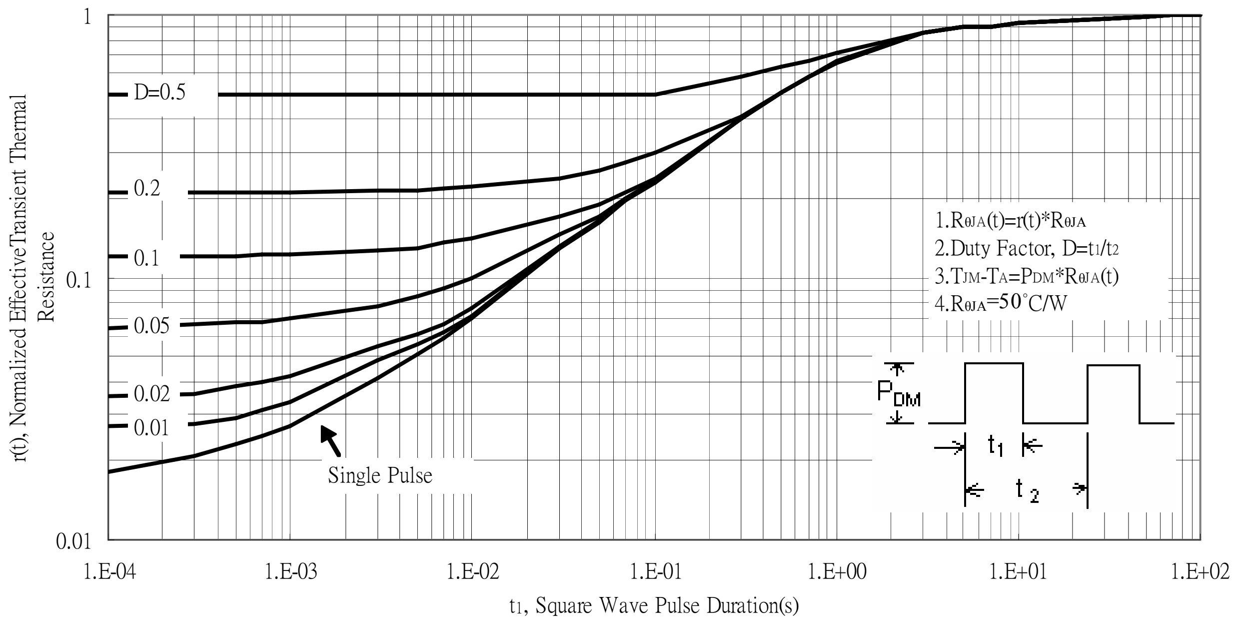




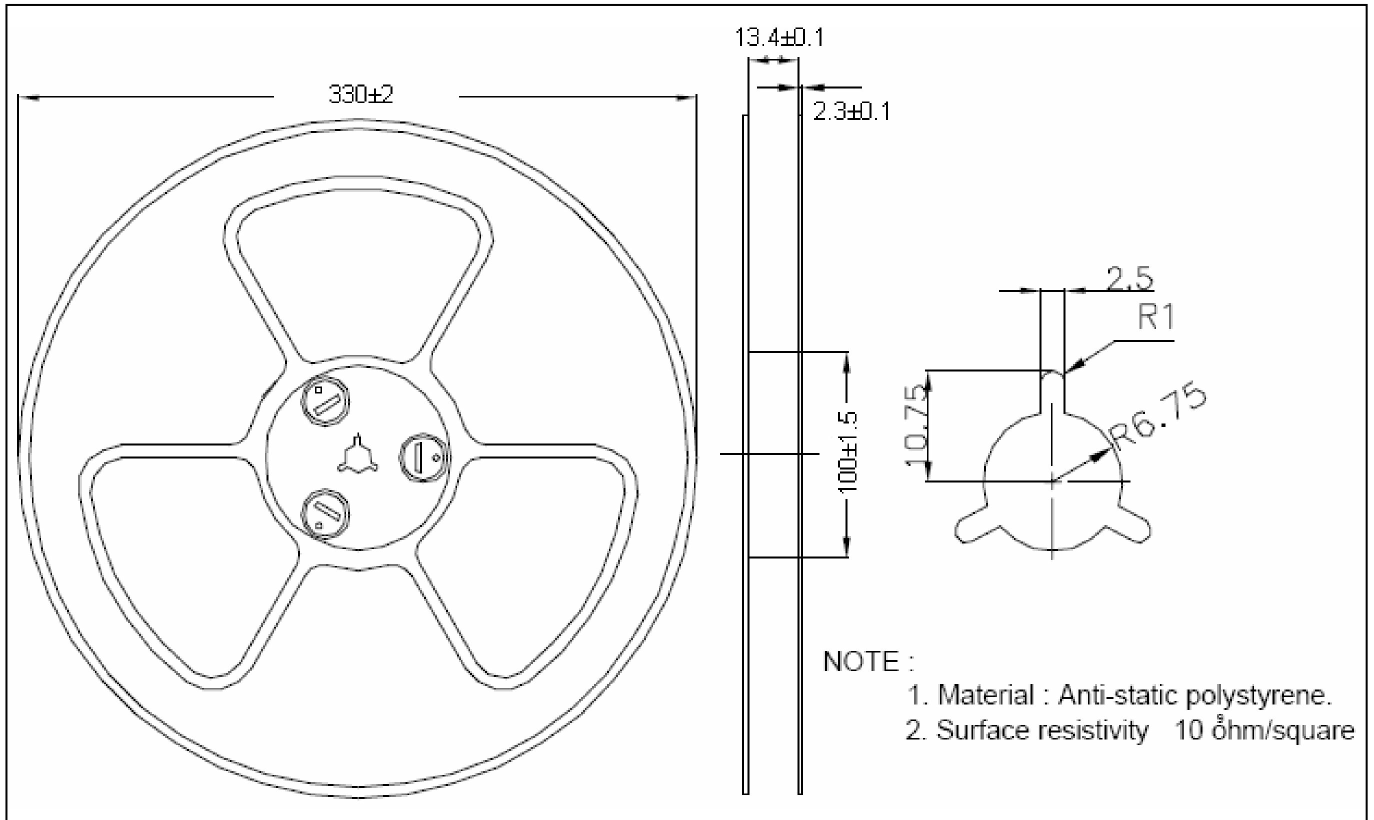


Typical Characteristics(Cont.)

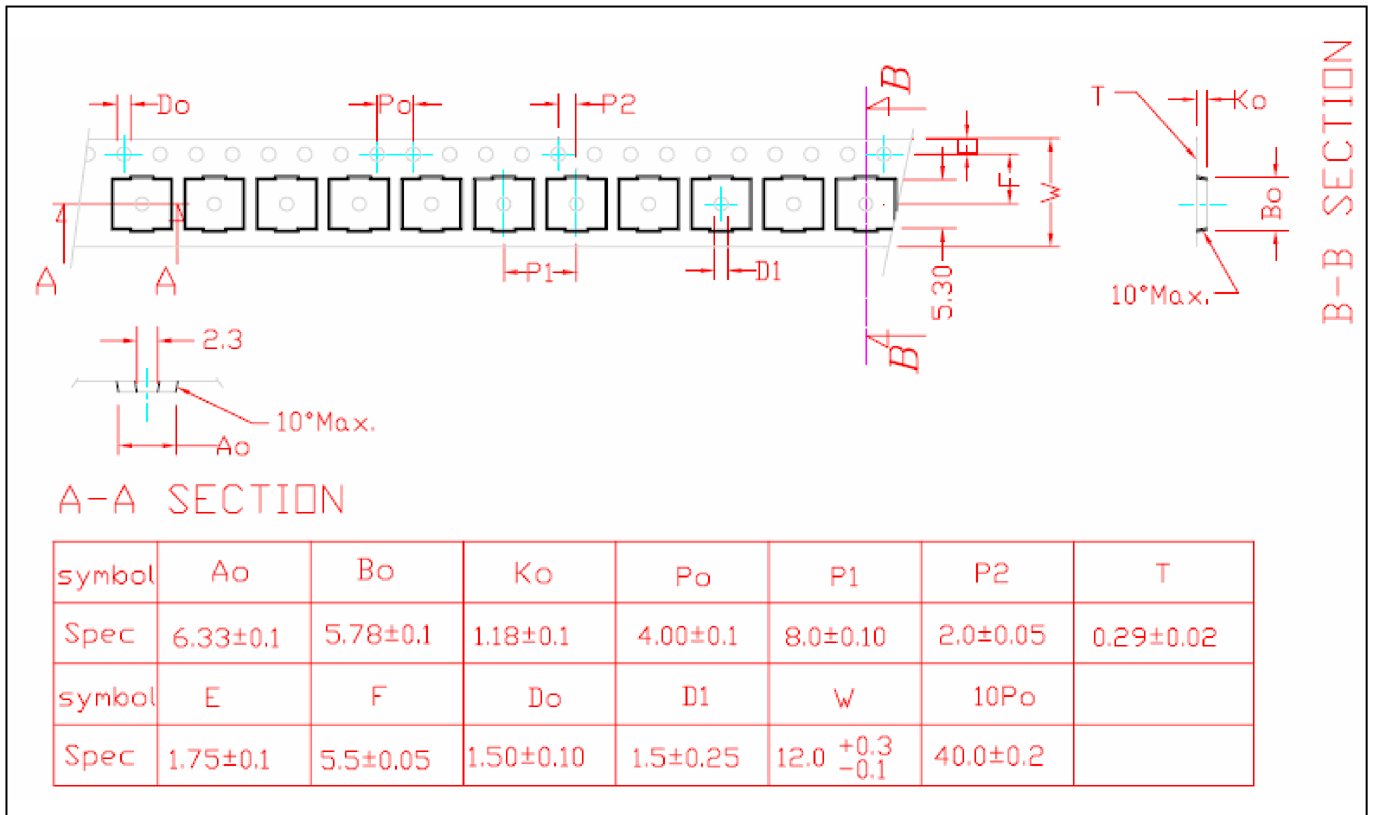




Reel Dimension



Carrier Tape Dimension

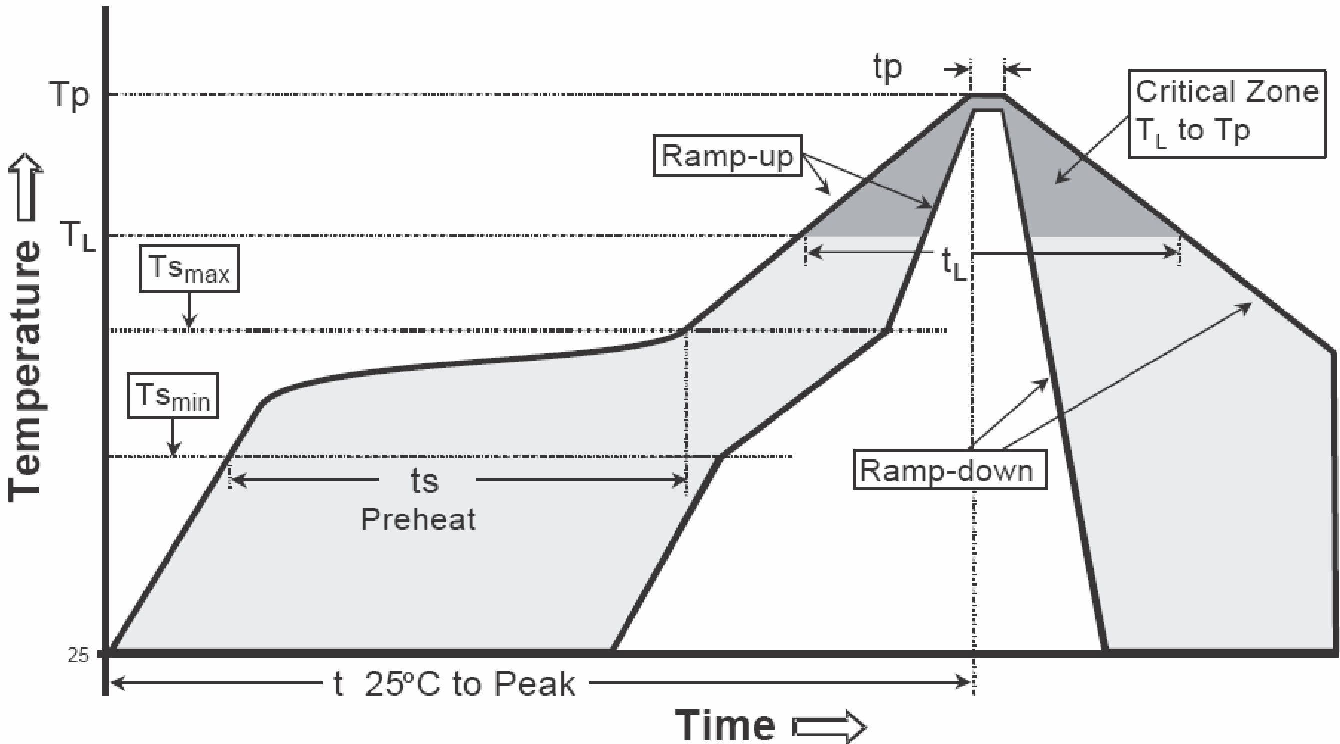


symbol	Ao	Bo	Ko	Po	P1	P2	T
Spec	6.33±0.1	5.78±0.1	1.18±0.1	4.00±0.1	8.0±0.10	2.0±0.05	0.29±0.02
symbol	E	F	Do	D1	W	10Po	
Spec	1.75±0.1	5.5±0.05	1.50±0.10	1.5±0.25	12.0 $^{+0.3}_{-0.1}$	40.0±0.2	

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

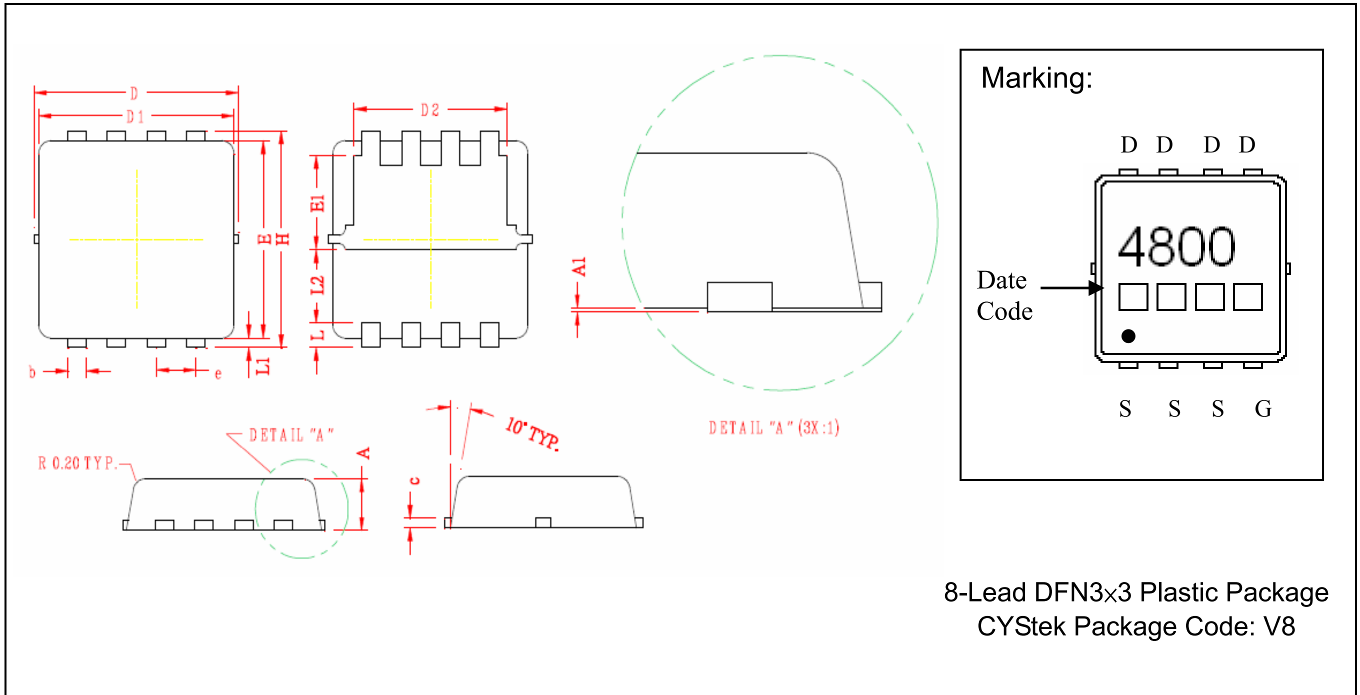
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time(ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN3x3 Dimension



8-Lead DFN3x3 Plastic Package
CYStek Package Code: V8

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0276	0.0354	0.70	0.90	E	0.1181	0.1260	3.00	3.20
A1	0.0000	0.0197	0.00	0.50	E1	0.0531	0.0610	1.35	1.55
b	0.0094	0.0138	0.24	0.35	e	0.0256 BSC		0.65 BSC	
c	0.0039	0.0079	0.10	0.20	H	0.1260	0.1339	3.20	3.40
D	0.1280	0.1339	3.25	3.40	L	0.0118	0.0197	0.30	0.50
D1	0.1201	0.1280	3.05	3.25	L1	0.0039	0.0079	0.10	0.20
D2	0.0945	0.1024	2.40	2.60	L2	0.0445 REF		1.13 REF	

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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