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## Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- Ideal for Printed Circuit Boards
- Designed for Saving Mounting Space
- UL Recognized File \# E223064


## Mechanical Data

- Case: Epoxy Case with Heat Sink Internally Mounted in the Bridge Encapsulation
- Terminals: Plated Leads Solderable per
 MIL-STD-202, Method 208
- Polarity: As Marked on Body
- Weight: 30 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



## Maximum Ratings and Electrical Characteristics $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

Single Phase, half wave, 60 Hz , resistive or inductive load.
For capacitive load, derate current by $20 \%$.

| Characteristics | Symbol | -00S | -01S | -02S | -04S | -06S | -08S | -10S | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | Vrrm <br> Vrwm VR | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | V |
| RMS Reverse Voltage | VR(RMS) | 35 | 70 | 140 | 280 | 420 | 560 | 700 | V |
| Average Rectified Output Current KBPC40 <br> @ $T_{C}=55^{\circ} \mathrm{C}$ KBPC50 | Io | $\begin{aligned} & 40 \\ & 50 \end{aligned}$ |  |  |  |  |  |  | A |
| Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half-sine-wave Superimposed <br> KBPC40 on Rated Load (JEDEC Method) | IFSM | $\begin{aligned} & 400 \\ & 400 \end{aligned}$ |  |  |  |  |  |  | A |
| Forward Voltage Drop KBPC40 @I <br> (per element) KBPC50 @ $I_{F}=25 A$ | VFM | 1.2 |  |  |  |  |  |  | V |
| Peak Reverse Current at Rated DC Blocking Voltage (per element) $\begin{array}{r} @ T_{A}=25^{\circ} \mathrm{C} \\ @ \mathrm{~T}_{\mathrm{A}}=100^{\circ} \mathrm{C} \end{array}$ | IR | $\begin{aligned} & 10 \\ & 1.0 \end{aligned}$ |  |  |  |  |  |  | $\begin{aligned} & \mu \mathrm{A} \\ & \mathrm{~mA} \end{aligned}$ |
| Typical Thermal Resistance (per element) (Note 1) | $\mathrm{R}_{\theta \mathrm{JC}}$ | 1.5 |  |  |  |  |  |  | K/W |
| RMS Isolation Voltage from Case to Lead | Viso | 2500 |  |  |  |  |  |  | V |
| Operating and Storage Temperature Range | Tj, Tsta | -55 to +150 |  |  |  |  |  |  | ${ }^{\circ} \mathrm{C}$ |

Note: 1. Thermal resistance junction to case per element mounted on $8 " \times 8 " \times 25 "$ thick AL plate.

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$\mathrm{T}_{\mathrm{C}}$, CASE TEMPERATURE ( ${ }^{\circ} \mathrm{C}$ )
Fig. 1 Forward. Current Derating Curve


NUMBER OF CYCLES AT 60 Hz
Fig. 3 Max Non-Repetitive Surge Current

$\mathrm{V}_{\mathrm{F}}$, INSTANTANEOUS FORWARD VOLTAGE (V)
Fig. 2 Typical Forward Characteristics (per element)

$\mathrm{V}_{\mathrm{R}}$, REVERSE VOLTAGE (V)
Fig. 4 Typical Junction Capacitance (per element)


PERCENT OF RATED PEAK REVERSE VOLTAGE (\%)
Fig. 5 Typical Reverse Characteristics (per element)

