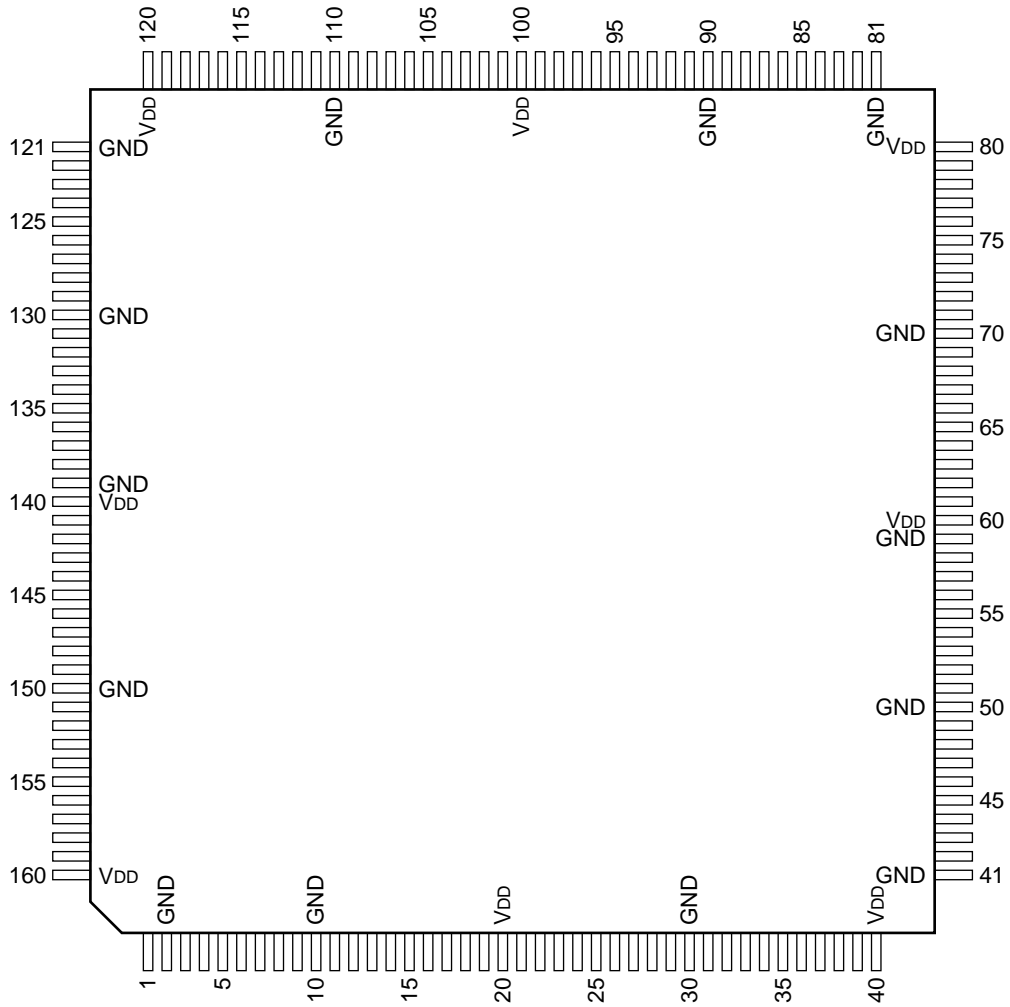


C-MOS AUDIO PROCESSOR

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	GND	41	—	GND	81	—	GND	121	—	GND
2	I	RST	42	I/O	I/O0	82	O	A16	122	I	TEST10
3	I	TEST0	43	I/O	I/O1	83	O	A17	123	I	TEST11
4	I	TEST1	44	I/O	I/O2	84	I	AEIM1	124	I	ADV1/2
5	I	TEST2	45	I/O	I/O3	85	I	AEIM2	125	I	ADV3/4
6	I	TEST3	46	I/O	I/O4	86	I	AEIM3	126	I	VAR1/2
7	I	MTRI	47	I/O	I/O5	87	I	AEIM4	127	I	VAR3/4
8	O	MTRO	48	I/O	I/O6	88	I	SIIM1	128	I	PPL1/2
9	O	CUE	49	I/O	I/O7	89	I	SIIM2	129	I	PPL3/4
10	—	GND	50	—	GND	90	—	GND	130	—	GND
11	O	LIN1/2	51	I/O	I/O8	91	I	SIIM3	131	I	CNF1/2
12	O	LIN3/4	52	I/O	I/O9	92	I	SIIM4	132	I	CNF3/4
13	I	ADC1/2	53	I/O	I/O10	93	I	ADVM1	133	I	REFV
14	I	ADC2	54	I/O	I/O11	94	I	ADVM2	134	I	CA0
15	I	ADC3/4	55	I/O	I/O12	95	I	ADVM3	135	I	CA1
16	I	ADC4	56	I/O	I/O13	96	I	ADVM4	136	I	CA2
17	I	TMODE2	57	I/O	I/O14	97	I	CNFM1	137	I	CA3
18	I	TMODE3	58	I/O	I/O15	98	I	CNFM2	138	I	FS256
19	I	TMODE4	59	—	GND	99	I	CNFM3	139	—	GND
20	—	VDD	60	—	VDD	100	—	VDD	140	—	VDD
21	I	TMODE5	61	O	OE	101	I	CNFM4	141	I	FS
22	I	TMODE6	62	O	WE	102	I	ENCM	142	I	CA4
23	I	TMODE7	63	O	A0	103	I	SIOM	143	I	CA5
24	I	SIF1/2	64	O	A1	104	I	AEOM	144	I	CA6
25	I	SIF3/4	65	O	A2	105	I	LINM	145	I	CA7
26	O	SIFO1/2	66	O	A3	106	I	MTRM	146	I	START
27	O	SIFO3/4	67	O	A4	107	I	IFLG1	147	I	CLOCK
28	O	AESO1/2	68	O	A5	108	I	IFLG2	148	I	SIN
29	O	AESO2	69	O	A6	109	I	IFLG3	149	O	SOUT
30	—	GND	70	—	GND	110	—	GND	150	—	GND
31	O	AESO3/4	71	O	A7	111	I	IFLG4	151	O	ENC1/2
32	O	AESO4	72	O	A8	112	I	TEST4	152	O	ENC3/4
33	I	AESI1/2	73	O	A9	113	I	TEST5	153	I	TEST12
34	I	AESI2	74	O	A10	114	I	TEST6	154	I	TEST13
35	I	AESI3/4	75	O	A11	115	I	TEST7	155	I	TEST14
36	I	AESI4	76	O	A12	116	I	TEST8	156	I	TEST15
37	I	DSPI1/2	77	O	A13	117	I	TEST9	157	I	TEST16
38	I	DSPI3/4	78	O	A14	118	I	TMODE0	158	I	TEST17
39	O	VTRG	79	O	A15	119	I	TMODE1	159	I	POM
40	—	VDD	80	—	VDD	120	—	VDD	160	—	VDD

13	ADC1/2	146	START	63	A0
14	ADC2	147	CLOCK	64	A1
15	ADC3/4	148	SIN	65	A2
16	ADC4	149	SOUT	66	A3
24	SIFI 1/2	3	TEST0	67	A4
25	SIFI 3/4	4	TEST1	68	A5
33	AESI 1/2	5	TEST2	69	A6
34	AESI 2	6	TEST3	71	A7
35	AESI 3/4	112	TEST4	72	A8
36	AESI4	113	TEST5	73	A9
7	MTRI	114	TEST6	74	A10
37	DSPI 1/2	115	TEST7	75	A11
38	DSPI 3/4	116	TEST8	76	A12
		117	TEST9	77	A13
		122	TEST10	78	A14
		123	TEST11	79	A15
		153	TEST12	82	A16
		154	TEST13	83	A17
		155	TEST14		
		156	TEST15		
		157	TEST16		
		158	TEST17		
				62	WE
				61	OE
84	AEIM 1			42	I/O0
85	AEIM 2			43	I/O1
86	AEIM 3			44	I/O2
87	AEIM 4			45	I/O3
88	SIIM 1			46	I/O4
89	SIIM 2			47	I/O5
91	SIIM 3			48	I/O6
92	SIIM 4			49	I/O7
93	ADVM1			51	I/O8
94	ADVM2			52	I/O9
95	ADVM3			53	I/O10
96	ADVM4			54	I/O11
97	CNFM1			55	I/O12
98	CNFM2			56	I/O13
99	CNFM3			57	I/O14
101	CNFM4			58	I/O15
102	ENCM			151	ENC 1/2
103	SIOM			152	ENC 3/4
104	AEOM			11	LIN 1/2
105	LINM			12	LIN 3/4
106	MTRM			26	SIFO1/2
107	IFLG1			27	SIFO3/4
108	IFLG2			28	AESO1/2
109	IFLG3			29	AESO2
111	IFLG4			31	AESO3/4
133	REFV			32	AESO4
141	FS			8	MTRO
138	FS256				
2	RST				
159	POM				
124	ADV1/2	118	TMODE0		
125	ADV3/4	119	TMODE1		
126	VAR1/2	17	TMODE2		
127	VAR3/4	18	TMODE3		
128	PPL1/2	19	TMODE4		
129	PPL3/4	21	TMODE5		
131	CNF1/2	22	TMODE6		
132	CNF3/4	23	TMODE7		
134	CA0	134	CA0		
135	CA1	135	CA1		
136	CA2	136	CA2		
137	CA3	137	CA3		
142	CA4	142	CA4		
143	CA5	143	CA5		
144	CA6	144	CA6		
145	CA7	145	CA7		
39	VTRG	39	VTRG		
9	CUE	9	CUE		

INPUT

ADC1/2	; A/D INPUT CH1/CH2
ADC2	; A/D INPUT CH2
ADC3/4	; A/D INPUT CH3/CH4
ADC4	; A/D INPUT CH4
ADV1/2	; DECODER INPUT CH1/CH2 (ADVANCE IN)
ADV3/4	; DECODER INPUT CH3/CH4 (ADVANCE IN)
ADVM1	; ADVANCE INPUT MUTE CH1 (ACTIVE LOW)
ADVM2	; ADVANCE INPUT MUTE CH2 (ACTIVE LOW)
ADVM3	; ADVANCE INPUT MUTE CH3 (ACTIVE LOW)
ADVM4	; ADVANCE INPUT MUTE CH4 (ACTIVE LOW)
AEIM1	; AES INPUT MUTE CH1 (ACTIVE LOW)
AEIM2	; AES INPUT MUTE CH2 (ACTIVE LOW)
AEIM3	; AES INPUT MUTE CH3 (ACTIVE LOW)
AEIM4	; AES INPUT MUTE CH4 (ACTIVE LOW)
AEOM	; AES OUTPUT MUTE (ACTIVE HIGH)
AES11/2	; AES INPUT CH1/CH2
AES12	; AES INPUT CH2
AES13/4	; AES INPUT CH3/CH4
AES14	; AES INPUT CH4
CA0 -CA7	; IC ADDRESS
CLOCK	; CPU I/F CLOCK
CNF1/2	; DECODER INPUT CH1/CH2 (CONFI IN)
CNF3/4	; DECODER INPUT CH3/CH4 (CONFI IN)
CNFM1	; CONFI INPUT MUTE CH1 (ACTIVE LOW)
CNFM2	; CONFI INPUT MUTE CH2 (ACTIVE LOW)
CNFM3	; CONFI INPUT MUTE CH3 (ACTIVE LOW)
CNFM4	; CONFI INPUT MUTE CH4 (ACTIVE LOW)
DSP11/2	; DSP INPUT CH1/CH2
DSP13/4	; DSP INPUT CH3/CH4
ENCM	; ENCODER OUTPUT MUTE (ACTIVE HIGH)
FS	; SYSTEM CLOCK (Fs)
FS256	; SYSTEM CLOCK (256 X Fs)
IFLG1	; INSERT FLAG CH1 (ACTIVE HIGH)
IFLG2	; INSERT FLAG CH2 (ACTIVE HIGH)
IFLG3	; INSERT FLAG CH3 (ACTIVE HIGH)
IFLG4	; INSERT FLAG CH4 (ACTIVE HIGH)
LINM	; LINE OUTPUT MUTE (ACTIVE HIGH)
MTRI	; MONITOR INPUT FOR 8-CHANNEL SYSTEM
MTRM	; MONITOR OUTPUT MUTE (ACTIVE HIGH)
POM	; POWER ON MUTE (ACTIVE LOW)
PPL1/2	; DECODER INPUT CH1/CH2 (P. PLAY IN)
PPL3/4	; DECODER INPUT CH3/CH4 (P. PLAY IN)
REFV	; SYSTEM CLOCK (V PULSE)
RST	; RESET (ACTIVE LOW)
SIF11/2	; SIF INPUT CH1/CH2
SIF13/4	; SIF INPUT CH3/CH4
SIN	; CPU I/F SERIAL IN
SIIM1	; SIF INPUT MUTE CH1 (ACTIVE LOW)
SIIM2	; SIF INPUT MUTE CH2 (ACTIVE LOW)
SIIM3	; SIF INPUT MUTE CH3 (ACTIVE LOW)
SIIM4	; SIF INPUT MUTE CH4 (ACTIVE LOW)
SIOM	; SIF OUTPUT MUTE (ACTIVE HIGH)
START	; CPU I/F START (PULSE)
TEST0 - TEST17	; TEST INPUT (DEFAULT LOW)
TMODE0	; TEST MODE 0 (DEFAULT LOW)
TMODE1	; TEST MODE 1 (DEFAULT LOW)
TMODE2	; TEST MODE 2 (DEFAULT LOW)
TMODE3	; TEST MODE 3 (DEFAULT LOW)
TMODE4	; TEST MODE 4 (DEFAULT LOW)
TMODE5	; TEST MODE 5 (DEFAULT LOW)
TMODE6	; TEST MODE 6 (DEFAULT LOW)
TMODE7	; TEST MODE 7 (DEFAULT LOW)
VAR1/2	; DECODER INPUT CH1/CH2 (VARIABLE IN)
VAR3/4	; DECODER INPUT CH3/CH4 (VARIABLE IN)

OUTPUT

AESO1/2	; AES OUTPUT CH1/CH2
AESO2	; AES OUTPUT CH2
AESO3/4	; AES OUTPUT CH3/CH4
AESO4	; AES OUTPUT CH4
A0 - A17	; EXTERNAL RAM ADDRESS
CUE	; CUE (D-MIX)
ENC1/2	; ENCODER OUTPUT CH1/CH2
ENC3/4	; ENCODER OUTPUT CH3/CH4
LIN1/2	; LINE OUTPUT CH1/CH2
LIN3/4	; LINE OUTPUT CH3/CH4
MTRO	; MONITOR OUTPUT FOR 8-CHANNEL SYSTEM
SIFO1/2	; SIF OUTPUT CH1/CH2
SIFO3/4	; SIF OUTPUT CH3/CH4
SOUT	; CPU I/F SERIAL OUT
OE	; EXTERNAL RAM OUTPUT ENABLE (ACTIVE LOW)
VTRG	; TRIGGER OUT (5V PULSE)
WE	; EXTERNAL RAM WRITE ENABLE (ACTIVE LOW)

INPUT/OUTPUT

I/O0 - I/O15	; EXTERNAL RAM
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