

REV.	CHANGE CONTENT	BY	DATE

Pins Description

Pin No.	Symbol	Description
1	/CS1	L:Chip Selected H:Chip Unselected
2	/RES	Reset: L: Enable; H: Disable
3	D/C	L:Instruction; H:Data
4	/WR	Write: L: Active
5	/RD	Read: L: Active
6-13	D0-D7	DATA BUS
14	VDD	Power supply pin:+3.0V
15	VSS	Ground
16	VEE	DC-DC voltage converter
17	C3N	For voltage booster circuit External capacitor about 0.47uF-1.0uF
18	C1P	
19	C1N	
20	C2N	
21	C2P	LCD driver voltage External capacitor about 0.1uF-0.47uF→VDD
22	VL2	
23	VL3	
24	VL4	
25	VL5	
26	VL6	

Reference documentation: SSD1815 DATASHEET

IC Connected With LCD

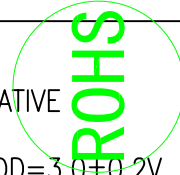
LCD PIN	IC PIN
C0-C63	COM0-COM63
S0-S127	SEG0-SEG127

Internal Connection

Pin Name	Connection
IRS	H: Internal Resistors
PS	H: Parallel interface
C68	L:8080 MPU Interface
MS	H: Master Mode
CLS	H: Internal Colock

NOTE:

1. DISPLAY TYPE: STN-BLUE-TRANSMISSIVE-NEGATIVE
2. VIEWING DIR.: 6:00
3. DRIVE METHOD:1/65DUTY,1/9BIAS,VOP=8.8V,VDD=3.0±0.2V
4. OP. TEMP.: -20°C~70°C
5. ST. TEMP.: -30°C~80°C
6. DRIVE IC: SSD1815BZ
7. BACKLIGHT:LED COLOR-WHITE, If=90mA, Vf=3.2±0.2V
LUMINANCE>70cd/m, X=0.27-0.32, Y=0.27-0.32



TOLERANCE
>0~6 ±0.10
>6~80 ±0.20
>80~240 ±0.30
>240~400 ±0.50
// ⊥ ⊙ ±0.10

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DRAWING No. : HG1286424G-bT62L-VC
DRAWING TITLE: LCM DRAWING
FILE NO:6

DESIGN	XH	20071227	STANDARD		REV. : A	SCALE : 1:1
CHECKED			TECHNOLOGY		PROJECTION:	UNIT : mm
APPROVED			Q. A			SHEET : 2/2