v.DataSheet4U.cor

TOSHIBA SOLID STATE AC RELAY

TSS1G45, TSS1J45, TSS1G47, TSS1J47

OPTICALLY ISOLATED, ZERO VOLTAGE TURN-ON, ZERO CURRENT TURN-OFF, NORMALLY OPEN SSR

COMPUTER PERIPHERALS MACHINE TOOL CONTROLS PROCESS CONTROL SYSTEMS TRAFFIC CONTROL SYSTEMS

R.M.S On-State Current : $I_{T(RMS)} = 1A$ Repetitive Peak Off-State Voltage $V_{DRM} = 400, 600V$

TTL Compatible

Isolation Voltage 2060V AC (t=1min.)

MAXIMUM RATINGS (Ta = 25°C) INPUT (CONTROL)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Control Input Voltage (DC) (Note 1)	V _{F (IN)}	6	V
Control Input Current (DC)	I _{F (IN)}	20	mA

OUTPUT (LOAD)

Repetitive Peak	TSS1G45 TSS1G47	V	400	V	
Off-State Voltage	TSS1J45 TSS1J47	$V_{ m DRM}$	600		
Nominal AC Line	TSS1G45 TSS1G47	VAG	120	V	
Voltage	TSS1J45 TSS1J47	VAC	240	V	
R.M.S On-State Current	I _T (RMS)	1	A		
Peak One Cycle Surge On-State Current (Non-Repetitive)		I_{TSM}	12 (50Hz)	A	
Operating Frequency Range		f	45~65	Hz	
Isolation Voltage (t=1min., Input to Output)		BVS/AC	2060	V	
Operating Temperature Range		$T_{ m opr}$	-30~80	$^{\circ}\mathrm{C}$	
Storage Temperature Range		$\mathrm{T}_{\mathrm{stg}}$	-30~80	$^{\circ}\mathrm{C}$	

Unit in mm

47 N	лах.	15 MAX.	
		<u> </u>	YPE IARK
4 3	2 1	24 MAX.	
	ø1.	0 (2	2)
(a) (5.	-	6)	_
0,12.	'- 10.16		

TYPE	а	b
TSS1G45 TSS1J45	7.2	7.62
TSS1G47 TSS1J47	9.7	5.08

- **OUTPUT (AC)** 2.
- OUTPUT (AC)
- INPUT(+) 3. INPUT (-)

JEDEC		
EIAJ	_	
TOSHIBA	TSS1G45 TSS1J45	10-45A1A
	TSS1G47 TSS1J47	10-45A2A

Weight: 10g

Note 1: Driving input rating: Insert an external resistance into SSR when the power supply over 6V is used.

Note 2: Snubber network (C-R) is necessary to protect from surge voltage and dv/dt fire.

Snubber network is to be connected between #1 and #2 terminal.

Note 3: Mounting: Soldering of printed wiring board should be used under 260°C and 10 second.

2001-05-24

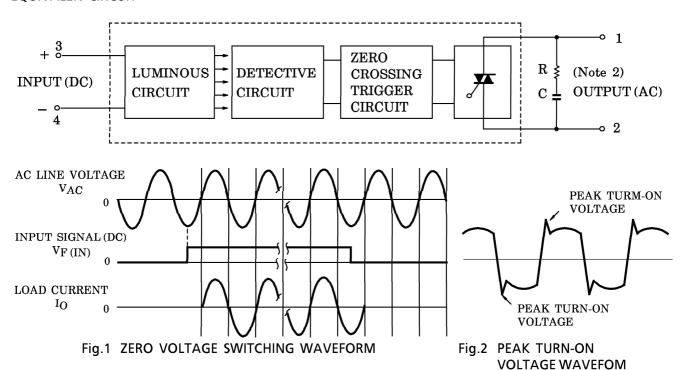
ELECTRICAL CHARACTERISTICS (Ta = 25°C) INPUT (CONTROL)

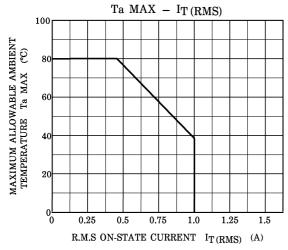
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Pick Up Voltage	$V_{ extbf{FT}}$	$ m V_{AC}\!=\!100V_{rms}$ Resistive Load ($R_{L}\!=\!100\Omega$)	_	_	4.5	V
Drop Out Voltage	$ m V_{FD}$		1.0	_	_	V
Input Resistance	R(IN)		_	300	_	Ω

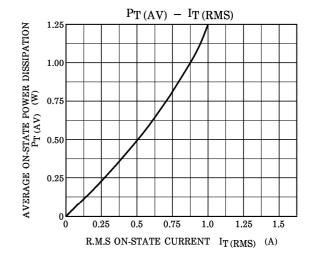
OUTPUT (LOAD)

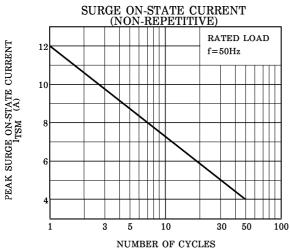
Off-State TSST Leakage Current TSST	TSS1G45 TSS1G47	I _{OL}	$V_{ m AC}$ =100 $V_{ m rms}$, f=50Hz			1	- mA
	TSS1J45 TSS1J47		$V_{ m AC}$ =200 $V_{ m rms}$, f=50Hz			2	
Peak On-State Vo	ltage	$V_{ extbf{TM}}$	I _{TM} =6A		_	2.6	V
Peak Turn-On Vol	ltage	v_{ON}	$V_{AC} = 100 V_{rms}$ (Fig.2)			5	V
dv / dt (Off-State)		dv / dt	$V_{ m DRM} = 0.7 imes m Rated$	50			V/μs
dv / dt (Commutati:	ng)	(dv / dt) c	$V_{DRM} = 0.7 \times Rated, I_{T} = 1A$	2			V/μs
Turn-On Time		ton	$V_{AC} = 100 V_{rms}$			1/2	Cycle
Turn-Off Time		t_{off}	Resistive Load ($R_L = 100\Omega$)	_		1/2	Cycle
Isolation Resistance	ce	$R_{\mathbf{S}}$	V=1kV, R.H=40~60%	_	10^{9}	_	Ω

EQUIVALEN CIRCUIT









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