TOSHIBA Field Effect Transistor Silicon P, N Channel MOS Type (U-MOS IV / U-MOS III)

TPCP8403

Portable Equipment Applications
Motor Drive Applications
DC-DC Converter Applications

- Lead(Pb)-Free
- Low drain-source ON resistance : P Channel $R_{DS\ (ON)}$ = 55 mΩ (typ.)
  N Channel $R_{DS\ (ON)}$ = 31 mΩ (typ.)
- High forward transfer admittance : P Channel $|Y_{fs}|$ = 6.0 S (typ.)
  N Channel $|Y_{fs}|$ = 8.6 S (typ.)
- Low leakage current : P Channel I_{DSS} = −10 μA (V_{DS} = −40 V)
  N Channel I_{DSS} = 10 μA (V_{DS} = 40 V)
- Enhancement mode
  : P Channel V_{th} = −0.8 to −2.0 V (V_{DS} = −10 V, I_D = −1mA)
  N Channel V_{th} = 1.3 to 2.5 V (V_{DS} = 10 V, I_D = 1mA)

Absolute Maximum Ratings (Ta = 25°C)

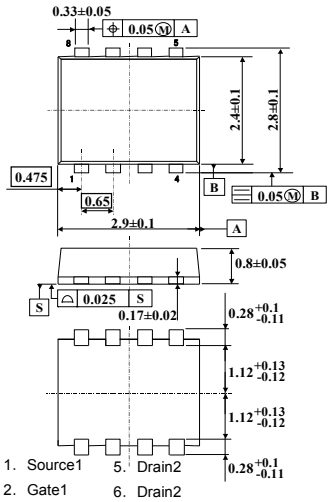
Characteristics		Symbol	Rating		Unit
Drain-source voltage		V_{DSS}	−40	40	V
Drain-gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	−40	40	V
Gate-source voltage		V_{GSS}	±20	±20	V
Drain current	DC (Note 1)	I_D	−3.4	4.7	A
	Pulse (Note 1)	I_{DP}	−13.6	18.8	
Drain power dissipation (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	$P_{D\ (1)}$	1.48	1.48	W
	Single-device value at dual operation (Note 3b)	$P_{D\ (2)}$	1.23	1.23	
Drain power dissipation (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	$P_{D\ (1)}$	0.58	0.58	
	Single-device value at dual operation (Note 3b)	$P_{D\ (2)}$	0.36	0.36	
Single pulse avalanche energy (Note 4)		E_{AS}	5.5	10.6	mJ
Avalanche current		I_{AR}	−3.4	4.7	A
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3b, 5)		E_{AR}	0.12		mJ
Channel temperature		T_{ch}	150		°C
Storage temperature range		T_{stg}	−55~150		°C

Note: For Notes 1 to 6, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with caution.

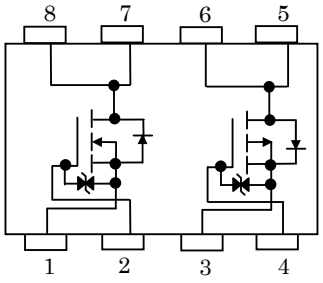
Unit: mm



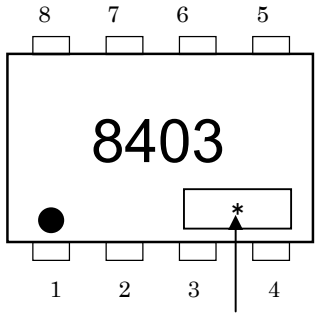
JEDEC	—
JEITA	—
TOSHIBA	2-3V1G

Weight: 0.017 g (typ.)

Circuit Configuration



Marking (Note 6)



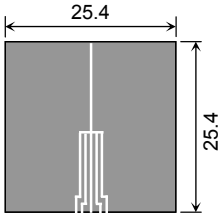
Lot No.

Thermal Characteristics

Characteristics		Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	Single-device operation (Note 3a)	$R_{th\ (ch\text{-}a)\ (1)}$	84.5	°C/W
	Single-device value at dual operation (Note 3b)	$R_{th\ (ch\text{-}a)\ (2)}$	101.6	
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	Single-device operation (Note 3a)	$R_{th\ (ch\text{-}a)\ (1)}$	215.5	°C/W
	Single-device value at dual operation (Note 3b)	$R_{th\ (ch\text{-}a)\ (2)}$	347.2	

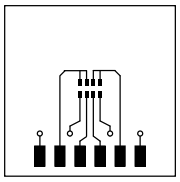
Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(a)



FR-4
25.4 × 25.4 × 0.8
(Unit: mm)

(b)

Note 3: a) The power dissipation and thermal resistance values shown are for a single device. (During single-device operation, power is only applied to one device.)

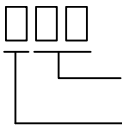
b) The power dissipation and thermal resistance values shown are for a single device. (During dual operation, power is evenly applied to both devices.)

Note 4: P Channel: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω, I_{AR} = −3.4 A
N Channel: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω, I_{AR} = 4.7 A

Note 5: Repetitive rating: pulse width limited by maximum channel temperature

Note 6: ● on the lower left of the marking indicates Pin 1.

※ Weekly code (3 digits):

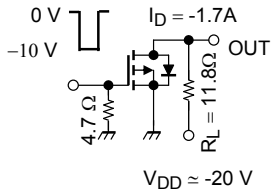


Week of manufacture
(01 for the first week of the year, continuing up to 52 or 53)

Year of manufacture
(The last digit of the calendar year)

P-ch

Electrical Characteristics (Ta = 25°C)

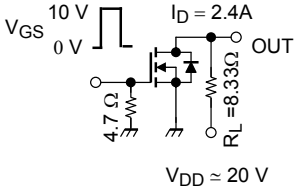
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut-off current		I_{DSS}	V_{DS} = −40 V, V_{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = −10 mA, V_{GS} = 0 V	−40	—	—	V
		$V_{(BR)\ DSX}$	I_D = −10 mA, V_{GS} = 20 V	−25	—	—	
Gate threshold voltage		V_{th}	V_{DS} = −10 V, I_D = −1 mA	−0.8	—	−2.0	V
Drain-source ON resistance		$R_{DS\ (ON)}$	V_{GS} = −4.5 V, I_D = −1.7 A	—	80	105	mΩ
			V_{GS} = −10 V, I_D = −1.7 A	—	55	70	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = −10 V, I_D = −1.7 A	3.0	6.0	—	S
Input capacitance		C_{iss}	V_{DS} = −10 V, V_{GS} = 0 V, f = 1 MHz	—	680	—	pF
Reverse transfer capacitance		C_{rss}		—	80	—	
Output capacitance		C_{oss}		—	100	—	
Switching time	Rise time	t_r		—	8.5	—	ns
	Turn-on time	t_{on}		—	16	—	
	Fall time	t_f		—	16	—	
	Turn-off time	t_{off}	Duty ≦ 1%, t_w = 10 μs	—	120	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ −32 V, V_{GS} = −10 V, I_D = −3.4 A	—	15	—	nC
Gate-source charge 1		Q_{gs1}		—	2	—	
Gate-drain (“miller”) charge		Q_{gd}		—	3.5	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	−13.6	A
Forward voltage (diode)		V_{DSF}	I_{DR} = −3.4 A, V_{GS} = 0 V	—	—	1.2	V

N-ch

Electrical Characteristics (Ta = 25°C)

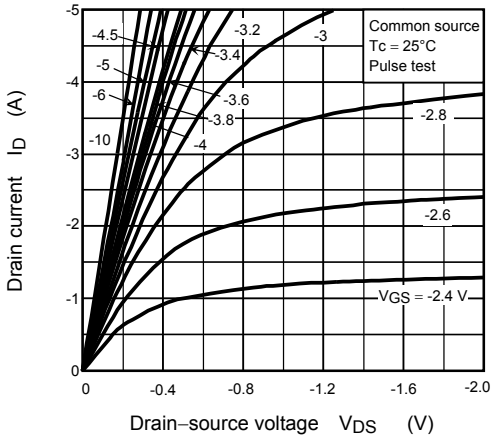
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	—	—	±10	μA
Drain cut-off current		I_{DSS}	V_{DS} = 40 V, V_{GS} = 0 V	—	—	10	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	40	—	—	V
		$V_{(BR)\ DSX}$	I_D = 10 mA, V_{GS} = −20 V	15	—	—	
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	1.3	—	2.5	V
Drain-source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 4.5 V, I_D = 2.4 A	—	43	60	mΩ
			V_{GS} = 10 V, I_D = 2.4 A	—	31	40	
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 2.4 A	4.3	8.6	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	770	—	pF
Reverse transfer capacitance		C_{rss}		—	70	—	
Output capacitance		C_{oss}		—	105	—	
Switching time	Rise time	t_r	 Duty ≦ 1%, t_w = 10 μs	—	8	—	ns
	Turn-on time	t_{on}		—	15	—	
	Fall time	t_f		—	9	—	
	Turn-off time	t_{off}		—	70	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≈ 32 V, V_{GS} = 10 V, I_D = 4.7 A	—	16	—	nC
Gate-source charge 1		Q_{gs1}		—	2.5	—	
Gate-drain (“miller”) charge		Q_{gd}		—	4	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

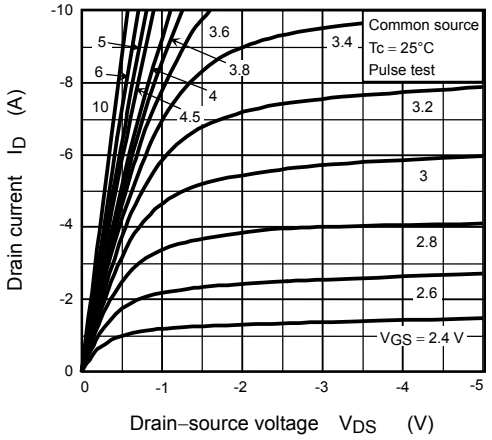
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Drain reverse current	Pulse (Note 1)	I_{DRP}	—	—	—	18.8	A
Forward voltage (diode)		V_{DSF}	I_{DR} = 4.7 A, V_{GS} = 0 V	—	—	−1.2	V

P-ch

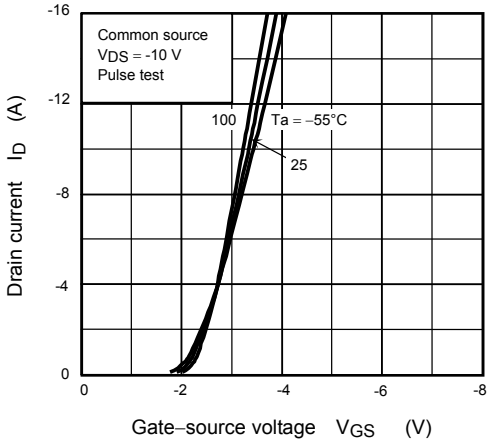
$I_D - V_{DS}$



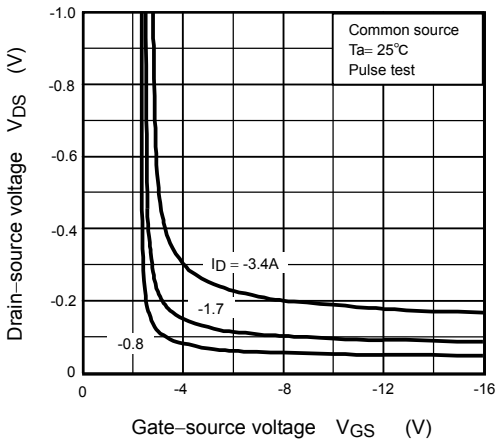
$I_D - V_{DS}$



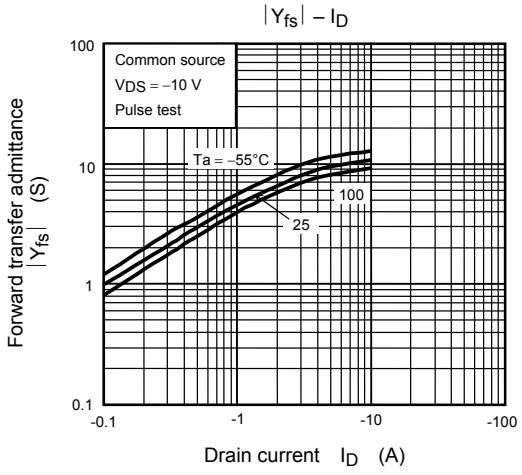
$I_D - V_{GS}$



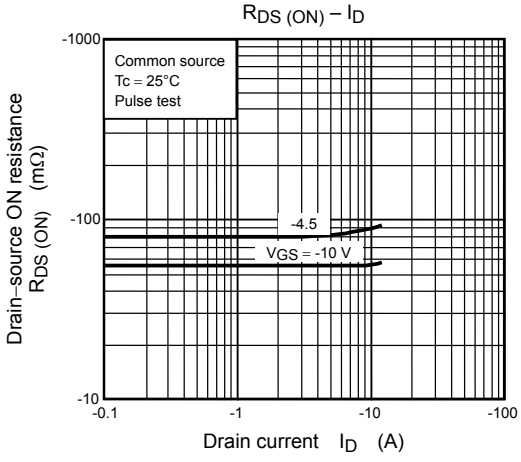
$V_{DS} - V_{GS}$



$|Y_{fs}| - I_D$

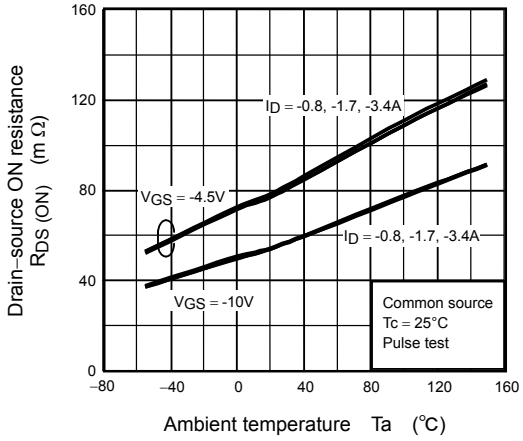


$R_{DS\ (ON)} - I_D$

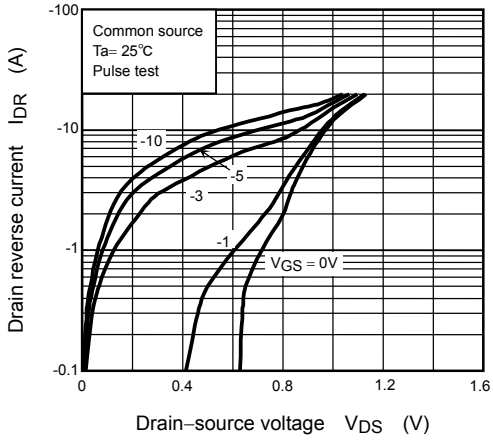


P-ch

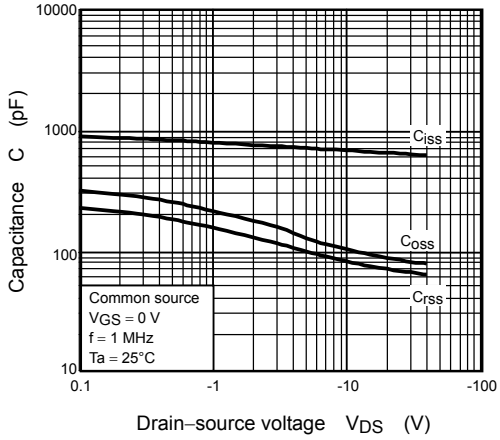
$R_{DS\ (ON)}$ – Ta



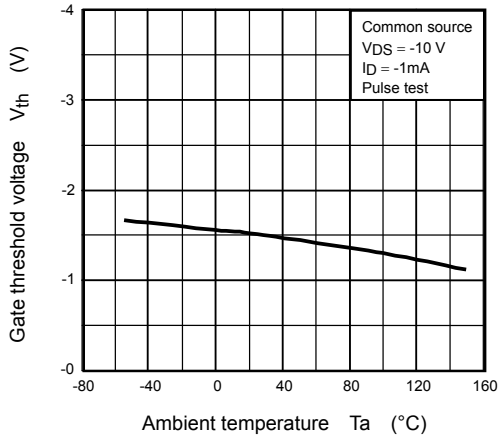
I_{DR} – V_{DS}



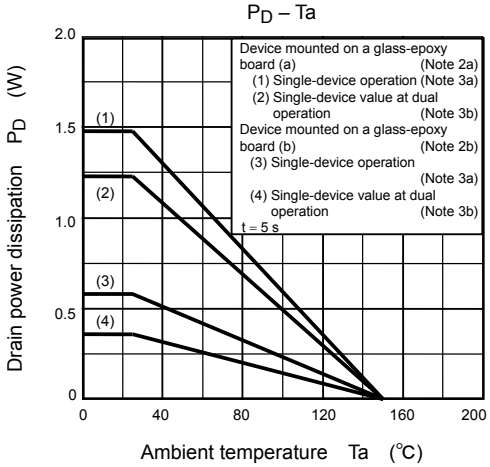
Capacitance – V_{DS}



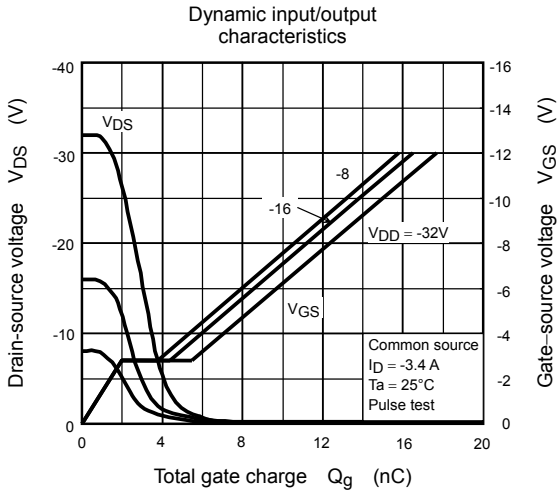
V_{th} – Ta



P_D – Ta

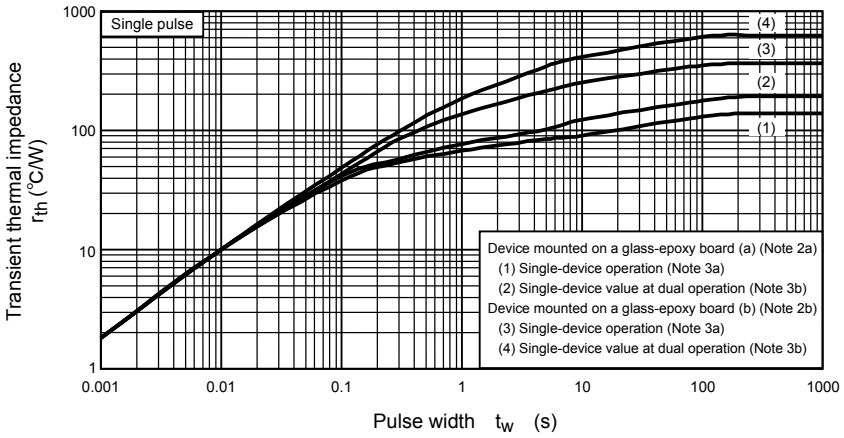


Dynamic input/output characteristics

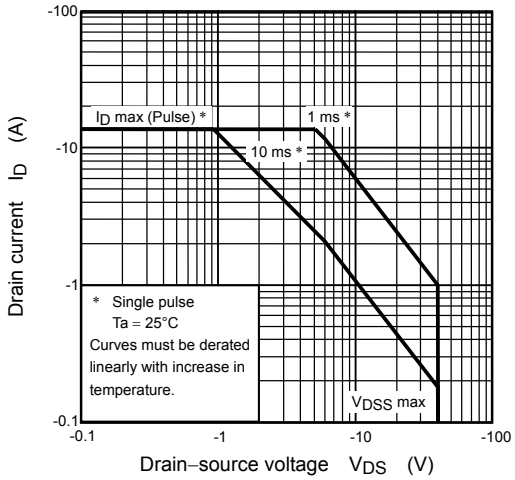


P-ch

$r_{th} - t_w$

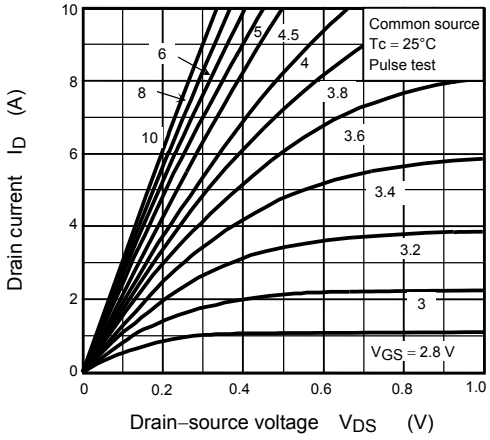


Safe operating area

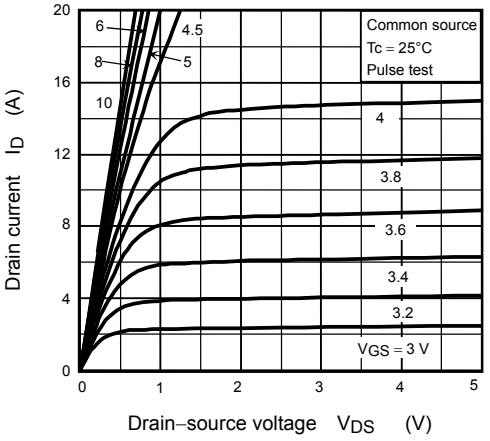


N-ch

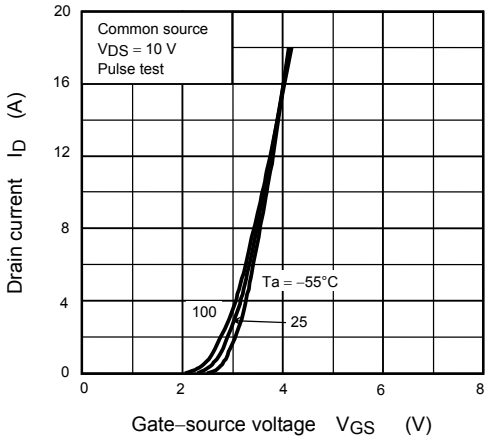
$I_D - V_{DS}$



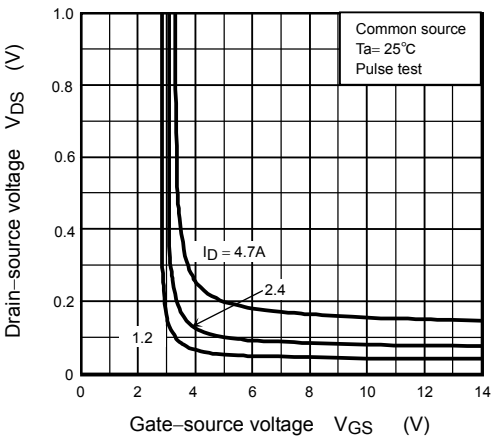
$I_D - V_{DS}$



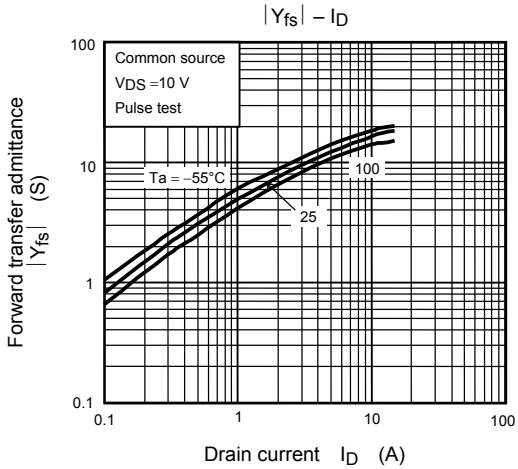
$I_D - V_{GS}$



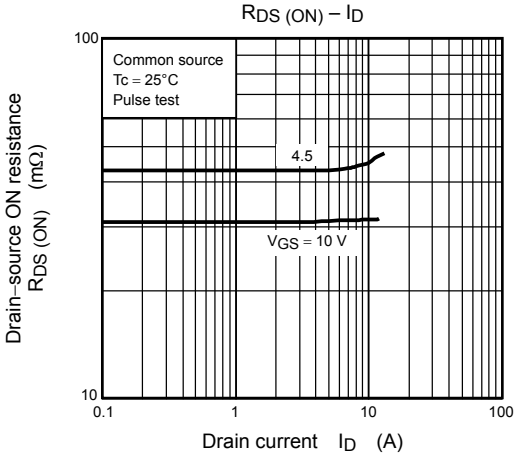
$V_{DS} - V_{GS}$



$|Y_{fs}| - I_D$

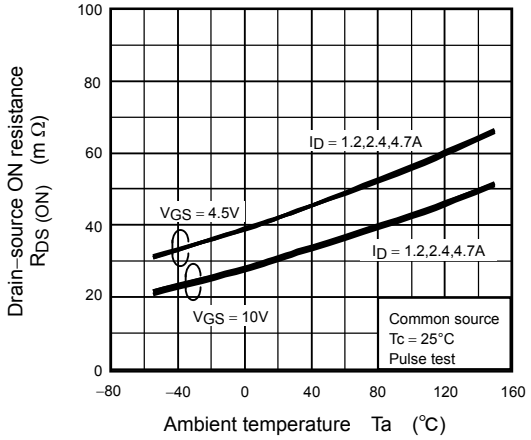


$R_{DS\ (ON)} - I_D$

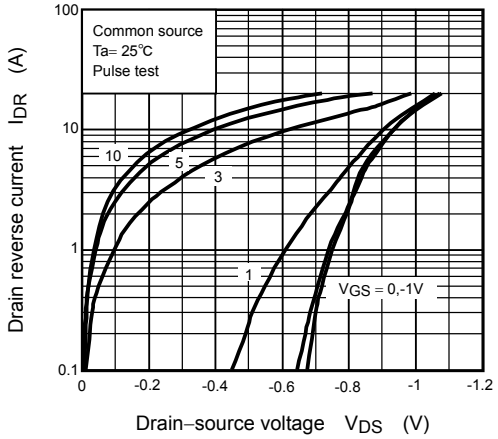


N-ch

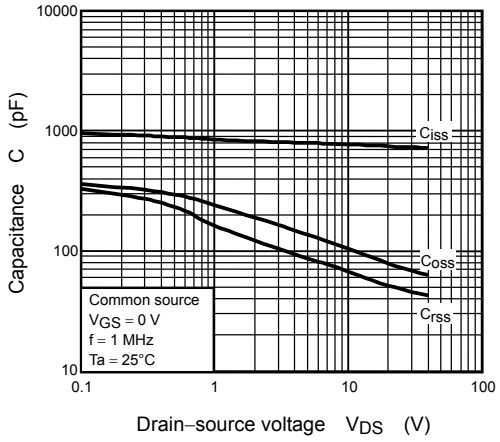
$R_{DS\ (ON)}$ – Ta



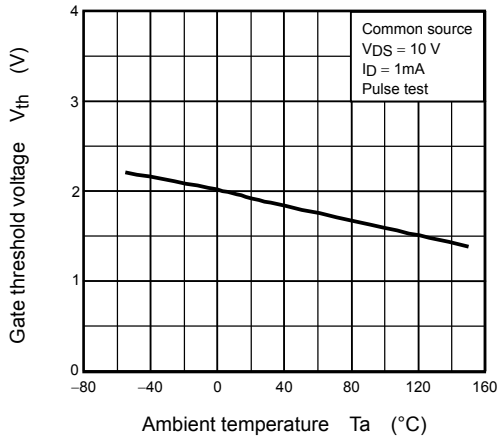
I_{DR} – V_{DS}



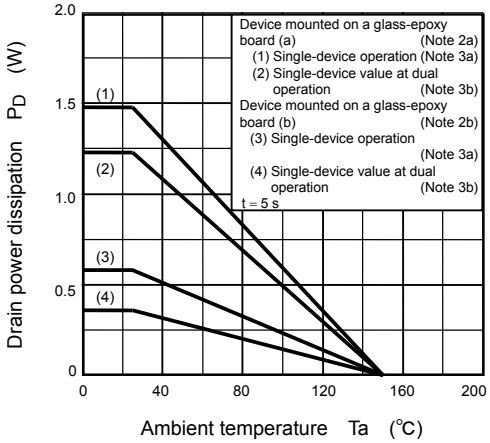
Capacitance – V_{DS}



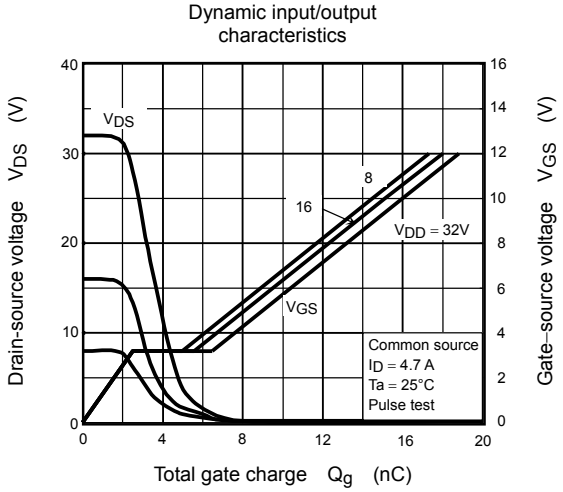
V_{th} – Ta



P_D – Ta

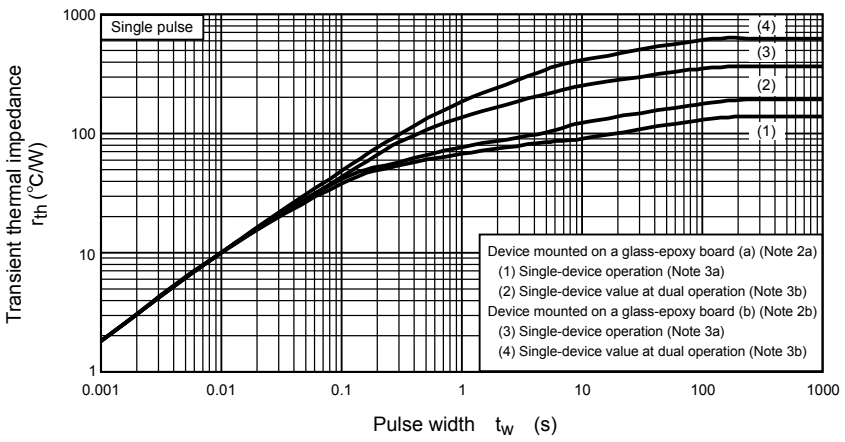


Dynamic input/output characteristics

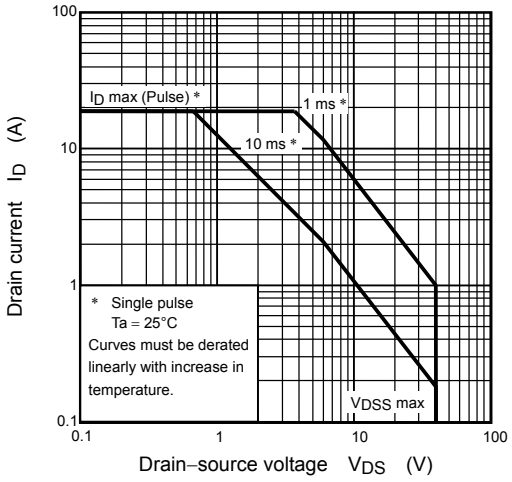


N-ch

$r_{th} - t_w$



Safe operating area



RESTRICTIONS ON PRODUCT USE

20070701-EN

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the “Handling Guide for Semiconductor Devices,” or “TOSHIBA Semiconductor Reliability Handbook” etc.
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