

CPW2-0650-S010BSilicon Carbide Schottky Diode Chip

Z-Rec® Rectifier

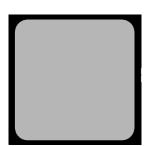
 $V_{RRM} = 650 \text{ V}$

 $I_F = 10 A$

 $\mathbf{Q}_{c} = 25 \text{ nC}$

Features Chip Outline

- 650-Volt Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F



Part Number	Die Size	Anode	Cathode
CPW2-0650-S010B	1.92 x 1.92 mm²	Al	Ni/Ag

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	650	V		
V _{RSM}	Surge Peak Reverse Voltage	650	V		
V _{DC}	DC Blocking Voltage	650	V		
I _F	Continuous Forward Current	10	А	T ₃ =175°C	
I _{FRM}	Repetitive Peak Forward Surge Current	67	А	$T_c=25$ °C, $t_p=10$ ms, Half Sine Wave, D=0.3	1
I_{FSM}	Non-Repetitive Peak Forward Surge Current	250	А	$T_c=25$ °C, $t_p=10$ µs, Pulse	1
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C		
T _{Proc}	Maximum Processing Temperature	325	°C	10 min Maximum	



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 2.0	1.8 2.4	V	I _F = 10 A T _J =25°C I _F = 10 A T _J =175°C	
I_R	Reverse Current	12 24	60 220	μΑ	$V_R = 650 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 650 \text{ V } T_J = 175^{\circ}\text{C}$	
Q _c	Total Capacitive Charge	25		nC	$V_R = 650 \text{ V, } I_F = 10 \text{ A}$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	480 50 42		pF	$V_R = 0 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$ $V_R = 200 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$ $V_R = 400 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$	

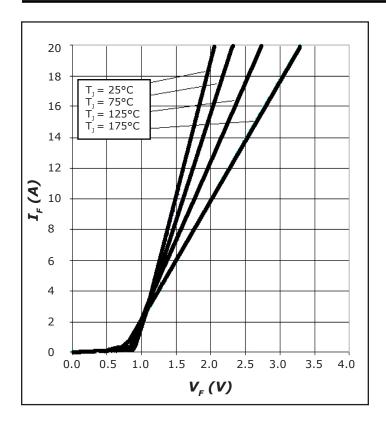
Note: 1. Assumes $R_{\rm ejc}$ Thermal Resistance $\leq 1.1 \, ^{\circ} \text{C/W}$ and $T_{\rm c} \leq 150 \, ^{\circ} \text{C}$

Mechanical Parameters

Parameter	Тур.	Unit				
Die Size	1.92 x 1.92	mm				
Anode Pad Size	1.65 x 1.65	mm				
Anode Pad Opening	1.45 x 1.45	mm				
Thickness	377 ± 10%	μm				
Wafer Size	100	mm				
Anode Metalization (AI)	4	μm				
Cathode Metalization (Ni/Ag)	1.8	μm				
Frontside Passivation	Polyimide					



Typical Performance



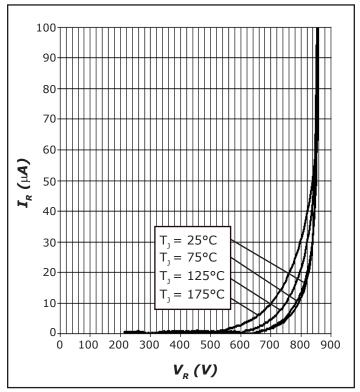


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics

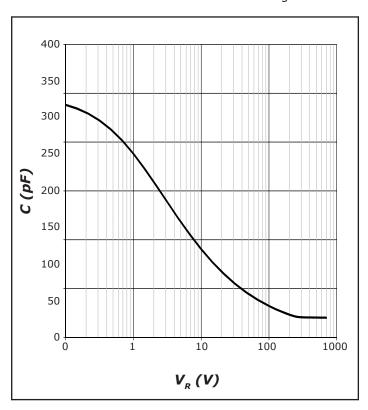
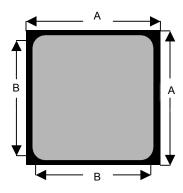


Figure 3. Capacitance vs. Reverse Voltage



Chip Dimensions



symbol	dimension		
	mm	inch	
Α	1.92	0.076	
В	1.45	0.057	

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of www.cree.com.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into
the human body nor in applications in which failure of the product could lead to death, personal injury or property
damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines,
cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control
systems, or air traffic control systems.

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