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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL product

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2SK740

Silicon N Channel MOS FET

REJ03G0904-0200
(Previous: ADE-208-1242)
Rev.2.00
Sep 07, 2005

Application

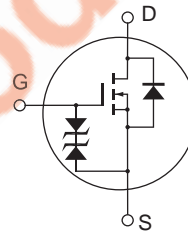
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

Outline

RENESAS Package code: PRSS0004AC-A
(Package name: TO-220AB)



1. Gate
2. Drain
(Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DS}	150	V
Gate to source voltage	V _{GS}	±20	V
Drain current	I _D	10	A
Drain peak current	I _{D(pulse)} ^{*1}	40	A
Body to drain diode reverse drain current	I _{DR}	10	A
Channel dissipation	P _{ch} ^{*2}	50	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%
 2. Value at T_C = 25°C

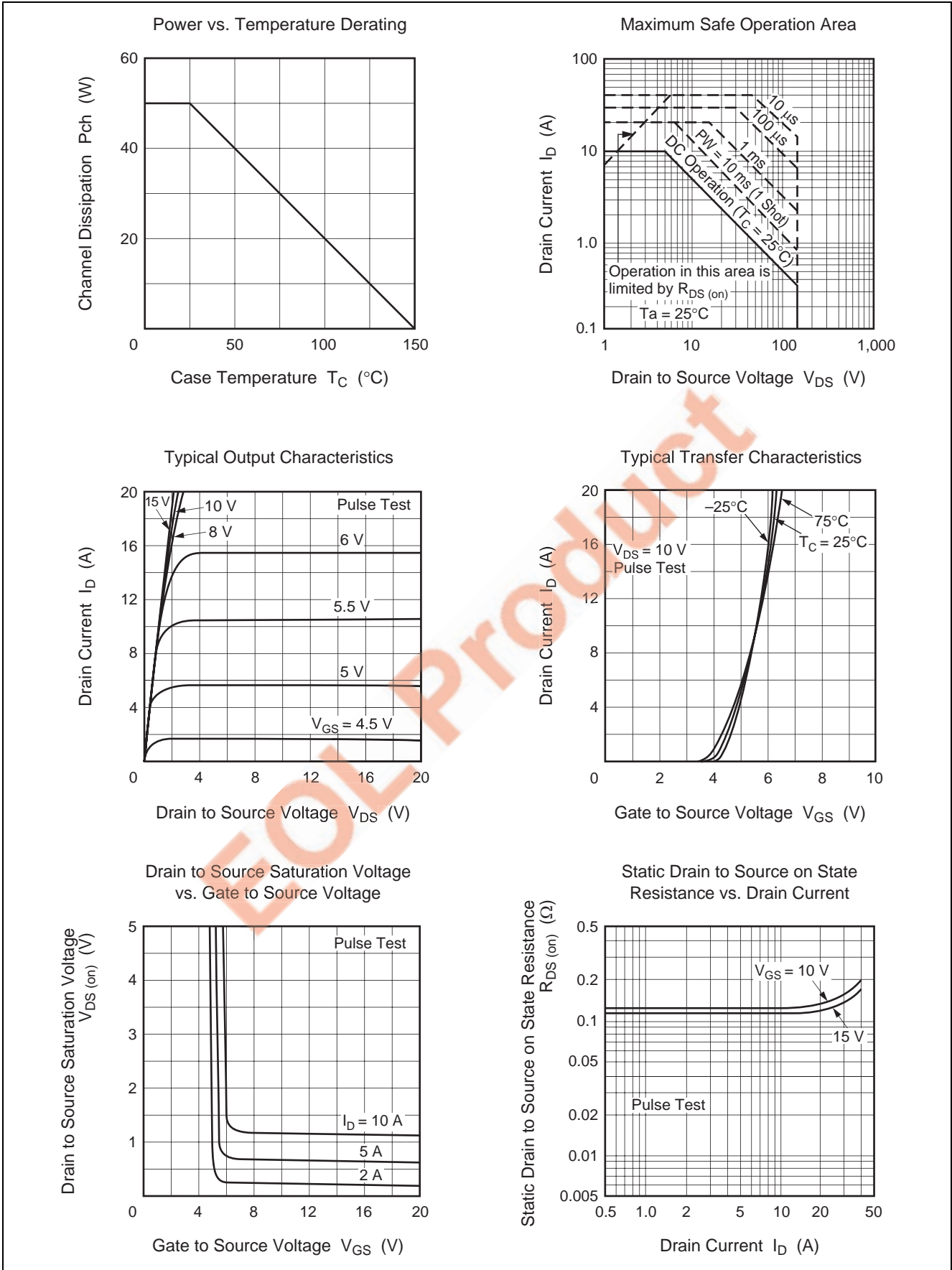
Electrical Characteristics

(Ta = 25°C)

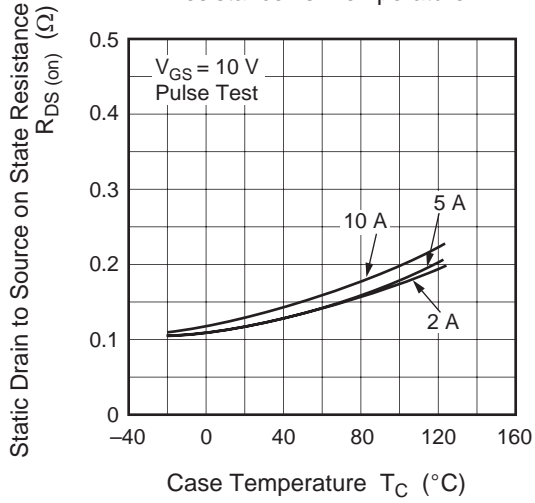
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	150	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	250	μA	V _{DS} = 120 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS(off)}	2.0	—	4.0	V	I _D = 1 mA, V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	0.12	0.15	Ω	I _D = 5 A, V _{GS} = 10 V ^{*3}
Forward transfer admittance	y _{fs}	4.0	7.0	—	S	I _D = 5 A, V _{DS} = 10 V ^{*3}
Input capacitance	C _{iss}	—	1200	—	pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output capacitance	C _{oss}	—	550	—	pF	
Reverse transfer capacitance	C _{rss}	—	85	—	pF	
Turn-on delay time	t _{d(on)}	—	20	—	ns	I _D = 5 A, V _{GS} = 10 V, R _L = 6 Ω
Rise time	t _r	—	50	—	ns	
Turn-off delay time	t _{d(off)}	—	70	—	ns	
Fall time	t _f	—	40	—	ns	
Body to drain diode forward voltage	V _{DF}	—	1.2	—	V	I _F = 10 A, V _{GS} = 0
Body to drain diode reverse recovery time	t _{rr}	—	220	—	ns	I _F = 10 A, V _{GS} = 0, di _F /dt = 50 A/μs

Note: 3. Pulse test

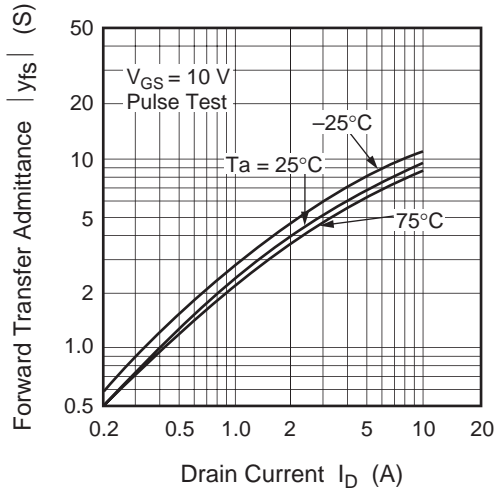
Main Characteristics



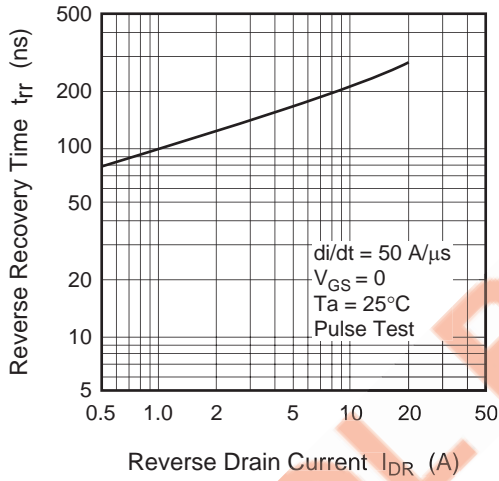
Static Drain to Source on State Resistance vs. Temperature



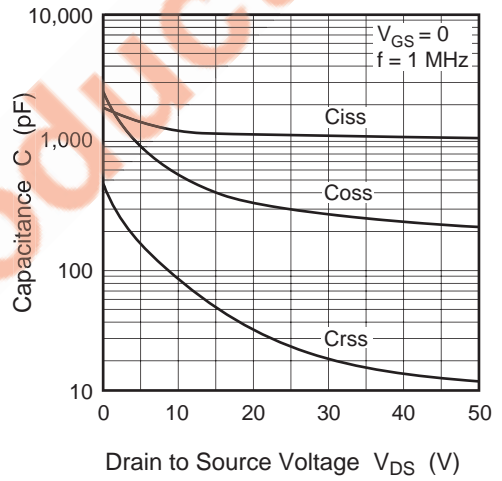
Forward Transfer Admittance vs. Drain Current



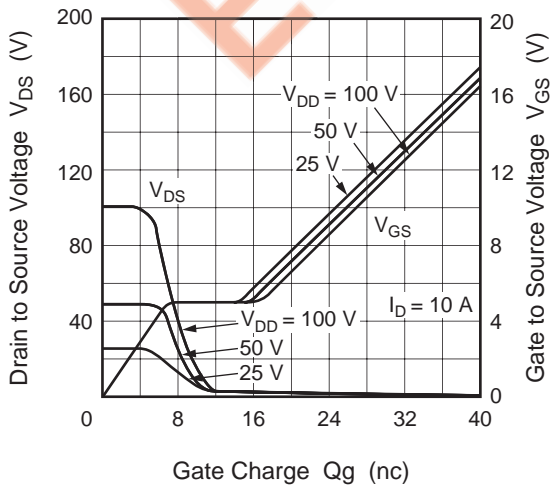
Body to Drain Diode Reverse Recovery Time



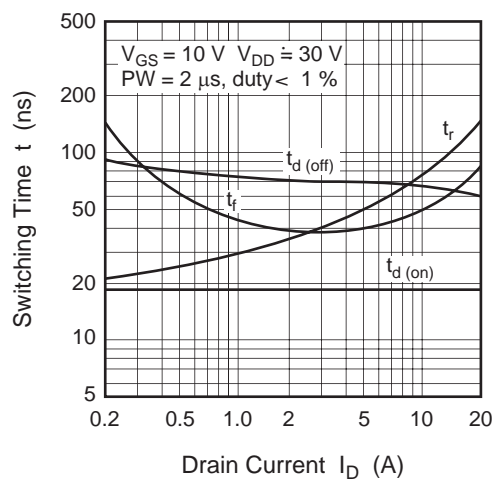
Typical Capacitance vs. Drain to Source Voltage



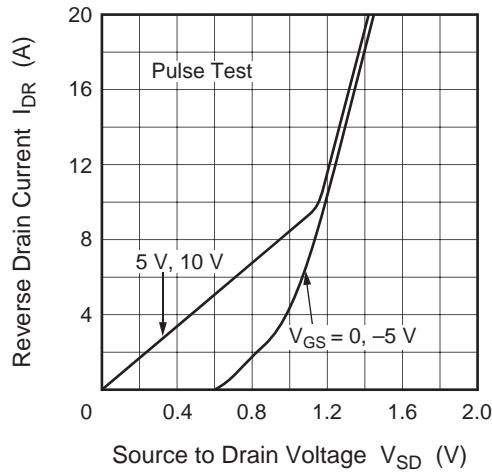
Dynamic Input Characteristics



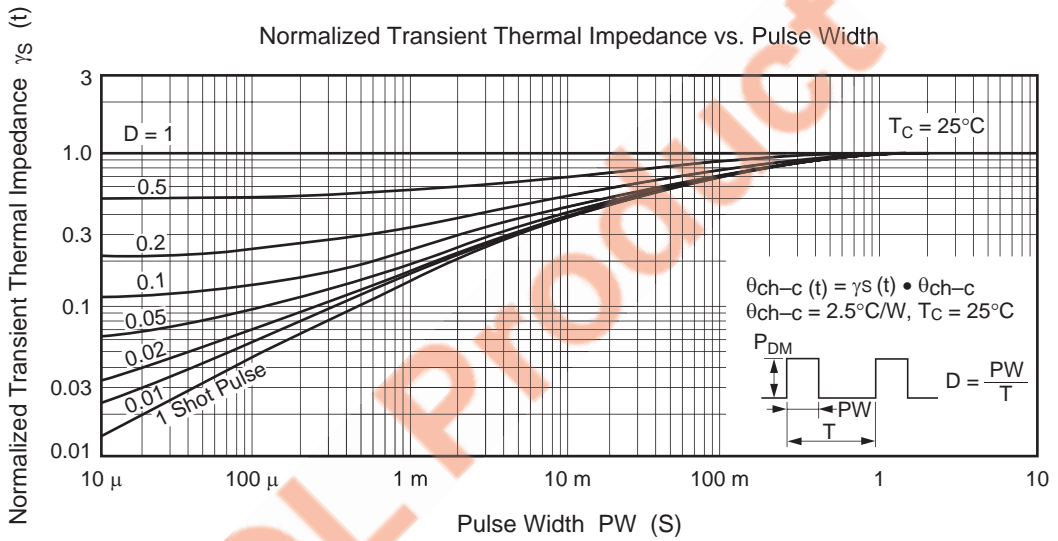
Switching Characteristics



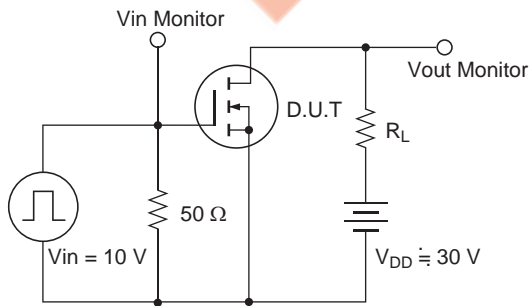
Reverse Drain Current vs. Source to Drain Voltage



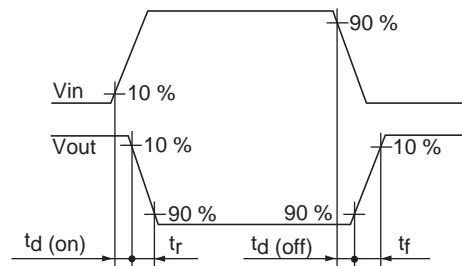
Normalized Transient Thermal Impedance vs. Pulse Width



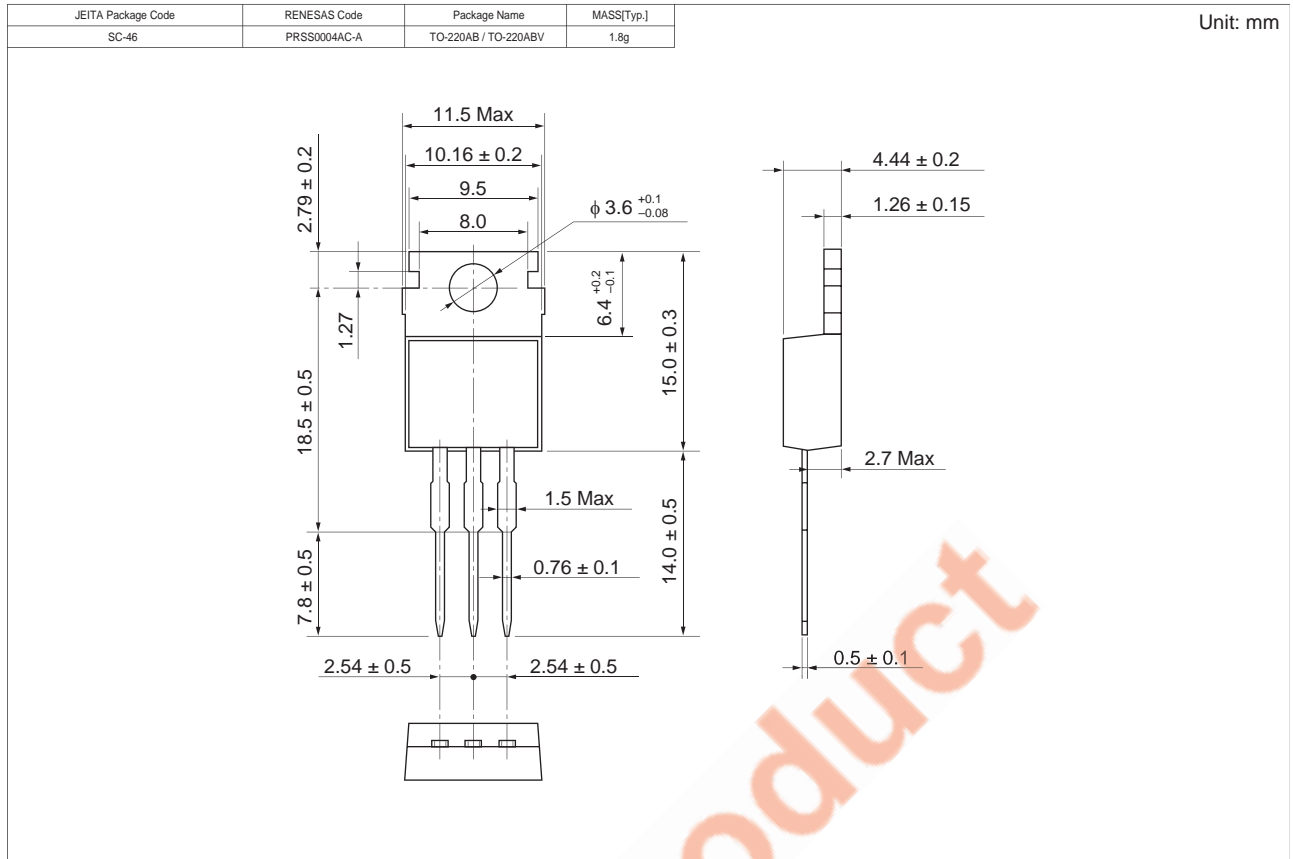
Switching Time Test Circuit



Waveforms



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK740-E	500 pcs	Box (Sack)

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450 Holger Way, San Jose, CA 95134-1368, U.S.A.
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.

Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd.

Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510