TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

# TPD1008SA

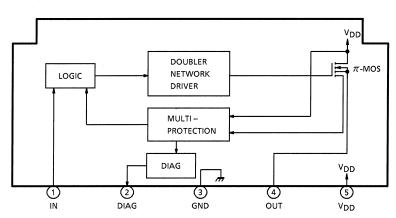
High-side Power Switch for Motors, Solenoids, and Lamp Drivers

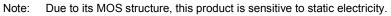
The TPD1008SA is a monolithic power IC for high-side switches. The IC has a vertical MOS FET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The device offers intelligent self-protection and diagnostic functions.

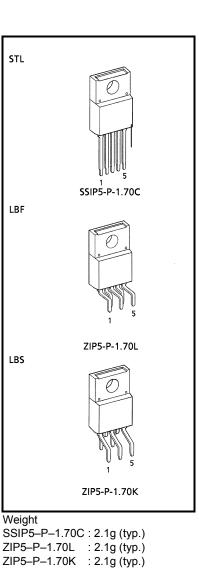
#### Features

- A monolithic power IC with a new structure combining a control block (Bi–CMOS) and a vertical power MOS FET (II–MOS) on a single chip
- One side of load can be grounded to a high-side switch.
- Can directly drive a power load from a microprocessor.
- Built-in protection against thermal shutdown and load short circuiting
- Incorporates a diagnosis function that allows diagnosis output to be read externally at load short-circuiting, opening, or overtemperature.
- Up to -10V of counter-electromotive force from an L load can be applied.
- Low on-resistance :  $R_{DS}(ON) = 200m\Omega (max)$
- Low operating current  $: I_{DD} = 1 \text{mA} (\text{typ.}) (@V_{DD} = 12\text{V}, V_{IN} = 0\text{V})$
- 5-pin TO-220 insulated package
- Three standard lead configurations

#### Pin Assignment

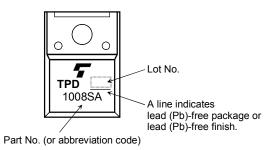




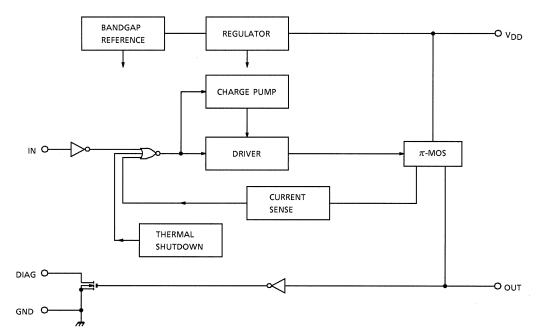


# <u>TOSHIBA</u>

### Marking



### **Block Diagram**

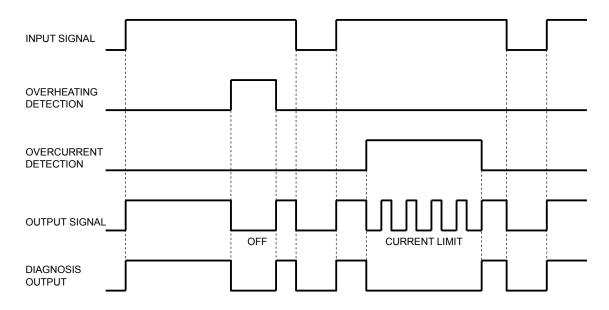


#### **Pin Description**

Pin No.	Symbol	Function
1	IN	Input is CMOS-compatible, with pull-down resistor connected. Even if the input is open, output will not accidentally turn on.
2	DIAG	Self-diagnosis detection pin. Goes low when overheating is detected or when output is short-circuited with input on (high). n-channel open drain.
3	GND	Ground pin.
4	OUT	When the load is short-circuited and current in excess of the detection current flows to the output pin, the output automatically turns on or off.
5	V <sub>DD</sub>	Power pin.

# **TOSHIBA**

## **Timing Chart**



#### **Truth Table**

Input Signal	Output Signal	Diagnosis Output	State
Н	Н	Н	Normal
L	L	L	Normai
Н	L	L	Load short circuited
L	L	L	
Н	Н	Н	Load open
L	Н	Н	
Н	L	L	Quartemperatura
L	L	L	Overtemperature

#### Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit
Drain-source Voltage		V <sub>DS</sub>	60	V
Supply Voltage	DC	V <sub>DD (1)</sub> 25		V
Supply Voltage	Pulse	V <sub>DD (2)</sub>	60 (Rs = 1Ω, τ = 250ms)	V
Input Voltage	DC	V <sub>IN (1)</sub>	-0.5~12	V
input voltage	Pulse	V <sub>IN (2)</sub>	V <sub>DD (1)</sub> + 1.5 (t = 100ms)	V
Diagnosis Output Voltage		VDIAG	-0.5~25	V
Output Current		Ι <sub>Ο</sub>	Internally Limited	А
Input Current		l <sub>IN</sub>	±10	mA
Diagnosis Output Curre	ent	I <sub>DIAG</sub>	5	mA
Power Dissipation	Tc = 25°C	P <sub>D (1)</sub> 30		W
	Ta = 25°C	P <sub>D (2)</sub>	2	W
Operating Temperature	2	T <sub>opr</sub>	-40~110	°C
Junction Temperature		Тј	150	°C
Storage Temperature		T <sub>stg</sub>	-55~150	°C
Lead Temperature/Time	e	T <sub>SOL</sub>	275 (5s), 260 (10s)	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Characteri	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Operating Supply Volta	V <sub>DD (opr)</sub>	—	_	5	12	18	V	
Supply Current	I <sub>DD</sub>	—	V <sub>DD</sub> = 12V, V <sub>IN</sub> = 0V	_	1	5	mA	
Input Voltage		V <sub>IH</sub>	—	V <sub>DD</sub> = 12V, I <sub>O</sub> = 2A	3.5	—		V
		VIL	—	V <sub>DD</sub> = 12V, I <sub>O</sub> = 1.2mA	_	—	1.5	V
Input Current		I <sub>IN (1)</sub>		V <sub>DD</sub> = 12V, V <sub>IN</sub> = 5V	_	50	200	μA
		I <sub>IN (2)</sub>		V <sub>DD</sub> = 12V, V <sub>IN</sub> = 0V	-0.2	—	0.2	μA
On Voltage		V <sub>DS (ON)</sub>	—	$V_{DD}$ = 12V, I <sub>O</sub> = 2A, T <sub>C</sub> = 25°C	—	_	0.4	V
On Resistance		R <sub>DS (ON)</sub>	—	$V_{DD}$ = 12V, I <sub>O</sub> = 2A, T <sub>C</sub> = 25°C	—	_	0.2	Ω
Output Leakage Current		I <sub>OL</sub>	—	V <sub>DD</sub> = 18V, V <sub>IN</sub> = 0V	—	_	1.2	mA
Diagnosis Output Voltage	"L" Level	V <sub>DL</sub>	_	V <sub>DD</sub> = 12V, I <sub>DL</sub> = 2mA	_	_	0.4	V
Diagnosis Output Current	"H" Level	IDH	_	V <sub>DD</sub> = 18V, V <sub>DH</sub> = 18V	_	_	10	μA
Overcurrent Protection		I <sub>S (1)</sub> (Note 1)	1	עסע = 12V, T <sub>C</sub> = 25°C	4	6	8	Α
		I <sub>S (2)</sub> (Note 2)	2	VDD - 12V, 1C - 25 C	4	8	12	Α
Thermal Shutdown	Temperature	Τ <sub>S</sub>		—	150	160	200	°C
i nermai Shutdown	Hysteresis	ΔT <sub>S</sub>		—	—	10	—	°C
Open Detection Resistance		R <sub>ops</sub>	—	V <sub>DD</sub> = 8V	1	20	100	kΩ
Switching Time		t <sub>ON</sub>	3	V <sub>DD</sub> = 12V, R <sub>L</sub> = 5Ω T <sub>C</sub> = 25°C	10	100		μs
		tOFF			10	30	—	μs

#### Electrical Characteristics (T<sub>C</sub> = -40~110°C, V<sub>DD</sub> = 8~18V)

Note 1: Overcurrent detection value when load is short-circuited and  $V_{IN} = "L" \rightarrow "H"$ Note 2: Overcurrent detection value when load current is increased while  $V_{IN} = "H"$ 

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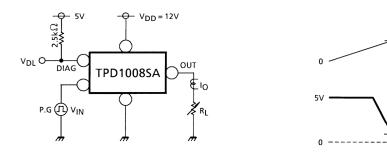
VDL

ls (1)

<0.4V

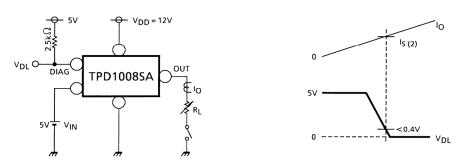
#### Test Circuit 1

#### **Overcurrent Detection**



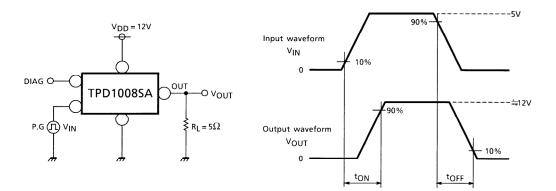
#### **Test Circuit 2**

#### **Overcurrent Detection**

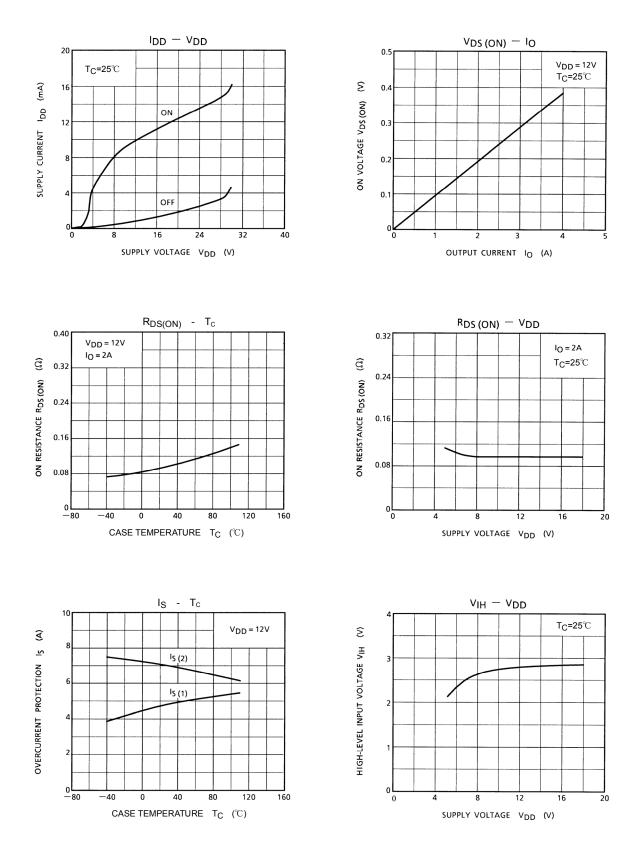


#### Test Circuit 3

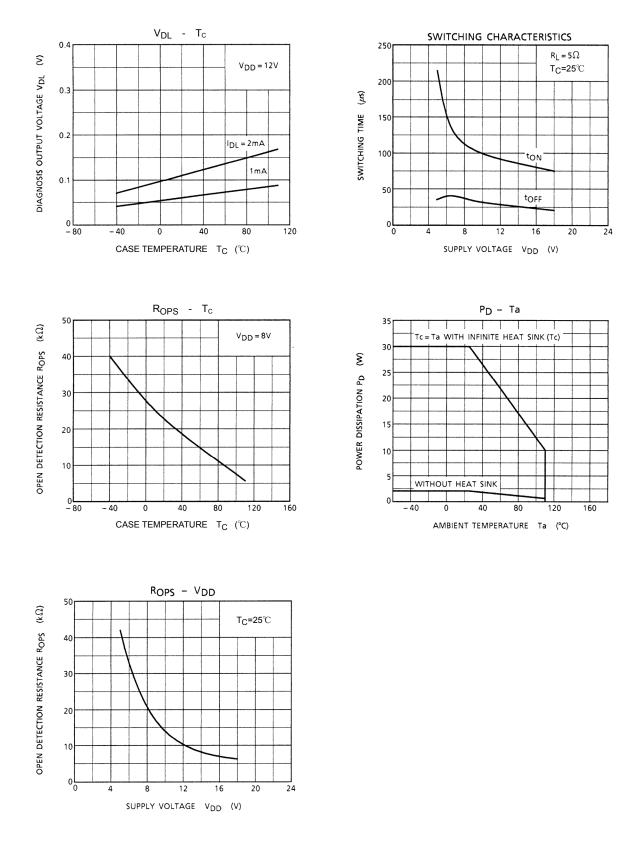
#### **Switching Time**



# **TOSHIBA**



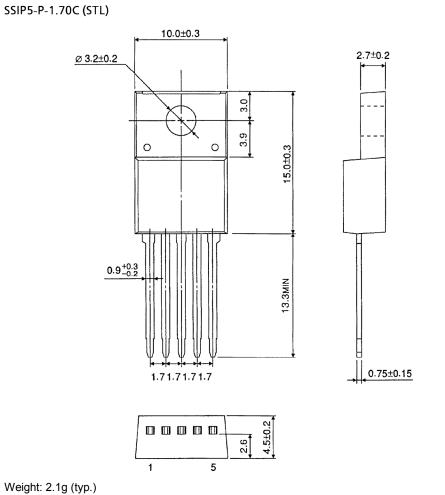
# TOSHIBA



#### Precaution

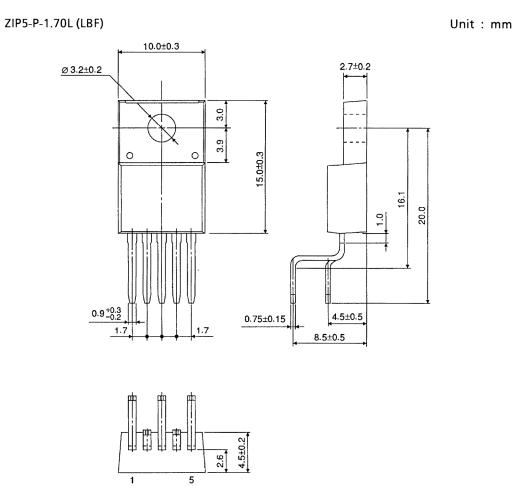
1. Since there is no built-in protection against reverse connection of batteries, etc., provide such protection using external circuits.

### Package Dimensions



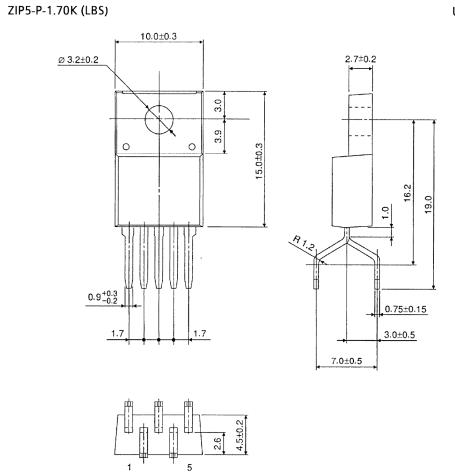
Unit : mm

### Package Dimensions



Weight: 2.1g (typ.)

### Package Dimensions



Weight: 2.1g (typ.)

Unit : mm

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