

NPN SILICON PLASTIC POWER TRANSISTORS

MJD148

DPAK (TO-252) Plastic Package



2. COLLECTOR

3. EMITTER

Designed for General Purpose Amplifier and Low Speed Switching Applications

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	V _{CEO}	45	V
Collector Base Voltage	V _{CBO}	45	V
Emitter Base Voltage	V _{EBO}	5.0	V
Collector Current Continuous	Ι _C	4.0	Α
Peak		7.0	Α
Base Current	I _B	50	mA
Total Power Dissipation at T _c =25 ^o C	P _D	20	W
Derate Above 25°C		0.16	W/ºC
Total Power Dissipation at T _a =25 ^o C	*P _D	1.75	W
Derate Above 25°C		0.014	W/ºC
Operating and Storage Junction Temperature Range	T_{j},T_{stg}	- 55 to +150	°C
THERMAL CHARACTERISTICS			
Junction to Case	R _{th (j-c)}	6.25	°C/W
Junction to Ambient in free air	*R _{th (j-a)}	71.4	°C/W
ELECTRICAL CHARACTERISTICS (T _c =2		fied otherwise)	•
DESCRIPTION	SYMBOL	TEST CONDITION MIN TYP MAX	UNIT

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Emitter Sustaining Voltage	**V _{CEO(sus)}	I _C =100mA, I _B =0	45			V
Collector Cut Off Current	I _{CBO}	V_{CB} =45V, I _E =0			20	μΑ
Emitter Cut Off Current	I _{EBO}	V_{EB} =5V, I _C =0			1.0	mA
DC Current Gain	**h _{FE}	I _C =10mA, V _{CE} =5V	40			
		I _C =0.5A, V _{CE} =1V	85		375	
		I _C =2A, V _{CE} =1V	50			
		I _C =3A, V _{CE} =1V	30			

*These rating are applicable when surface mounted on the minimum pad sizes recommended

**Pulse Test:- Pulse Width \leq 300ms, Duty Cycle \leq 2%



- 3. EMITTER

ELECTRICAL CHARACTERISTICS (T_c=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Emitter Saturation Voltage	**V _{CE (sat)}	I _C =2A, I _B =0.2A			0.5	V
Base Emitter On Voltage	**V _{BE (on)}	I _C =2A, V _{CE} =1V			1.1	V

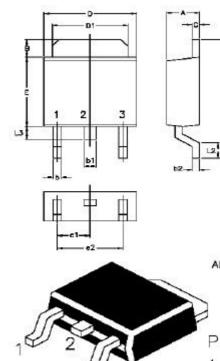
DYNAMIC CHARACTERISTICS

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Current Gain Bandwidth Product	f _T	I_{C} =250mA, V_{CE} =1V, f=1MHz	3			MHz

MARKING	CDIL	
	MJD148	
	ΧΥ ΜΧ	
XY= Date Code		

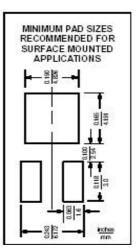
**Pulse Test:- Pulse Width < 300ms, Duty Cycle < 2%

DPAK PACKAGE OUTLINE DIMENSIONS



3

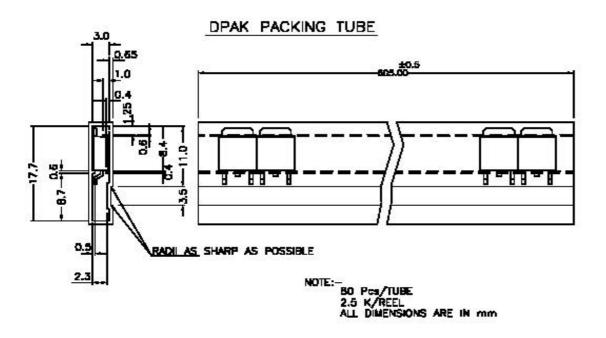
DIM	MIN.	MAX.
A	2.20	2.40
в	1.30	1.50
b	0.55	0.65
ь1	0.75	0.85
b2	0.46	0.56
С	0.46	0.56
D	6.40	6.60
D1	5.20	5.40
E	5.40	5.60
e1	2.25	2.35
e2	4.50	4.70
L1	9.25	9.75
LZ	0.5	-
L3	0.90	1.10



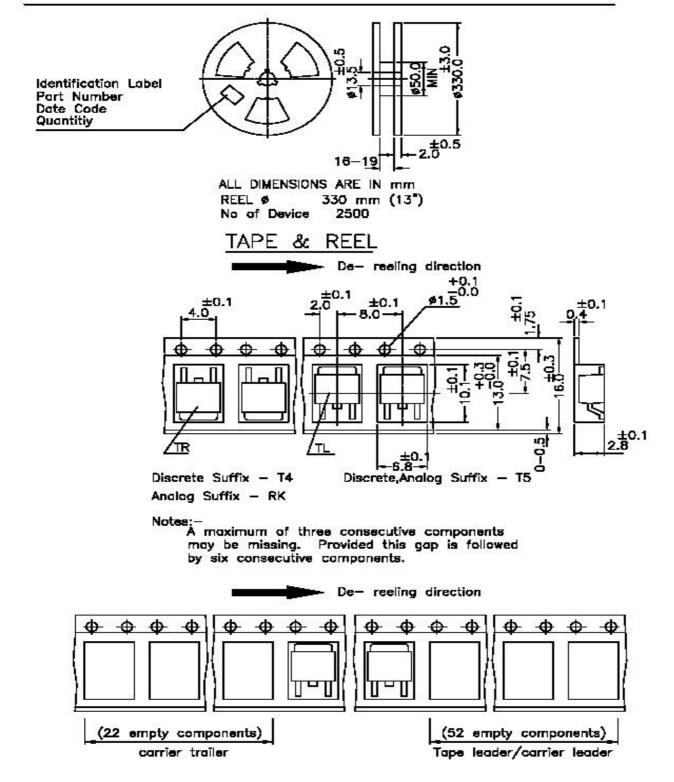


PIN CONFIGURATION

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER



DPAK TAPE & REEL SPECIFICATION



Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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