



SPP7407

P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP7407 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where high-side switching , and low in-line power loss are needed in a very small outline surface mount package.

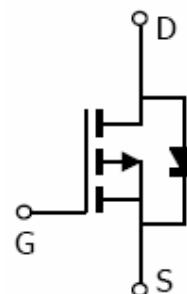
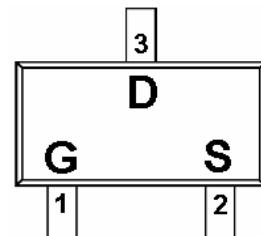
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

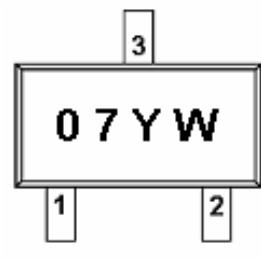
FEATURES

- ◆ -20V/-3.4A,R_{DS(ON)}= 100mΩ@V_{GS}=-4.5V
- ◆ -20V/-2.4A,R_{DS(ON)}= 125mΩ@V_{GS}=-2.5V
- ◆ -20V/-1.7A,R_{DS(ON)}= 150mΩ@V_{GS}=-1.8V
- ◆ -20V/-1.0A,R_{DS(ON)}= 220mΩ@V_{GS}=-1.25V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-323 (SC-70) package design

PIN CONFIGURATION (SOT-323 ; SC-70)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP7407S32RG	SOT-323	07YW
SPP7407S32RGB	SOT-323	07YW

- ※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)
- ※ SPP7407S32RG : Tape Reel ; Pb – Free
- ※ SPP7407S32RGB : Tape Reel ; Pb – Free ; Halogen -Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	TA=25°C	-2.3	A
	TA=70°C		
Pulsed Drain Current	I _{DM}	-6	A
Continuous Source Current(Diode Conduction)	I _S	-1.4	A
Power Dissipation	TA=25°C	0.33	W
	TA=70°C		
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	105	°C/W



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ELECTRICAL CHARACTERISTICS

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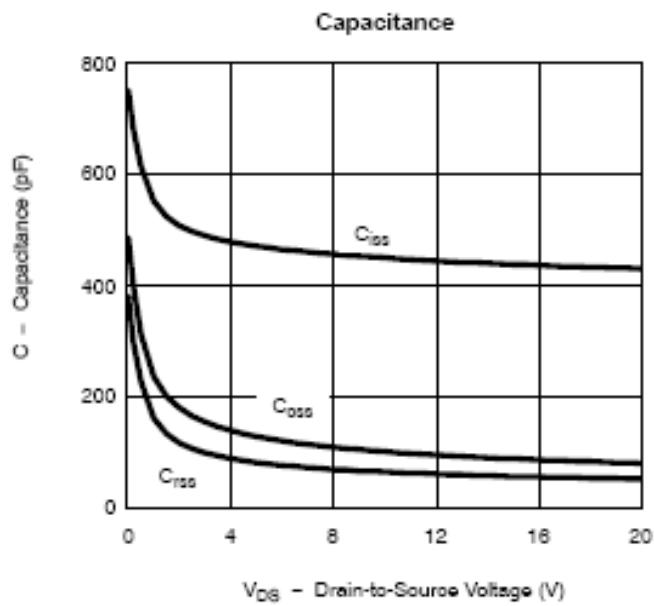
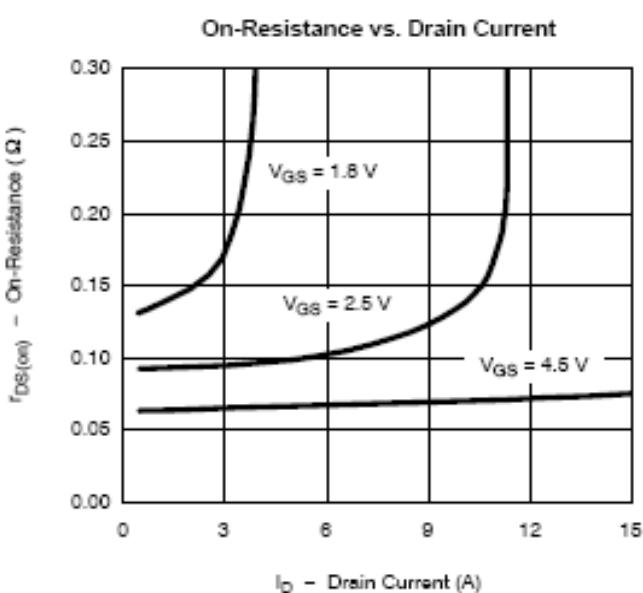
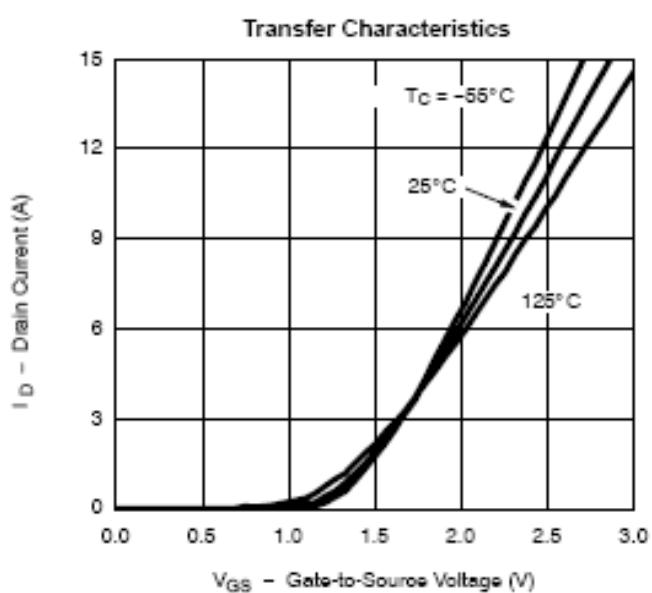
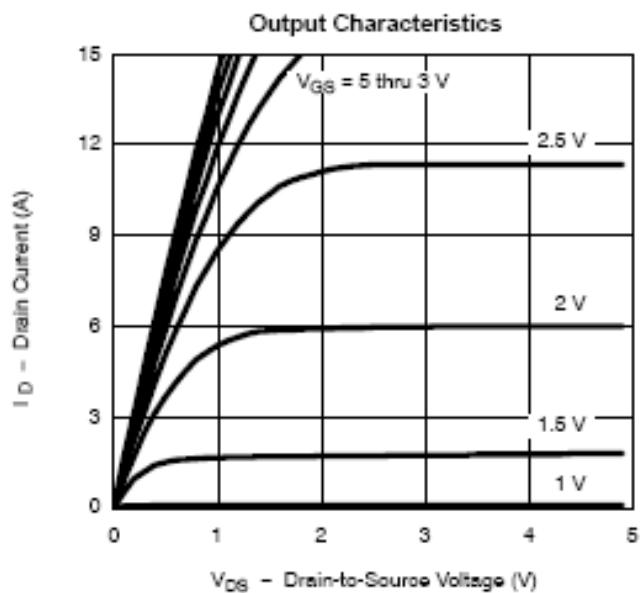
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-20			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=-250uA	-0.35		-0.8	
Gate Leakage Current	IGSS	VDS=0V, VGS=±12V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=-20V, VGS=0V			-1	uA
		VDS=-20V, VGS=0V TJ=55°C			-5	
On-State Drain Current	ID(on)	VDS≤-5V, VGS=-4.5V	-6			A
Drain-Source On-Resistance	RDS(on)	VGS=-4.5V, ID=-3.4A		0.080	0.100	Ω
		VGS=-2.5V, ID=-2.4A		0.105	0.125	
		VGS=-1.8V, ID=-1.7A		0.130	0.150	
		VGS=-1.25V, ID=-1.0A		0.180	0.22	
Forward Transconductance	gfs	VDS=-5V, ID=-2.8A		6		S
Diode Forward Voltage	VSD	IS=-1.5A, VGS=0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Qg	VDS=-6V, VGS=-4.5V ID=-2.8A		4.8	8	nC
Gate-Source Charge	Qgs			1.0		
Gate-Drain Charge	Qgd			1.0		
Input Capacitance	Ciss	VDS=-6V, VGS=0V f=1MHz		485		pF
Output Capacitance	Coss			85		
Reverse Transfer Capacitance	Crss			40		
Turn-On Time	td(on)	VDD=-6V, RL=6Ω ID=-1.0A, VGEN=-4.5V RG=6Ω		10	16	ns
	tr			13	23	
Turn-Off Time	td(off)			18	25	
	tf			15	20	



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TYPICAL CHARACTERISTICS

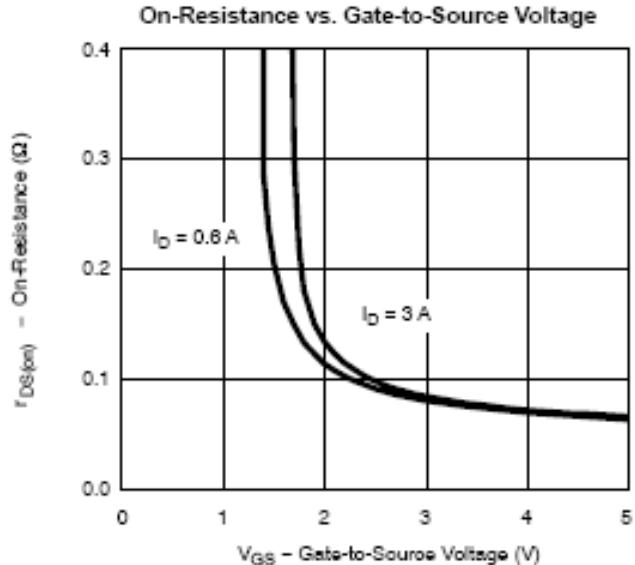
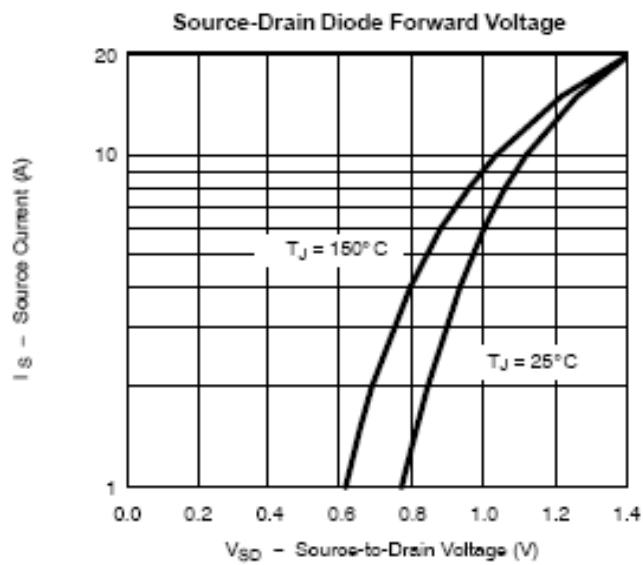
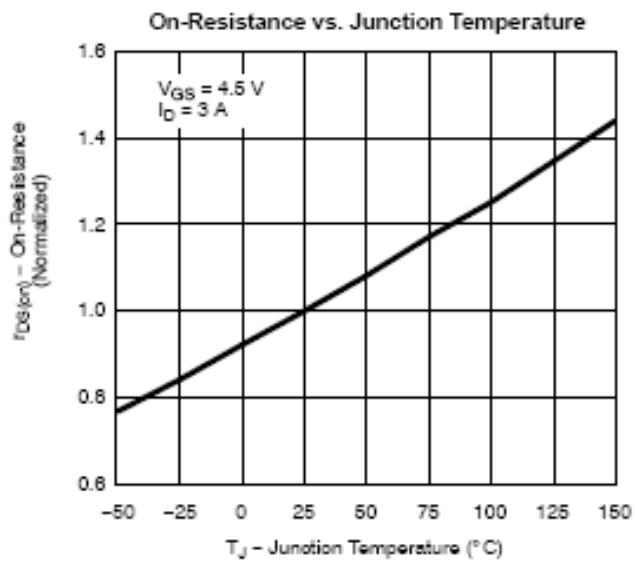
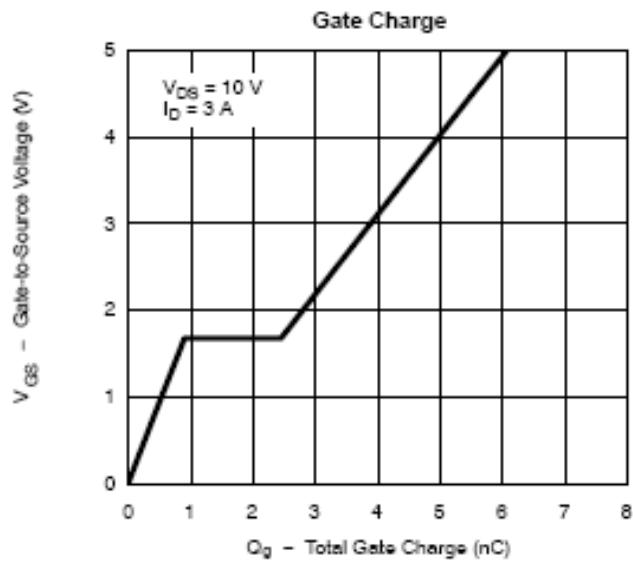




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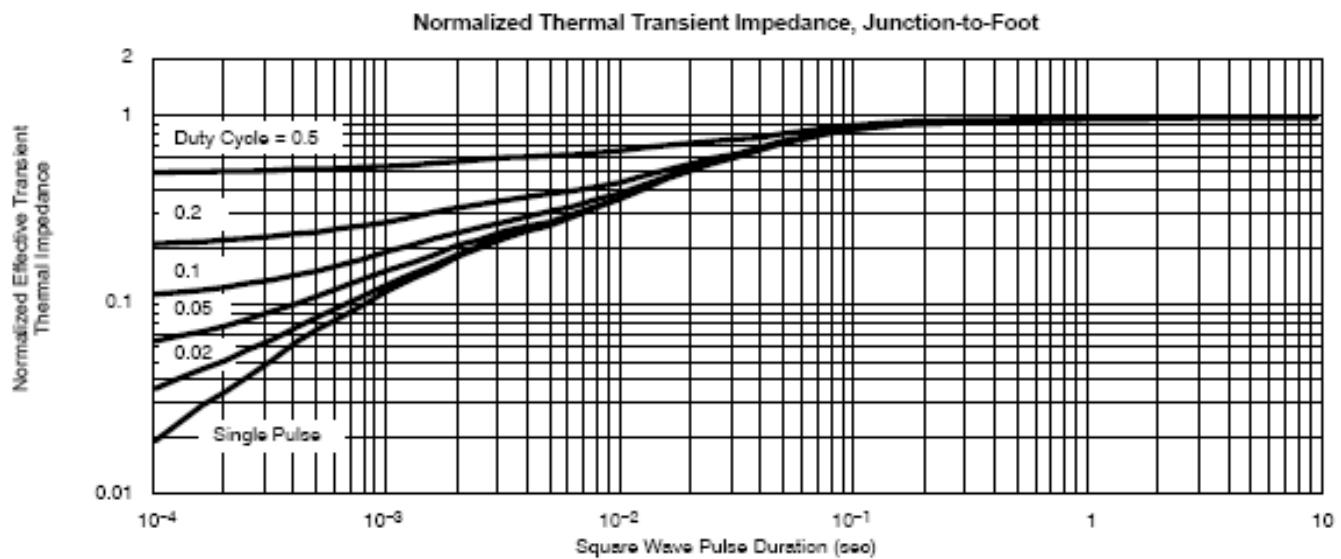
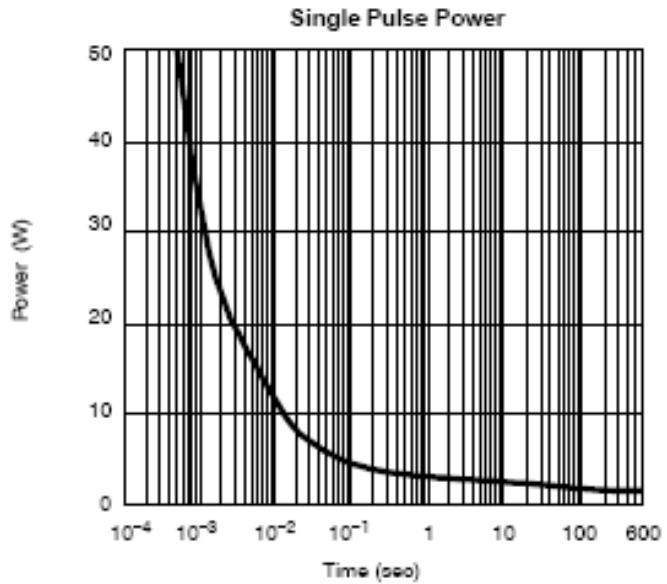
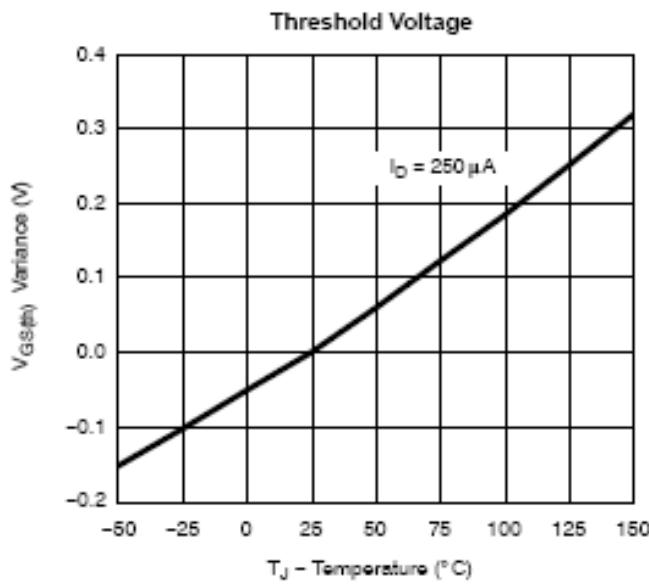




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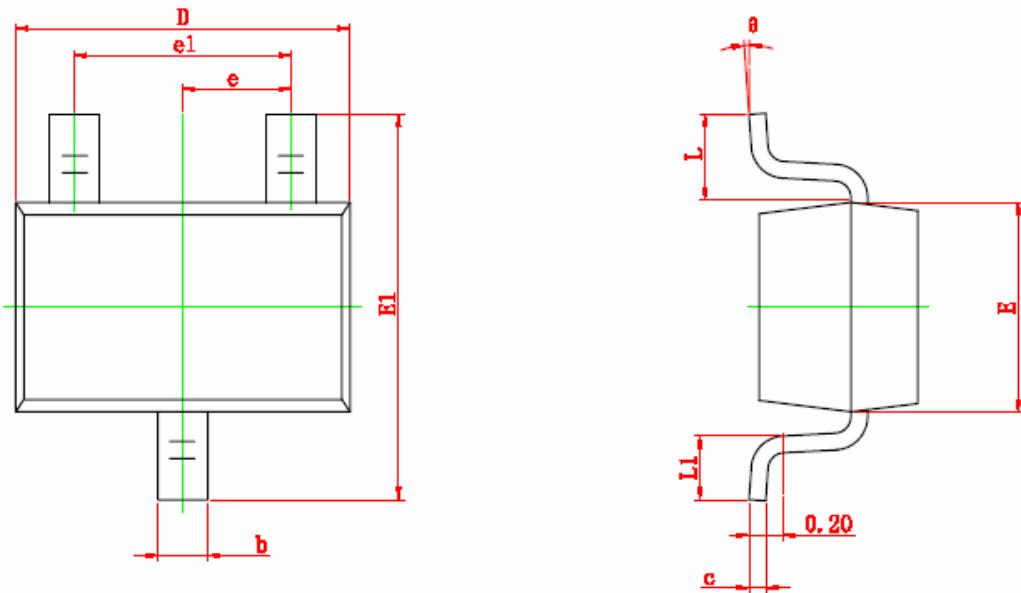




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SOT-323 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°



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