

-100mA/-50V Digital transistors(with built-in resistors)

DTA024EM / DTA024EEB / DTA024EUB

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors. (See equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

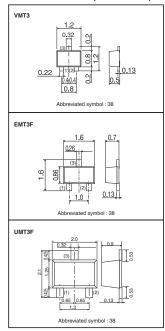
Applications

Inverter, Interface, Driver

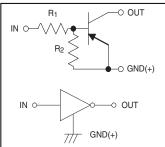
Packaging specifications

	Package	VMT3	EMT3F	UMT3F
	Packaging Type	Taping	Taping	Taping
Туре	Code	T2L	TL	TL
	Basic ordering unit (pieces)	8000	3000	3000
DTA024EM		0	-	-
DTA024EEB		-	0	-
DTA024EUB		-	-	0

•Dimensions (Unit : mm)



Equivalent circuit



 $R_1=R_2=22k\Omega$

•Absolute maximum (Ta=25°C)

Parameter	Symbol	Limits(I	Unit	
		M E	3 UB	Offic
Supply voltage	V _{CC}		V	
	V _{IN}		V	
Input voltage	♥ IN		V	
Collector current *1	I _{C(max)}		mA	
Output current	Ι _ο		mA	
Power dissipation *2	PD	150	200	mW
Junction temperature	Tj		٥°C	
Range of storage temperature	Tstg	-55 to +150		٥C

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land

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•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Input voltage	V _{I(off)}	-	-	-0.5	V	V _{CC} =-5V / I _O =-100uA
input voltage	V _{I(on)}	-3.0	-	-	V	V _O =-0.3V / I _O =-5mA
Output voltage	V _{O(on)}	-	-0.07	-0.15	V	I _O =-5mA / I _I =-0.5mA
Input current	I _I	-	-	-0.36	mA	V _I =-5V
Output current	I _{O(off)}	-	-	-500	nA	V _{CC} =-50V / V _I =0V
DC current gain	G _I	60	-	-	-	V _o =-10V / I _o =-5mA
Transition frequency *	f _T	-	250	-	MHz	V _{CE} =-10V /I _E =5mA f=100MHz
Input resistance	R ₁	15.4	22	28.6	kΩ	
Resistance ratio	R ₂ /R ₁	0.8	1.0	1.2	-	

* Characteristics of built-in transistor

•Electrical characteristics curves

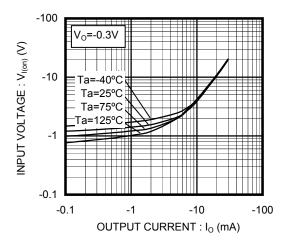


Fig.1 Input Voltage vs. Output Current (ON characteristics)

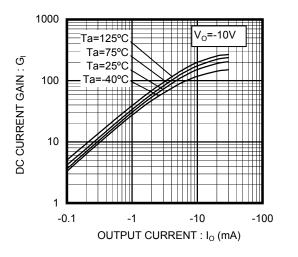


Fig.3 DC Current Gain vs. Output Current

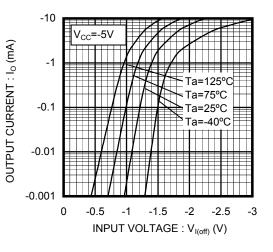


Fig.2 Input Voltage vs. Output Current (OFF characteristics)

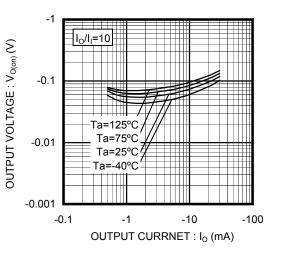


Fig.4 Output Voltage vs. Output Current

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