



An ISO/TS 16949, $\,$ ISO 9001 and ISO 14001 Certified Company

PNP COMPLEMENTARY PLANAR SILICON TRANSISTORS



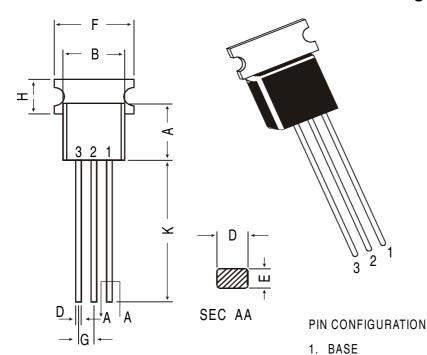
CIL1020Y (BPL) TO-237 BCE

ABSOLUTE MAXIMUM RATINGS(Ta=25deg C unless otherwise specified)

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector -Base Voltage	VCBO	60	V
Collector -Emitter Voltage	VCEO	60	V
Emitter Base Voltage	VEBO	6	V
Collector Current	IC	3	Α
Power Dissipation Ta=25 deg C	PC	900	mW
Operating And Storage Junction	Tj, Tstg	-55 to +150	deg C
Temperature Range	<i>y</i> . 3		J

Operating And Storage Junction	Ij, Istg	•	-55 to +150			deg C	
Temperature Range							
ELECTRICAL CHARACTERISTICS (Ta=25 deg C Unless Otherwise Specified)							
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT	
Collector -Emitter Voltage	VCEO	IC=10mA, IB=0	50	-	-	V	
Collector Cut off Current	ICBO	VCB=50V, IE=0	-	-	1.0	uA	
Emitrer Cut off Current	IEBO	VEB=5V, IC=0	-	-	1.0	uA	
DC Current Gain	hFE	IC=100mA,VCE=2V	100	-	560		
		IC=1.5A,VCE=2V	40	-	-		
Collector Emitter Saturation Voltage	VCE(Sat)	IC=1A, IB=50mA	-	-	0.50	V	
Base Emitter Saturation Voltage	VBE(Sat)	IC=1A, IB=50mA	-	-	1.2	V	
Dynamic Characteristics							
Gain Bandwidth Product	ft	VCE=10V,IC=50mA	-	150	-	MHz	
Output Capacitance	Cob	VCB=10V, IE=0 f=1MHz	-	40	-	pF	
Switching Time							
Turn on Time	ton	VCC=30V, IB1=1B2	-	0.1		us	
Storage Time	tstg	=50mA, RL=30 ohms		1.0		us	
Fall Time	tf	Duty Cycle=1%		0.1		us	

TO-237 Plastic Package



	DIM	MIN.	MAX.		
	Α	4.32	5.33		
	В	4.45	5.20		
	C	3.18	4.19		
All diminsions in mm.	D	0.41	0.55		
	Е	0.35	0.50		
	F	I	5.40		
	G	1.14	1.40		
	Ι	1	2.54		
	K	12.70	_		
	L	5 DEG			
	J	1.14	1.53		

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-237 Bulk TO-237 T&A	1K/polybag 2K/ammo box	240 gm/1K pcs 725 gm/2K pcs	3" x 7.5" x 7.5" 12.5" x 8" x 1.8"		17" x 15" x 13.5" 17" x 15" x 13.5"	80.0K 32.0K	26.2 kgs 13.8 kgs

COLLECTOR
 EMITTER

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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