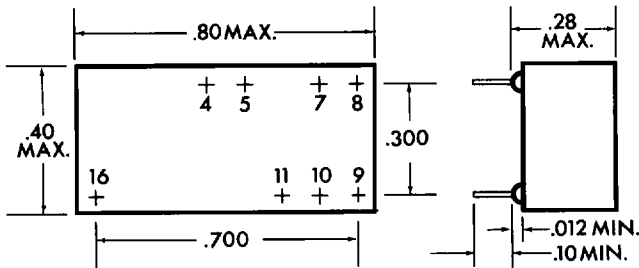




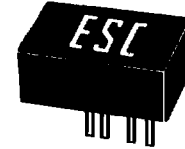
# TTL DIGITAL PROGRAMMABLE DELAY LINES 16 PIN PACKAGE 3 BIT

## SERIES PTT3

Also available as Series PTTD3 (Auto-Insertable)

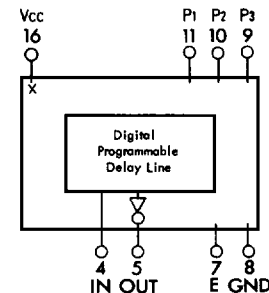


White Dot locates Pin 1



Model No.	Zero Step Delay (ns)	Maximum Delay (ns) (nom)	Delay per step (ns)
PTT3-1	7 ± 2	14	1 ± .5
PTT3-2		21	2 ± .5
PTT3-3		28	3 ± .6
PTT3-4		35	4 ± .8
PTT3-5		42	5 ± 1
PTT3-6		49	6 ± 1
PTT3-7		56	7 ± 1
PTT3-8		63	8 ± 1
PTT3-9		70	9 ± 1
PTT3-10		77	10 ± 1

Modules available in delay/steps up to 50ns. Contact plant.



TYPICAL TRUTH TABLE EXAMPLE:										
Model Number	Data Select Pins	P3-9	L	L	L	L	H	H	H	H
		P2-10	L	L	H	H	L	L	H	H
P1-11	L	H	L	H	L	H	L	L	H	
PTT3-5	—	7	5	10	15	20	25	30	35	

### SPECIFICATIONS:

- Zero step delay is measured from the input pin #4
- All programmable delays after step zero are referenced to step zero
- Programmable delay tolerances: ± 2ns or ± 5% whichever is greater
- Rise Time: 4ns max
- \* ● Operating temp. range: 0°C to +70°C
- Temp. coeff. of delay: 1.0ns + 500ppm/°C
- Vol: .4V max
- Voh: 2.4V min
- Vcc: 5VDC ± 0.25V
- Icc: 85mA
- Minimum Pulse Width: 40% of Total Delay
- Maximum Duty Cycle: 50%
- Terminals: Electro tin plated alloy 42, .020w x .010th

\* Available for -55°C to +125°C operation. Specify PTT3 series.  
The dimensions will increase to .825L x .360H.

### TEST CONDITIONS:

- Vcc = 5.0VDC, Temp. 25°C
- Enable is grounded (0vohs)
- Time delay measured at the 1.5 volt level of the leading edge
- Rise time measured from .75V to 2.4 volts
- Output loaded with 15pf
- Input pulse width: 3 x max time delay
- Pulse amplitude: 3.0 volts
- Pulse spacing: 1000ns