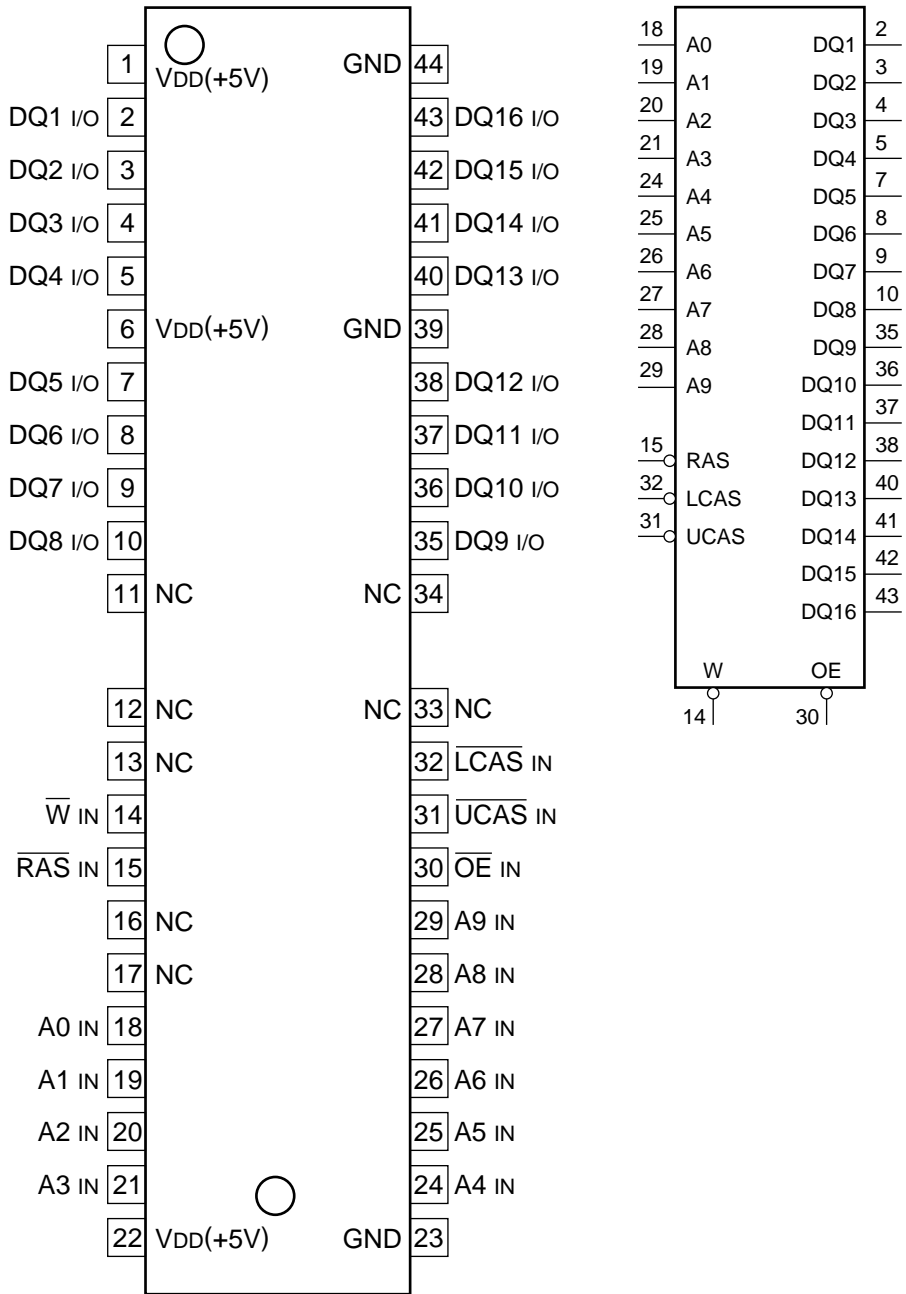


\*\*\*\*\*

C-MOS 1M 16-BIT DYNAMIC RAM WITH FAST PAGE MODE  
-TOP VIEW-



- A0-A9 ; ADDRESS INPUTS
- DQ1-DQ16 ; DATA INPUTS/OUTPUTS
- LCAS ; LOWER COLUMN ADDRESS STROBE INPUT
- OE ; DATA OUTPUT ENABLE INPUT
- RAS ; ROW ADDRESS STROBE INPUT
- UCAS ; UPPER COLUMN ADDRESS STROBE INPUT
- W ; READ/WRITE INPUT

RAS	LCAS	UCAS	W	OE	DQ1-DQ8	DQ9-DQ16	STATE
1	X	X	X	X	HI-Z	HI-Z	STANDBY
0	1	1	X	X	HI-Z	HI-Z	REFRESH
0	0	1	1	0	DQ-OUT	HI-Z	LOWER BYTE READ
0	1	0	1	0	HI-Z	DQ-OUT	UPPER BYTE READ
0	0	0	1	0	DQ-OUT	DQ-OUT	WORD READ
0	0	1	0	1	DQ IN	—	LOWER BYTE WRITE
0	1	0	0	1	—	DQ-IN	UPPER BYTE WRITE
0	0	0	0	1	DQ-IN	DQ-IN	WORD WRITE
0	0	0	1	1	HI-Z	HI-Z	—

0 ; LOW LEVEL  
 1 ; HIGH LEVEL  
 X ; DON'T CARE  
 HI-Z ; HIGH IMPEDANCE

