



### **General Description**

The AOZ6134 is a high performance single-pole double-throw (SPDT), low power, TTL-compatible bus switch.

The AOZ6134 will accept analog and digital signals. Signals with voltages up to  $V_{CC}$  (1.65 V to 5.5 V) can be transmitted in either direction.

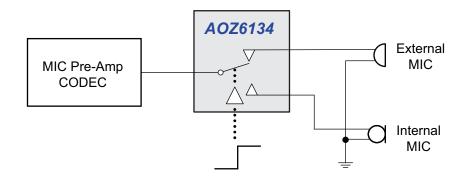
When the Select pin is LOW,  $B_0$  is connected to the output A pin. When the Select pin is HIGH,  $B_1$  is connected to the output A pin. The path that is open will have a high-impedance state with respect to the output. Break-before-make is guaranteed.

### Features

- DFN 1.2 mm x 1.0 mm x 0.55 mm 6-Lead Package
- 1.65 V to 5.5 V V<sub>CC</sub> operation
- 1 Ω connection between ports
- Break-before-make switching



## Typical Application





## **Ordering Information**

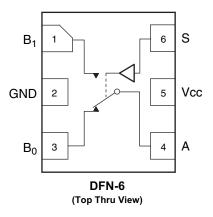
Part Number	umber Ambient Temperature Range Package		Environmental		
AOZ6134DI	-40 °C to +85 °C	DFN 1.2 mm x 1.0 mm, 6L	RoHS Compliant Green Product		



AOS Green Products use reduced levels of Halogens, and are also RoHS compliant.

Please visit www.aosmd.com/web/quality/rohs\_compliant.jsp for additional information.

# **Pin Configuration**



## **Truth Table**

Logic S Input	Function		
0	B <sub>0</sub> Connected to A		
1	B <sub>1</sub> Connected to A		



## **Absolute Maximum Ratings**

Exceeding the Absolute Maximum ratings may damage the device.

Symbol	Parameter	Rating			
V <sub>CC</sub>	Supply Voltage	-0.5 V to +6 V			
V <sub>S</sub>	Switch Voltage <sup>(1)</sup>	-0.5 V to $V_{CC}$			
V <sub>IN</sub>	Input Voltage <sup>(1)</sup>	-0.5 V to $V_{CC}$			
I <sub>IK</sub>	Minimum Input Diode Current <sup>(2)</sup>	-50 mA			
I <sub>SW</sub>	Switch Current	200 mA			
I <sub>SWPEAK</sub>	Peak Switch Current (Pulsed at 1 ms, < 10% Duty Cycle)	400 mA			
T <sub>STG</sub>	Storage Temperature Range	-65 °C to +150 °C			
TJ	Maximum Junction Temperature	+150 °C			
PD	DFN-6 Power Dissipation at 85 °C <sup>(3)</sup>	560 mW			
ESD	Human Body Model (JESD22A-114E)	8000 V			

### **Recommend Operating Ratings**

The device is not guaranteed to operate beyond the Maximum Operating Ratings.

Symbol	Parameter	Rating		
V <sub>CC</sub>	Supply Voltage	1.65 V to +5.5 V		
V <sub>IN</sub>	Control Input Voltage <sup>(4)</sup>	0 V to V <sub>CC</sub>		
V <sub>SW</sub>	Switch Input Voltage	0 V to V <sub>CC</sub>		
T <sub>A</sub>	Operating Temperature	-40 °C to +85 °C		

Notes:

1. Signals on A, or B or S exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

2. Negative current should not exceed minimum negative value.

3. All leads welded or soldered to PC Board.

4. Unused inputs must be held HIGH or LOW. They may not float.



# **Electrical Characteristics**

Unless otherwise indicated, specifications indicate a temperature range of -40 °C to +85 °C. All typical values are at 25 °C unless otherwise specified.

Symbol	Parameter	Test Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units
DC CHAF	RACTERISTICS			1	<u> </u>	1	
V <sub>IH</sub>	Input Voltage High		1.65 to 2.7	1.0			V
			2.7 to 3.6	1.5			
			4.5 to 5.5	2.0			
V <sub>IL</sub>	Input Voltage Low		1.65 to 2.7			0.4	V
			2.7 to 3.6			0.6	
			4.5 to 5.5			0.8	
R <sub>ON</sub>	On Resistance	I <sub>OUT</sub> =100 mA, B0 or B1 = 0 V, 1.5 V, 2.5 V, 3.5 V, 3.75 V, or 4.5 V	4.5		0.6	1.2	Ω
		l <sub>OUT</sub> = 100 mA, B0 or B1 = 0 V, 1.5 V, 2 V, or 2.7 V	2.7		1.0	1.5	
		I <sub>OUT</sub> =100mA, B0 or B1 = 0 V, 1.25 V, 1.5 V, or 1.8 V	1.8		3.0	6.0	
R <sub>FLAT</sub>	On Resistance Flatness	I <sub>OUT</sub> =100 mA,	4.5		0.2		Ω
		B0 or B1 = 0 V to $V_{CC}$	2.7		0.4		
			1.8		2.5		
$\Delta R_{ON}$	On Resistance Matching Between Channels	I <sub>OUT</sub> =100 mA, B0 or B1 = 1.5 V	4.5		0.03	0.15	Ω
		I <sub>OUT</sub> =100 mA, B0 or B1 = 3.5 V					
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V or V <sub>CC</sub>	1.95 to 5.5			±1.0	μA
I <sub>B(off)</sub>	Off Stage Switch Leakage	A = 1 V, 4.5 V, B0 or B1 = 4.5 V, 1 V	1.95 to 5.5			±30	nA
I <sub>A(on)</sub>	On State Switch Leakage	A = 1 V, 4.5 V, B0 or B1 = 4.5 V, 1 V or floating	1.95 to 5.5			±40	nA
POWER S	SUPPLY						
V <sub>CC</sub>	Power Supply Range		1.65 to 5.5	1.65		5.5	V
ICCQ	Quiescent Supply Current	$V_{IN} = 0 V \text{ or } V_{CC}, I_{OUT} = 0 V$	5.5			0.5	μA
I <sub>CCT</sub>	Increase in I <sub>CC</sub>	V <sub>IN</sub> = 1.8 V	5.5		30	40	μA
	per Input	V <sub>IN</sub> = 2.6 V			18	25	]



## Electrical Characteristics (Continued)

Unless otherwise indicated, specifications indicate a temperature range of -40 °C to +85 °C. All typical values are at 25 °C unless otherwise specified.

Symbol	Parameter	Test Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units
AC CHAF	RACTERISTICS		I			1	
t <sub>ON</sub>	Turn-On Time	$B_0$ or $B_1$ = 1.5 V, $R_L$ = 50 Ω, $C_L$ = 35 pF	2.7 to 3.6			70 65	ns
			4.5 to 5.5			55 55	
t <sub>OFF</sub>	Turn-Off Time	$B_0 \text{ or } B_1 = 1.5 \text{ V}, R_L = 50 \Omega,$ $C_L = 35 \text{ pF}$	2.7 to 3.6			50 45	ns
			4.5 to 5.5			45 45	
t <sub>BBM</sub>	Break-Before-Make		1.65 to 1.95		20		ns
	Time		2.3 to 2.7		15		
			3.0 to 3.65		10		
			4.5 to 5.5		10		
Q	Charge Injection <sup>(2)</sup>	$C_L$ = 1.0 nF, $V_{GE}$ = 0 V, $R_{GEN}$ = 0 $\Omega$	4.5 to 5.5		90		рС
			2.7 to 3.6		50		
ANALOG	SWITCH CHARACTER	ISTICS <sup>(2)</sup>					•
OIRR	Off Isolation	$R_L = 50 \Omega$ , f = 1 MHz	2.7 to 5.5		-60		dB
X <sub>TALK</sub>	Crosstalk	$R_L = 50 \Omega$ , f = 1 MHz	2.7 to 5.5		-60		dB
BW	-3 dB Bandwidth	$R_L = 50 \Omega$	2.7 to 5.5		180		MHz
THD	Total Harmonic Distortion	$V_{IN} = 2 V_{pk-pk}$ , f = 20 Hz to 20 kHz	2.7 to 5.5		0.002		%
CAPACIT	ANCE <sup>(3)</sup>						-
C <sub>IN</sub>	Control Pin Capacitance	f = 1 MHz, Vbias = 1.5 V	4.5		3.0		pF
C <sub>OFF</sub>	B Port Off Capacitance	f = 1 MHz, Vbias = 1.5 V	4.5		7.0		pF
C <sub>ON</sub>	A Port Capacitance When Switch Enable	f = 1 MHz, Vbias = 1.5 V	4.5		40.0		pF

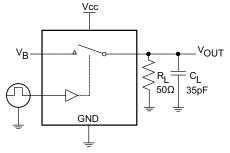
#### Notes:

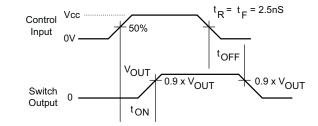
1. Typical values are for design aid only, not guaranteed nor subject to production testing.

2.  $T_A$  = +25 °C, parameters are characterized but not tested in production and guaranteed by design.

3. T<sub>A</sub> = +25 °C, f = 1 MHz, capacitance is characterized but not tested in production.

# AC Loading and Waveforms





Logic input waveform are inverted for switches with opposite

logic sense

CL Includes Fixture and Stray Capacitance

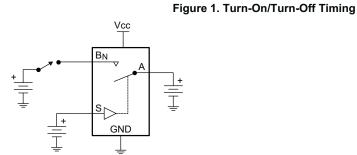


Figure 2. Off State Leakage Current

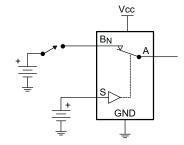
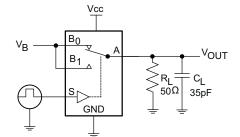


Figure 3. On State Leakage Current



C<sub>L</sub> Includes Fixture and Stray Capacitance

#### Figure 4. Break-Before-Make Timing

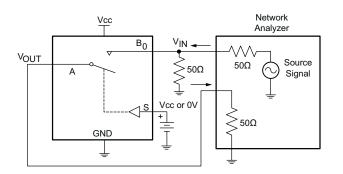
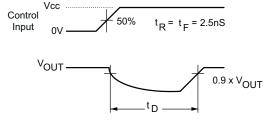


Figure 5. Off Isolation



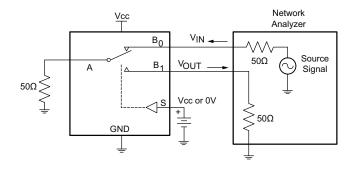


Figure 6. Crosstalk



# AC Loading and Waveforms (Continued)

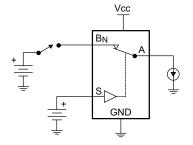
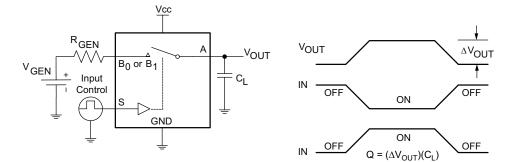


Figure 7. On State Resistance





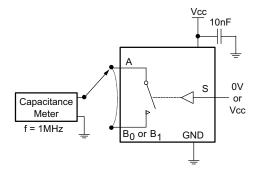


Figure 9. ON/Off Capacitance Measurement

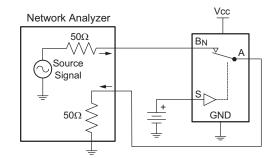


Figure 10. Bandwidth

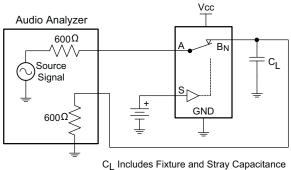
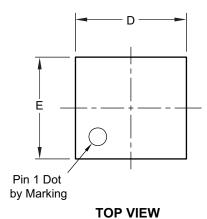
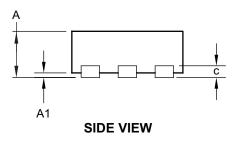


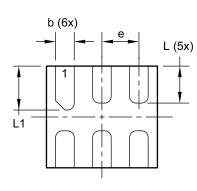
Figure 11. Harmonic Distortion



# Package Dimensions, DFN 1.2 mm x 1.0 mm, 6L

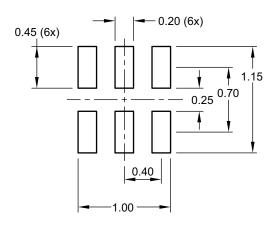






**BOTTOM VIEW** 

#### **RECOMMENDED LAND PATTERN**



Dimensions in millimeters					<b>Dimensions in inches</b>			
Symbols	Min.	Nom.	Max.		Symbols	Min.	Nom.	Max.
A	0.50	0.55	0.60		А	0.020	0.022	0.024
A1	0.00	_	0.05		A1	0.000	—	0.002
b	0.15	0.20	0.25	1 [	b	0.006	0.008	0.010
С	0.152 Ref.				С	0.006 Ref.		
D	1.05		1.25		D	0.045	0.047	0.049
E	0.95		1.15		E	0.041	0.043	0.045
е	0.40 BSC				е	0	.016 BS	C
L	0.30	0.40	0.50		L	0.012	0.016	0.020
L1	0.375	0.475	0.575		L1	0.015	0.019	0.023

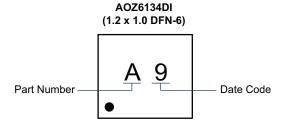
UNIT: mm

#### Note:

1. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



# Part Marking



This datasheet contains preliminary data; supplementary data may be published at a later date. Alpha & Omega Semiconductor reserves the right to make changes at any time without notice.

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