

UTC UNISONIC TECHNOLOGIES CO., LTD

BTB10

Preliminary

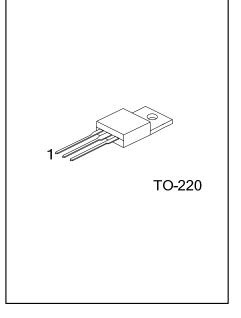
TRIAC

10A TRIACS

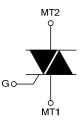
DESCRIPTION

The UTC BTB10 is a 10A triacs which can be operated in 4 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances and voltage insulated tab, etc.

The UTC BTB10 is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.



SYMBOL



ORDERING INFORMATION

Ordering	Deekage	Pin /	Assignr	Deaking		
Lead Free	Halogen Free	Package	1	2	3	Packing
BTB10L-x-x-TA3-T	BTB10G-x-x-TA3-T	TO-220	MT1	MT2	G	Tube

PE	
	Έ

SENSITIVITY AND TYPE

PART NUMBER	VOL	ΓAGE	SENSITIVITY TYPE			
PART NUMBER	600V	800V	SENSITIVIT	TTPE		
В	\bigcirc	\bigcirc	50mA	STANDARD		
С	0	\bigcirc	25mA	STANDARD		

O: Available

MARKING INFORMATION

PACKAGE	MARKING					
TO-220	UTC BTB10□ → G: Halogen Free Data Code					

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
RMS On-State Current (Full Sine Wave)	e Wave) T _C =95°C		I _{T(RMS)}	10	А
Non Repetitive Surge Peak On-State	F=50Hz	t=20ms	I _{TSM}	100	А
Current (Full Cycle T _J initial=25°C)	F=60Hz	t=16.7ms	.1.01	105	А
I ² t Value for Fusing	t _P =10ms		l ² t	55	A ² s
Critical Rate of Rise of On-State Current: I _G =2xI _{GT} , tr≤100ns	F=120Hz	TJ=125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	TJ=25°C	$V_{\text{DSM}}/V_{\text{RSM}}$	V _{DSM} /V _{RSM} +100	V
Peak Gate Current	t _P =20µs	TJ=125°C	I _{GM}	4	А
Average Gate Power Dissipation		TJ=125°C	P _{G(AV)}	1	W
Operating Junction Temperature			ТJ	-40~+125	°C
Storage Junction Temperature			T _{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	60	°C/W
Junction to Case (AC)	θ _{JC}	1.5	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J= 25°C, unless otherwise specified)

FOR STANDARD (4 QUADRANTS)

	SYMBOL	TEST CO			С		В			UNIT	
PARAMETER	STMBOL	TEST COI	NDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT	
Gate Trigger Current	1	V _D =12V,	1-11-111			25			50	mA	
(Note 1)	I _{GT}		IV			50			100	mA	
Gate Trigger Voltage	V_{GT}	R∟=33Ω	ALL			1.3			1.3	V	
Gate Non-Trigger Voltage	V_{GD}	V _D =V _{DRM} , R _L =3.3kΩ, T _J =125°C	ALL	0.2			0.2			V	
Holding Current (Note 2)	I _H	I⊤=500mA				25			50	mA	
Latabian Current		1 -1 01	I-III-IV	V		40			50	mA	
Latching Current	ΙL	I _G =1.2I _{GT}	11			80			100	mA	
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , T _J =125°C	Gate Open,	200			400			V/µs	
Critical Rate of Rise of Off-State Voltage at Commutation (Note 2)	(dV/dt)c	(dl/dt)c=4.4A/r	ns, T _J = 125°C	5			10			V/µs	

STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	VT	I _{TM} =14A, t _P =380µs T _J =25°C				1.55	V
Threshold Voltage (Note 2)	V _{TO}		TJ=125°C			0.85	V
Dynamic Resistance (Note 2)	RD		TJ=125°C			40	mΩ
Repetitive Peak Off-State Current	I _{DRM}		TJ=25°C			5	μA
	I _{RRM}	V _{DRM} =V _{RRM}	TJ=125°C			1	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.



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