

### Non-Synchronous PWM Boost Controller

### **General Description**

The FP5201 is a boost topology switching regulator for wide operating voltage applications. It provides built-in gate driver pin for driving the external N-MOSFET. The internal compensation network minimizes external component counts, and the non-inverting input of error amplifier connects to a 0.6V precision reference voltage. The FP5201 has internal soft start and short circuit protection function.

The FP5201 is available in the small footprint SOT23-6L package to fit in space-saving PCB layout for miscellaneous application fields.

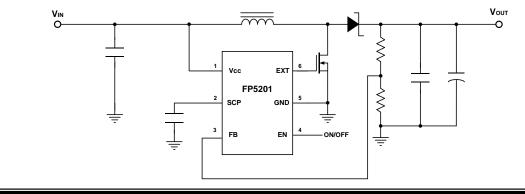
### **Features**

- Wide Supply Voltage Operating Range: 2.4 to 5.5V
- Precision Feedback Reference Voltage: 0.6V (±2%)
- ➢ Shutdown Current: <1µA</p>
- Internal Fixed PWM frequency: 550KHz
- Internal Soft Start Function :7ms (SS)
- Short Circuit Protection Function (SCP)
- Over Voltage Protection
- > Package: SOT23-6L
- Duty Cycle: PWM/PFM Switching Control Circuit (15%~90%)

### **Applications**

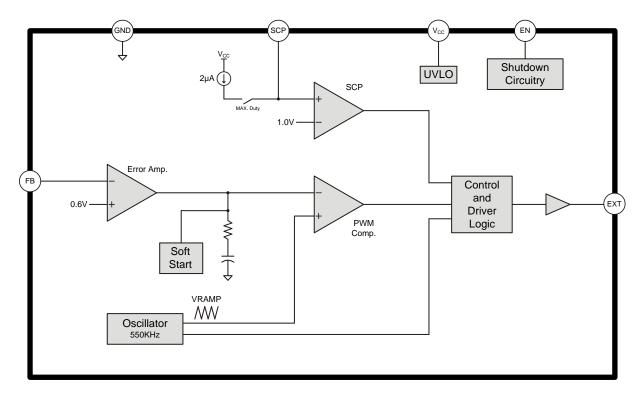
- > Chargers
- LCD Displays
- Digital Cameras
- Handheld Devices
- Portable Products

# **Typical Application Circuit**



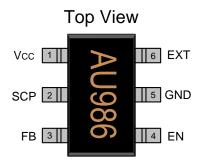


# **Function Block Diagram**



## **Pin Descriptions**

#### SOT23-6L

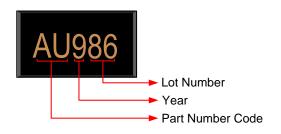


Name	No.	1/0	Description	
Vcc	1	Р	IC Power Supply	
SCP	2	Ι	Delay Time Setting Pin for SCP	
FB	3	Ι	Error Amplifier Inverting Input	
EN	4	Ι	Enable Control (Active High)	
GND	5	Р	IC Ground	
EXT	6	0	External Transistor Connection Pin	



# **Marking Information**

#### SOT23-6L



Lot Number: Wafer lot number's last two digits

For Example: 132386TB  $\rightarrow$  86

Year: Production year's last digit

Part Number Code: Part number identification code for this product. It should be always "AU".



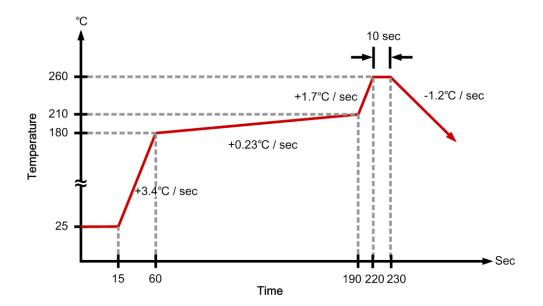
## **Ordering Information**

Part Number	Code	<b>Operating Temperature</b>	Package	MOQ	Description
FP5201LR-G1	AU	-25°C ~ 85°C	SOT23-6L	3000EA	Tape & Reel

### **Absolute Maximum Ratings**

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	V <sub>cc</sub>		0		5.5	V
EN,FB Voltage			0		5.5	V
Power Dissipation	PD	SOT23-6L @T <sub>A</sub> =25°C			455	mW
Thermal Resistance (Note1)	θ <sub>JA</sub>	SOT23-6L			+220	°C / W
Junction Temperature	TJ				+150	°C
Operating Temperature	T <sub>OP</sub>		-25		+85	°C
Storage Temperature	T <sub>ST</sub>		-65		+150	°C
Lead Temperature		(soldering, 10 sec)			+260	°C

## **IR Re-flow Soldering Curve**



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### **Recommended Operating Conditions**

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	V <sub>CC</sub>		2.4		5.5	V
Operating Temperature Range	T <sub>A</sub>	Ambient Temperature	-25		+85	°C

### DC Electrical Characteristics (V<sub>CC</sub>=3.3V, T<sub>A</sub>=25°C, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
System Supply Input	·	·				
Input Supply Range	Vcc		2.4		5.5	V
Under Voltage Lockout	V <sub>UVLO</sub>			2.1		V
UVLO Hysteresis				0.1		V
Quiescent Current	I <sub>CC</sub>	FB=1.0V, No switch		70		μA
Shutdown Current	Icc	V <sub>EN</sub> =GND		0.1		μA
Oscillator						
Operation Frequency	f <sub>osc</sub>	V <sub>FB</sub> =0.6V		550		kHz
PFM Switching Duty Ratio	%			15		%
Maximum Duty Ratio	%			90		%
Soft-Start Time	t <sub>SS</sub>	V <sub>IN</sub> =5V		7		ms
Reference Voltage						
Feedback Voltage	$V_{REF}$	V <sub>IN</sub> =5V	0.588	0.6	0.612	V
Enable Control	·		·			
Enable Voltage	$V_{\text{EN}}$		0.96			V
Shutdown Voltage	V <sub>EN</sub>				0.6	V
External Transistor Connection	on current			-		-
EXT Pin Output Current	I <sub>EXTH</sub>			-105		mA
EXT Pin Output Current	I <sub>EXTL</sub>			130		mA

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### **Function Description**

#### Operation

The FP5201 is a voltage mode boost controller. The switching is a fixed frequency 550kHz and operates with pulse width modulation (PWM). An internal resistive divider provides 0.6V reference for the error amplifier. The FP5201 changes to PFM mode when output is light load. It can increase efficiency, but PFM mode also increases output voltage ripple.

#### **Soft Start Function**

Soft start circuitry is integrated into FP5201 to avoid inrush current during power on. After the IC is enabled, the output of error amplifier is clamped by the internal soft-start function, which causes PWM pulse width increasing slowly and thus reducing input surge current.

#### **Short Circuit Protection**

When a heavy loading or short circuit condition makes output voltage dropping too much, the error amplifier output will rise to very high. That makes the duty cycle rise to maximum 90% and the short circuit protection function is triggered. External SCP capacitor is charged (2µA Typ.) until SCP threshold voltage 1V, and then pre-driver will be turned off.

The time of short circuit protection is:

 $T_{SCP}(sec)=0.5 \times C_{SCP}(\mu F)$ 

#### **Shutdown Function**

Drive Enable pin to ground to shut down the FP5201. Shutdown mode forces to turn off all internal circuitry, and reduces the VIN supply current to  $0.1\mu$ A (typ). The Enable pin rising threshold is 0.96V (typ). Before any operation begins, the voltage at Enable pin must exceed 0.96V (typ).

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### **Application Information**

#### **Inductor Selection**

Inductance value is decided based on different condition. 3.3uH to 4.7µH inductor value is recommended for general application circuit. There are three important inductor specifications, DC resistance, saturation current and core loss. Low DC resistance has better power efficiency.

#### **Capacitor Selection**

The output capacitor is required to maintain the DC voltage during switching. Low ESR capacitors are preferred to reduce the output voltage ripple. Ceramic capacitor of X5R and X7R are recommended, which have low equivalent series resistance (ESR) and wider operation temperature range.

#### **Diode Selection**

Schottky diodes with fast recovery times and low forward voltages are recommended. Ensure the diode average and peak current rating exceed the average output current and peak inductor current. In addition, the diode's reverse breakdown voltage must exceed the output voltage.

#### **Output Voltage Programming**

The output voltage is set by a resistive voltage divider from the output voltage to FB. The output voltage is:

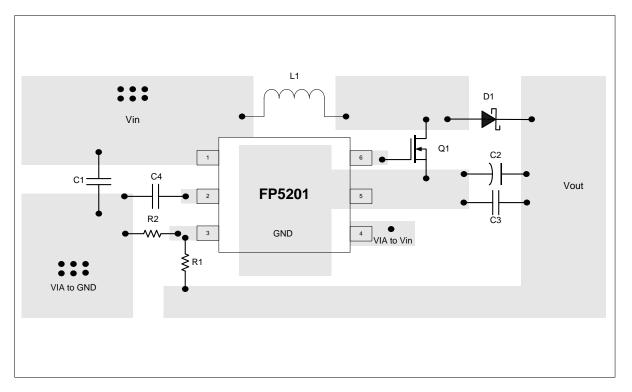
$$V_{OUT} = 0.6V \left(1 + \frac{R1}{R2}\right)$$

#### **Layout Considerations**

- The power traces, consisting of the GND trace, the MOS drain trace and the V<sub>CC</sub> trace should be kept short, direct and wide.
- Layout switching node MOS drain, inductor and diode connection traces wide and short to reduce EMI.
- 3. Place C<sub>IN</sub> nearby V<sub>CC</sub> pin as closely as possible to maintain input voltage steady and filter out the pulsing input current.
- 4. The resistive divider R1 and R2 must be connected to FB pin directly and as closely as possible.
- 5. FB is a sensitive node. Please keep it away from switching node, MOS drain.
- The GND of the IC, C<sub>IN</sub> and C<sub>OUT</sub> should be connected close together and directly to a ground plane.

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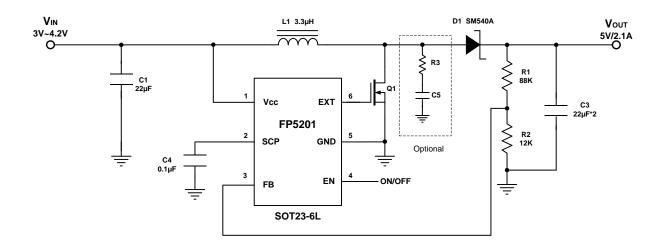


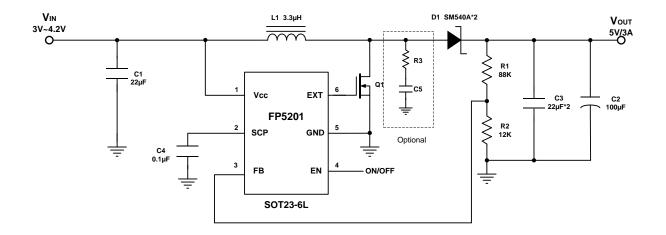


Suggested Layout



# **Application Information**





#### Note:

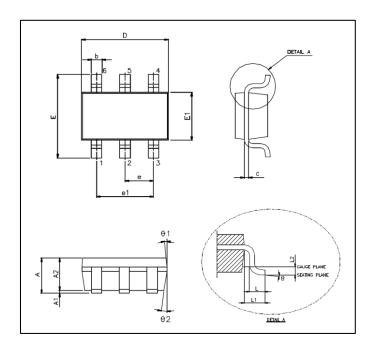
- 1. C1 and C3 choose ceramic capacitor of X5R or X7R.
- 2. R3 and C5 are added for reducing EMI (Electromagnetic Interference).

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### **Package Outline**

#### SOT23-6L



Symbols	Min. (mm)	Max. (mm)		
A	1.050	1.450		
A1	0.050	0.150		
A2	0.900	1.300		
b	0.300	0.500		
С	0.080	0.220		
D	2.900	) BSC		
E	2.800 BSC			
E1	1.600	) BSC		
е	0.950	) BSC		
e1	1.900	BSC		
L	0.300	0.600		
L1	0.600 REF			
L2	0.250 BSC			
θ°	0°	8°		
θ1°	3°	7°		
θ2°	6°	15°		

#### Note:

- 1. Package dimensions are in compliance with JEDEC outline: MO-178 AB.
- 2. Dimension "D" does not include molding flash, protrusions or gate burrs.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.

# FP5201

Unit: mm

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