TOSHIBA CCD LINEAR IMAGE SENSOR CCD(Charge Coupled Device)

TCD1252AP

TOSHIBA-CCD-ADVANCED-PLASTIC-PACKAGE

The TCD1252AP is a high sensitive and low dark current 2700elements linear image sensor. The sensor can be used for facsimile, imagescanner and OCR. The device contains a row of 2700 photodiodes, which provide a 8 lines / mm (200 DPI) across a A3 size paper and a 12 lines / mm (300DPI) across a A4 size paper. The device is operated by 5V (pulse), and 12V power supply.

FEATURES

• Number of Image Sensing Elements : 2700

• Image Sensing Element Size : 11μm by 11μm on 11μm centers Photo Sensing Region : High sensitive low dark current

Clock : 2 phase (5V)

Package : 22 pin DIP (T-CAPP) WDIP22-G-400-2.54A

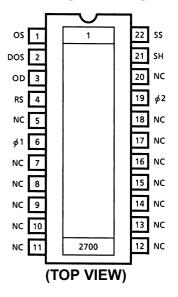
Weight: 2.7g (Typ.)

MAXIMUM RATINGS (Note 1)

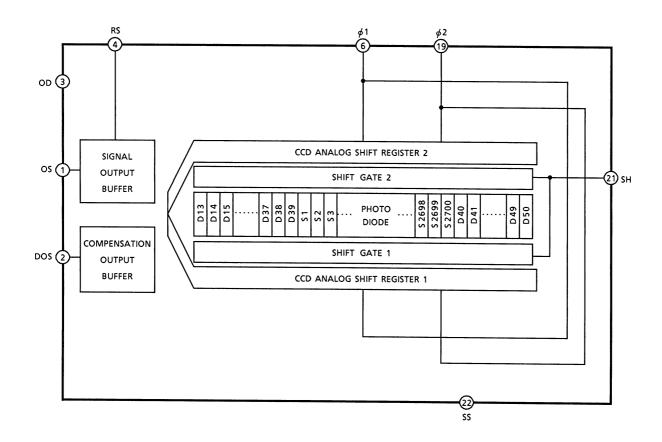
| CHARACTERISTIC | SYMBOL | RATING | UNIT |
|-----------------------|------------------|---------|------|
| Clock Pulse Voltage | V_{ϕ} | | > |
| Shift Pulse Voltage | V_{SH} | -0.3~8 | |
| Reset Pulse Voltage | V _{RS} | 0.5 0 | |
| Power Supply Voltage | V _{OD} | | |
| Operating Temperature | T _{opr} | -25~60 | °C |
| Storage Temperature | T _{stg} | -40~100 | °C |

Note 1: All voltage are with respect to SS terminals (Ground).

PIN CONNECTION



CIRCUIT DIAGRAM



2

PIN NAMES

| φ1 | Clock (Phase 1) |
|-----|---------------------|
| φ2 | Clock (Phase 2) |
| SH | Shift Gate |
| RS | Reset Gate |
| OS | Signal Output |
| DOS | Compensation Output |
| OD | Power |
| SS | Ground |
| NC | Non Connection |

OPTICAL / ELECTRICAL CHARACTERISTICS

(Ta = 25°C, V_{OD} = 5V, V_{ϕ} = V_{SH} = V_{RS} = 5V (PULSE), f_{ϕ} = 0.5MHz, f_{RS} = 1MHz,), LOAD RESISTANCE=100kΩ, t_{INT} (INTEGRATION TIME) = 10ms LIGHT SOURCE = DAYLGIHT FLUORESCENT LAMP)

| CHARACTERISTIC | SYMBOL | MIN | TYP. | MAX | UNIT | NOTE |
|--------------------------------|----------------------|-----|------|-----|----------|-----------|
| Sensitivity | R | 44 | 63 | 82 | V / lx·s | (Note 2) |
| Dhata Danasa Nag Haifamita | PRNU (1) | _ | _ | 10 | % | (Note 3) |
| Photo Response Non Uniformity | PRNU (3) | _ | 7 | 16 | mV | (Note 10) |
| Register Imbalance | RI | _ | _ | 3 | % | (Note 4) |
| Saturation Output Voltage | V _{SAT} | 1.0 | 1.5 | _ | V | (Note 5) |
| Saturation Exposure | SE | _ | 0.02 | _ | lx⋅s | (Note 6) |
| Dark Signal Voltage | V _{DRK} | _ | 2 | 6 | mV | (Note 7) |
| Dark Signal Non Uniformity | DSNU | _ | 3 | 7 | mV | (Note 7) |
| DC Power Dissipation | PD | _ | 35 | 100 | mW | |
| Total Transfer Efficiency | TTE | 92 | _ | _ | % | |
| Output Impedance | Z _O | _ | _ | 1 | kΩ | |
| Dynamic Range | DR | _ | 750 | _ | _ | (Note 8) |
| DC Signal Output Voltage | Vos | 2.5 | 3.5 | 4.5 | V | (Note 9) |
| DC Compensation Output Voltage | V _{DOS} | 2.5 | 3.5 | 4.5 | V | (Note 9) |
| DC Mismach Voltage | Vos-V _{DOS} | _ | 50 | 100 | mV | (Note 9) |
| Random Noise | ND_{σ} | _ | 1.5 | _ | mV | (Note 11) |

Note 2: Sensitivity for 2856K W-Lamp is 105V / Ix·s (Typ.) Sensitivity for LED (567nm) is 22.7V / Ix·s (Typ.)

Note 3: Measured at 50% of SE (Typ.)

Definition of PRNU =
$$\frac{\Delta \chi}{\bar{\chi}} \times 100(\%)$$

Where $\bar{\chi}$ is average of total signal outputs and $\Delta\chi$ is the maximum deviation from $\bar{\chi}$ under uniform illumination.

Note 4: Measured at 50% of SE (Typ.)

RI is defined as follows:

RI =
$$\frac{\sum_{x=1}^{2699} |xn - xn + 1|}{2699 \times \overline{x}} \times 100(\%)$$

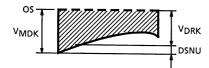
Where χ n and χ n+1 are signal outputs of each pixel. $\bar{\chi}$ V is average of total signal outputs.

3

Note 5: V_{SAT} is defined as minimum saturation output voltage of all effective pixels.

Note 6: Definition of SE : =
$$\frac{V_{SAT}}{R}(x \cdot s)$$

Note 7: V_{DRK} is defined as average dark signal voltage of all effective pixels. DSNU is defined as different voltage between V_{DRK} and V_{MDK} when V_{MDK} is maximum dark signal voltage.

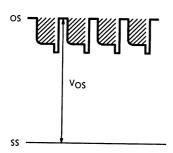


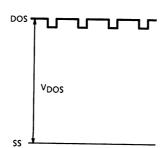
Note 8: Definition of DR : DR = $\frac{V_{SAT}}{V_{DRK}}$

 V_{DRK} is proportional to t_{INT} (Integration time).

So the shorter $t_{\mbox{\scriptsize INT}}$ condition makes wider DR value.

Note 9: DC signal output voltage and DC compensation output voltage are defined as follows:





VDD = 4.7V DC Mismatch Voltage.

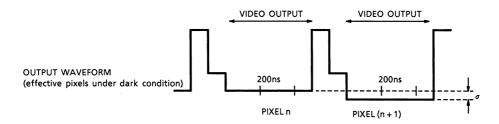
Note 10: PRNU (3) is defined as maximum voltage with next pixel where measured 5% of SE (Typ.).

Note 11:

1. DEFINITION

Random noise is defined as the standard deviation (sigma) of the output level differencebetween two adjacent effective pixels under no illumination (i.e. dark condition) calculated by the following procedure.

2. CALCULATION PROCEDUREThe following is the calculation procedure of random noise.



- 1) Two adjacent pixels (pixel n and n+1) in one reading are fixed as measurement points.
- 2) Each of the output levels at video output period is averaged over 200 nanosecond periodto get Vn and Vn+1.
- 3) Vn+1 is subtracted from Vn to get ΔV .

4) The standard deviation of ΔV is calculated after procedure 2) and 3) are repeated 30 times (30 readings).

$$\overline{\Delta V} = \frac{1}{30} \sum_{i=1}^{30} \left| \Delta V^i \right| \qquad \quad \sigma = \sqrt{\frac{1}{30} \sum_{j=1}^{30} \left(\! \Delta V^j \! \left| - \overline{\Delta V} \right|^{\! 2} \! \right)}$$

5) Procedure 2), 3) and 4) are repeated 10 times to get 10 sigma values.

$$\overline{\sigma} = \frac{1}{10} \sum_{j=1}^{10} \sigma_j$$

6) $\bar{\sigma}$ value calculated using the above procedure is observed $\sqrt{2}$ times larger than that measured relative to the ground level. So we specify the random noise as the following.

Random noise
$$(N_{D\sigma}) = \frac{1}{\sqrt{2}} \overline{\sigma}$$

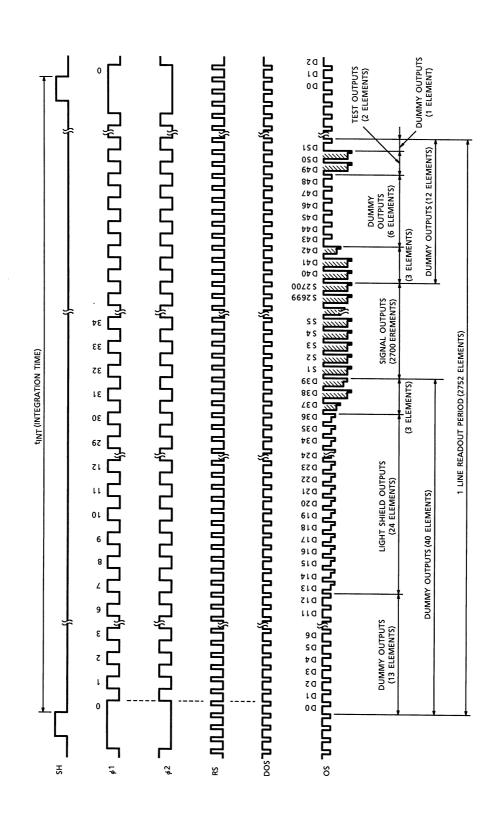
OPERATING CONDITION

| CHARACTERISTIC | | SYMBOL | MIN | TYP. | MAX | UNIT |
|----------------------------------------------|-----------|-----------------|-----|------|-----|------|
| Clock Pulse Voltage "H" Level V _φ | 4.5 | 5 | 5.5 | V | | |
| | "L" Level | Vφ | 0 | 0 | 0.3 | 1 v |
| Shift Pulse Voltage | "H" Level | V _{SH} | 4.5 | 5 | 5.5 | V |
| | "L" Level | | 0 | 0 | 0.3 | |
| Reset Pulse Voltage | "H" Level | \/ | 4.7 | 5 | 5.5 | · V |
| | "L" Level | V_{RS} | 0 | 0 | 0.3 | ٧ |
| Power Supply Voltage | | V_{DD} | 4.7 | 5.0 | 5.3 | V |

CLOCK CHARACTERISTICS (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | MIN | TYP. | MAX | UNIT |
|------------------------|-----------------|------|------|-----|------|
| Clock Pulse Frequency | f_{ϕ} | 0.15 | 0.5 | 1.5 | MHz |
| Reset Pulse Frequency | f _{RS} | 0.3 | 1 | 3 | MHz |
| Clock Capacitance | Сф | _ | 200 | 300 | pF |
| Shift Gate Capacitance | C _{SH} | _ | 100 | 200 | pF |
| Reset Gate Capacitance | C _{RS} | _ | 10 | 30 | pF |

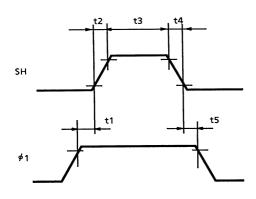
TIMING CHART



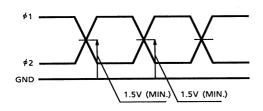
7

TIMING REQUIREMENTS

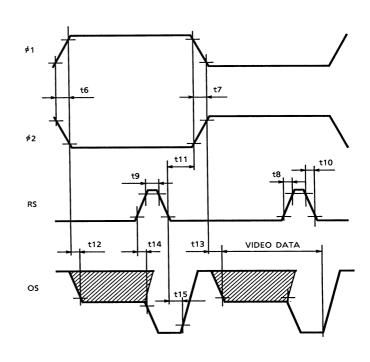
SH, ϕ 1 Timing



 ϕ 1, ϕ 2 Cross Point



 ϕ 1, ϕ 2, RS, OS Timing

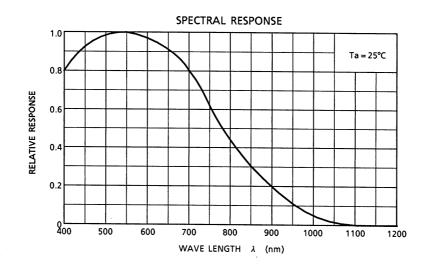


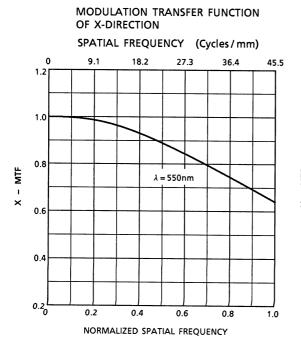
| CHARACTERISTIC | SYMBOL | MIN | TYP. (Note12) | MAX | UNIT |
|-------------------------------------------|----------|------|------------------|-----|------|
| Pulse Timing of SH and $\phi 1$ (Note 14) | t1, t5 | 500 | 1000 | _ | ns |
| SH Pulse Rise Time, Fall Time | t2, t4 | 0 | 50 | _ | ns |
| SH Pulse Width (Note 14) | t3 | 1000 | 2000 | _ | ns |
| φ1, φ2 Pulse Rise Time, Fall Time | t6, t7 | 0 | 100 | _ | ns |
| RS Pulse Rise Time, Fall Time | t8, t10 | 0 | 20 | 60 | ns |
| RS Pulse Width | t9 | 40 | 250 | _ | ns |
| Pulse Timing ofφ1, φ2 and RS | t11 | 230 | _ | _ | ns |
| Video Data Delay Time (Note 13) | t12, t13 | _ | 120 | _ | ns |
| RS Noise Delay Time | t14 | _ | 10 | _ | ns |
| NO NOISE Delay Tille | t15 | _ | 200 | _ | ns |

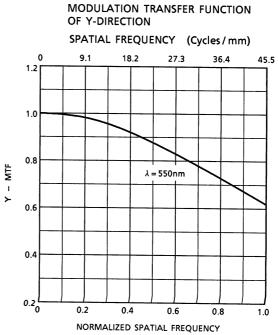
Note 12: TYP. is the case of f_{RS} =1MHz Note 13: Load Resistance is $100k\Omega$

Note 14: This condition is SHR= 0Ω

TYPICAL PERFORMANCE CURVES

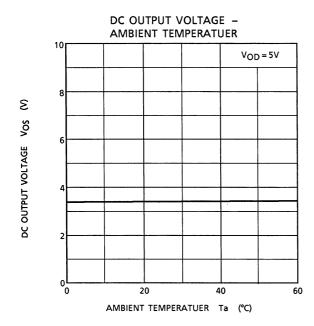


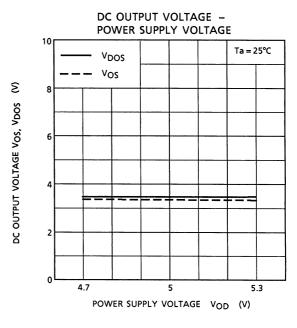


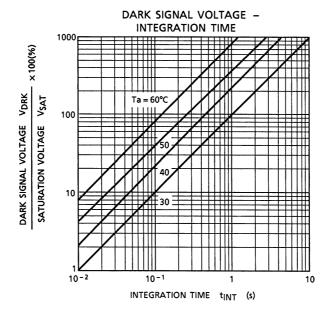


9

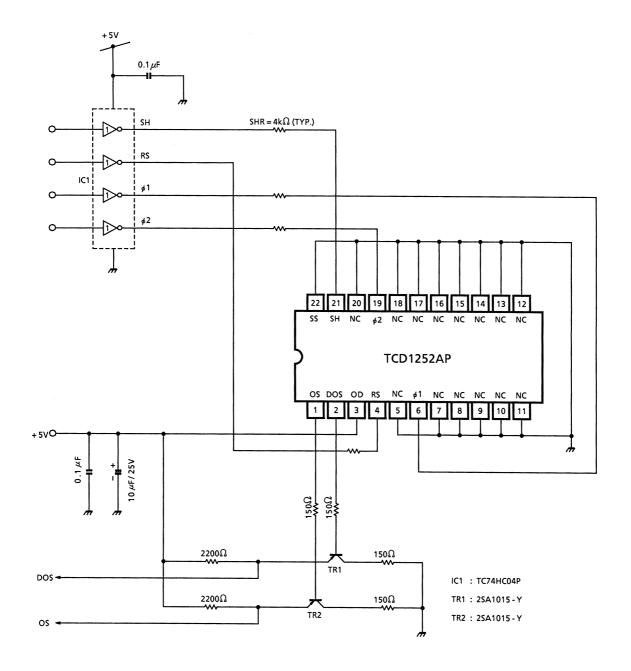
TYPICAL PERFOMANCE CURVES







TYPICAL DRIVE CIRCUIT



CAUTION

1. Window Glass

The dust and stain on the glass window of the package degrade optical performance of CCD sensor. Keep the glass window clean by saturating a cotton swab in alcohol and lightly wiping the surface, and allow the glass to dry, by blowing with filtered dry N2. Care should be taken to avoid mechanical or thermal shock because the glass window is easily to damage.

2. Electrostatic Breakdown

Store in shorting clip or in conductive foam to avoid electrostatic breakdown.

CCD Image Sensor is protected against static electricity, but interior puncture mode device due to static electricity is sometimes detected. In handing the device, it is necessary to execute the following static electricity preventive measures, in order to prevent the trouble rate increase of the manufacturing system due to static electricity.

- a. Prevent the generation of static electricity due to friction by making the work with bare hands or by putting on cotton gloves and non-charging working clothes.
- b. Discharge the static electricity by providing earth plate or earth wire on the floor, door or stand of the work room.
- c. Ground the tools such as soldering iron, radio cutting pliers of or pincer.
 It is not necessarily required to execute all precaution items for static electricity.
 It is all right to mitigate the precautions by confirming that the trouble rate within the prescribed range.

3. Incident Light

CCD sensor is sensitive to infrared light. Note that infrared light component degrades resolution and PRNU of CCD sensor.

4. Lead Frame Forming

Since this package is not strong against mechanical stress, you should not reform the lead frame. We recommend to use a IC-inserter when you assemble to PCB.

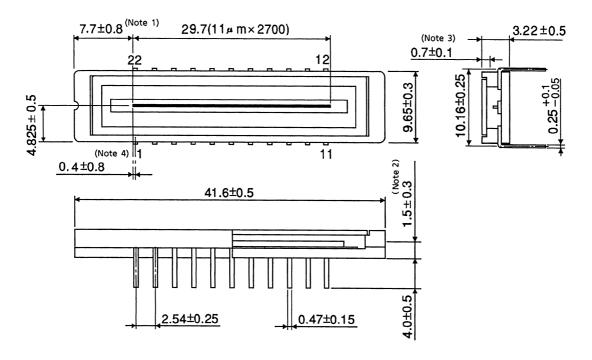
5. Soldering

Soldering by the solder flow method cannot be guaranteed because this method may have deleterious effects on prevention of window glass soiling and heat resistance.

Using a soldering iron, complete soldering within ten seconds for lead temperatures of up to 260°C, or within three seconds for lead temperatures of up to 350°C.

PACKAGE DIMENSIONS

WDIP22-G-400-2.54A Unit: mm



Note 1: No. 1 SENSOR ELEMENT (S1) TO EDGE OF PACKAGE.

Note 2: TOP OF CHIP TO BOTTOM OF PACKAGE.

Note 3: GLASS THICKNES (n=1.5)

Note 4: No. 1 SENSOR ELEMENT (S1) TO CENTER OF No. 1 PIN.

Weight: 2.7g (Typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No
 responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other
 rights of the third parties which may result from its use. No license is granted by implication or otherwise under
 any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.