

## Dual-Modulus Divider 1:128/129 for 1 GHz with Standby Operation

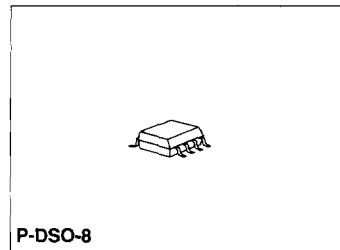
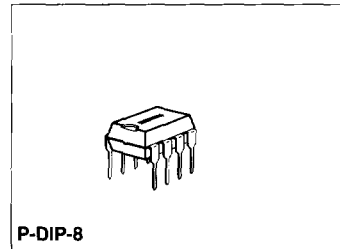
TBB 202

### Preliminary Data

Bipolar IC

### Features

- High Frequency Operation:  $f_{max} = 1100$  MHz
- Pulse Swallow Function: 128/129
- Low Power Supply current: 8.5 mA typ.  
 $V_S = 3$  V
- Stable Output Amplitude:  $V_Q = 1.0$  Vpp typ.
- Stable output amplitude is obtained up to output load capacitance of 15 pF.



Type	Ordering Code	Package
TBB 202	Q67000-H8217	P-DIP-8
TBB 202 G	Q67000-H8218	P-DSO-8 (SMD)

The TBB 202 is specially intended for applications in radiotelephones. It contains several ECL divider stages, which have a total divider ratio of 1:128/129, depending on the control of the MOD input. It can be employed in standby mode (input STB = low).

### Circuit Description

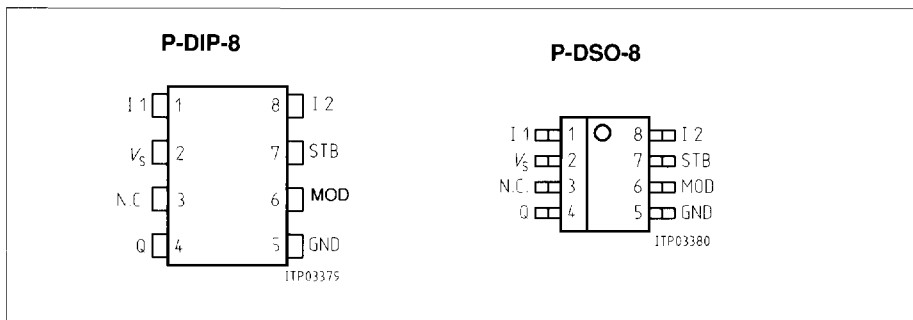
The divider has balanced push-pull inputs. If it is driven unbalanced, the unused input has to be blocked by a capacitor (approx. 1.5 nF) with low series inductance. The divider of the component consists of several status-controlled master-slave flipflops, which have a total divider ratio of 1:128/129. The inputs MOD (divider ratio) and STB (standby mode) are controllable with TTL/CMOS levels. The ECL output of the divider is CMOS compatible (see application circuit). The typical swing is 1 Vpp.

### Truth Table for Operating Modes

Input	Level	Function at Q
MOD	High or unwired <sup>1)</sup>	1:128
	low	1:129
STB	High or unwired <sup>1)</sup>	Normal
	low	Q = high, standby

### Pin Configurations

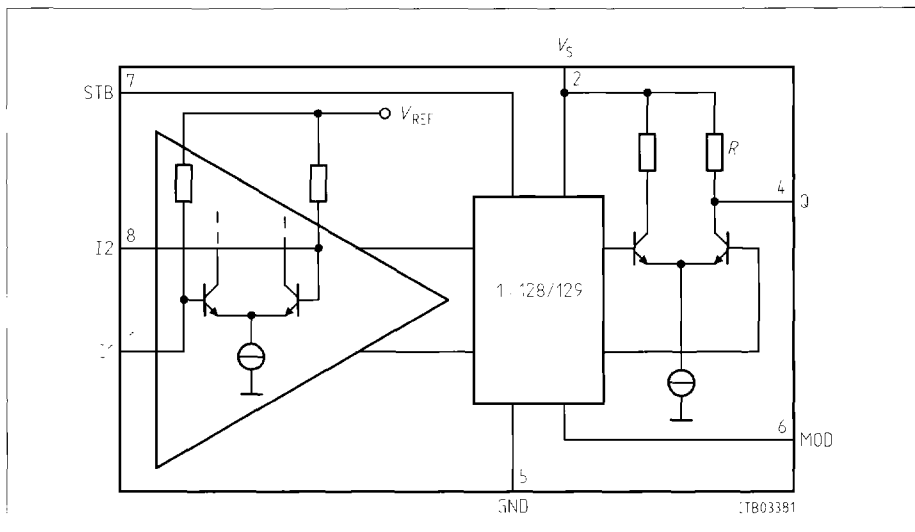
(top view)



### Pin Definitions and Functions

Pin No.	Symbol	Function
1	I1	Input
2	$V_s$	Supply voltage
3	N.C.	Not connected
4	Q	Output
5	GND	Ground
6	MOD	Input for control of divider ratio
7	STB	Input for standby mode
8	I2	Input

<sup>1)</sup> External circuitry increase noise immunity



**Block Diagram**

**Absolute Maximum Ratings**

$T_A = -40\text{ °C to }85\text{ °C}$

Parameter	Symbol	Limit Values		Unit
		min.	max.	
Supply voltage	$V_S$	-0.3	6	V
Input voltage (pin 1; pin 8)	$V_I$	-0.3	2.5	V
Input voltage (pin 6; pin 7)	$V_{MOD}, V_{STB}$	-0.3	6	V
Output voltage (pin 4)	$V_O$		$V_S$	V
Output current (pin 4)	$-I_O$		10	mA
Junction temperature	$T_j$		125	°C
Storage temperature	$T_{stg}$	-65	125	°C
Thermal resistance System – air P-DIP-8 P-DSO-8	$R_{th SA}$ $R_{th SA}$		105 185	K/W K/W

**Operating Range**

Supply voltage	$V_S$	3.0	5.5	V
Input frequency	$f$	140	1100	MHz
Ambient temperature	$T_A$	-40	85	°C

## Characteristics

$V_S = 3.0\text{ V to }5.5\text{ V}$ ,  $T_A = -40\text{ °C to }85\text{ °C}$ , refer to test circuit

Parameter	Symbol	Limit Values			Unit	Test Condition
		min.	typ.	max.		
Current consumption in operation	$I_S$		8.5		mA	Inputs blocked STB open, no load $T_A = 25\text{ °C}$ $V_S = 3\text{ V}$ $V_S = 4\text{ V}$ $V_S = 5\text{ V}$
	$I_S$		9.5	12.8	mA	
	$I_S$		10.5	13.5	mA	
in standby	$I_S$		1.5		mA	Output n.c. STB = GND Inputs blocked
Input level (input sensitivity)	$V_I$	10		100	mV <sub>rms</sub>	140 MHz (sine voltage)
		5		100	mV <sub>rms</sub>	400 MHz
		5		100	mV <sub>rms</sub>	600 MHz
		5		100	mV <sub>rms</sub>	800 MHz
		7		100	mV <sub>rms</sub>	900 MHz
		10		100	mV <sub>rms</sub>	1000 MHz

## MOD Input

Switching threshold	$V_I$	0.7		V	
H-input current	$I_{IH}$	0	50	$\mu\text{A}$	MOD = $V_S$ (1:128)
L-input current	$-I_{IL}$	110	210	$\mu\text{A}$	MOD = ground (1:129)

## Standby Input

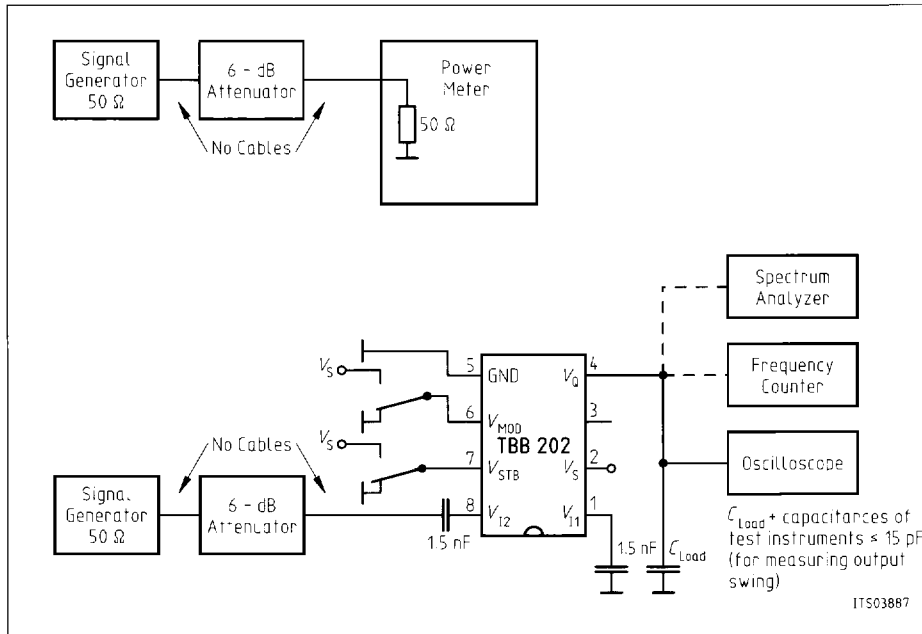
Switching threshold	$V_{STB}$	0.7		V	
H-input current	$I_{IH}$		50	$\mu\text{A}$	STB = $V_S$
L-input current	$-I_{IL}$	100	200	$\mu\text{A}$	STB = ground

## Output

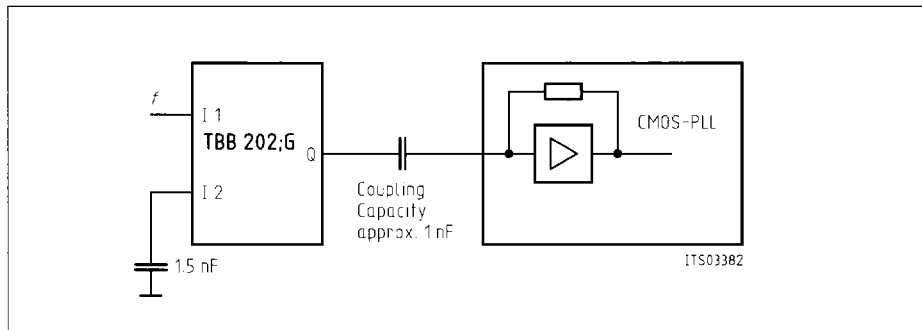
Output voltage swing	$V_O$	1		V <sub>pp</sub>	$C_L \leq 15\text{ pF}$
Output resistance	$R$	1		k $\Omega$	

## Switching Times

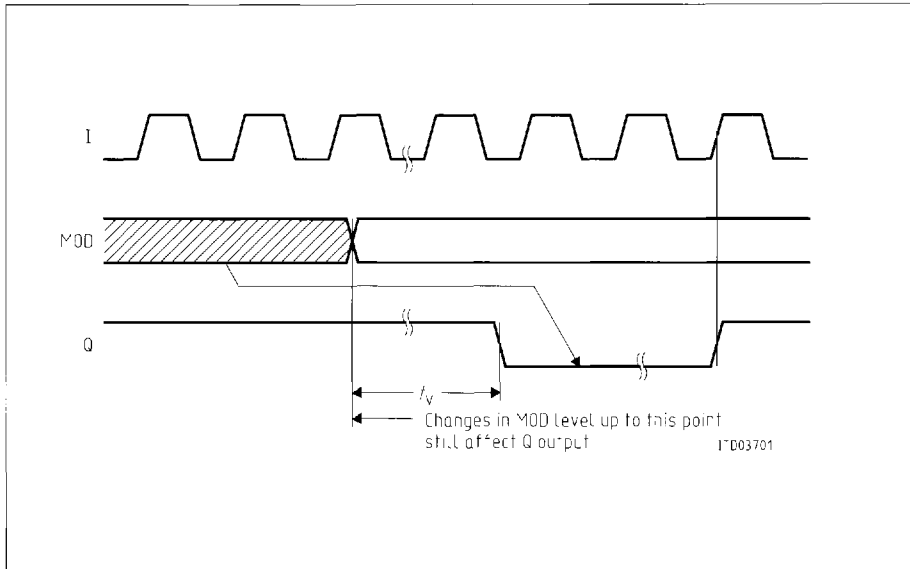
Setup time	$t_V$		28	ns	
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Test Circuit



Application Circuit



Diagram