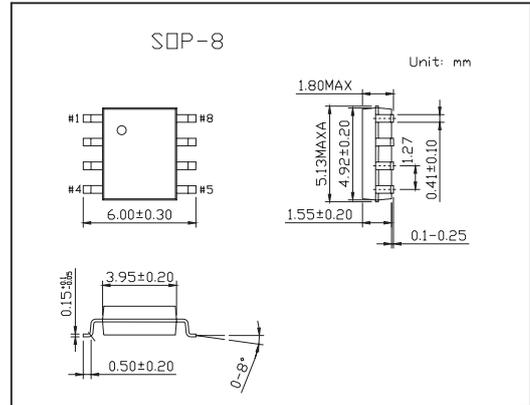
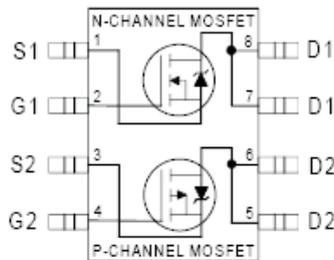


# KRF7317

## ■ Features

- Generation V Technology
- Ultra Low On-Resistance
- Dual N and P Channel MOSFET
- Surface Mount
- Fully Avalanche Rated



## ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	-20	V
Continuous Drain Current Ta = 25°C	I <sub>D</sub>	6.6	-5.3	A
Continuous Drain Current Ta = 70°C	I <sub>D</sub>	5.3	-4.3	
Pulsed Drain Current	I <sub>DM</sub>	26	-21	
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	2.5	-2.5	
Power Dissipation @Ta= 25°C *2	P <sub>D</sub>	2.0		W
Power Dissipation @Ta= 70°C *2		1.3		
Single Pulse Avalanche Energy	E <sub>AS</sub>	100	150	mJ
Avalanche Current	I <sub>AR</sub>	4.1	-2.9	A
Repetitive Avalanche Energy	E <sub>AR</sub>	0.20		mJ
Peak Diode Recovery dv/dt *1	dv/dt	5.0	-5	V/ ns
Gate-to-Source Voltage	V <sub>GS</sub>	±12		V
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150		°C
Maximum Junction-to-Ambient *2	R <sub>θJA</sub>	62.5		°C/W

\*1 N-Channel I<sub>SD</sub> ≤ 4.1A, di/dt ≤ 92A/μs, V<sub>DD</sub> ≤ V(BR)DSS, T<sub>J</sub> ≤ 150°C

P-Channel I<sub>SD</sub> ≤ -2.9A, di/dt ≤ -77A/μs, V<sub>DD</sub> ≤ V(BR)DSS, T<sub>J</sub> ≤ 150°C

\*2 Surface mounted on FR-4 board, t ≤ 10sec.

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■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions		Min	Typ	Max	Unit
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250 μ A	N-Ch	20			V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = -250 μ A	P-Ch	-20			
Breakdown Voltage Temp. Coefficient	ΔV <sub>(BR)DSS</sub> /ΔT <sub>J</sub>	I <sub>D</sub> = 1mA, Reference to 25°C	N-Ch		0.027		V/°C
		I <sub>D</sub> = -1mA, Reference to 25°C	P-Ch		0.031		
Static Drain-to-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6.0A*1	N-Ch		0.023	0.029	Ω
		V <sub>GS</sub> = 2.7V, I <sub>D</sub> = 5.2A*1			0.030	0.046	
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.9A*1	P-Ch		0.049	0.058	
		V <sub>GS</sub> = -2.7V, I <sub>D</sub> = -1.5A*1			0.082	0.098	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μ A	N-Ch	0.7			V
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μ A	P-Ch	-0.7			
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 6.0A*1	N-Ch		20		S
		V <sub>DS</sub> = -10V, I <sub>D</sub> = -1.5A*1	P-Ch		5.9		
Drain-to-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V	N-Ch			1.0	μ A
		V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V	P-Ch			-1.0	
		V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55°C	N-Ch			5.0	
		V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55°C	P-Ch			-25	
Gate-to-Source Forward Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±12V	N-Ch			±100	nA
			P-Ch			±100	
Total Gate Charge	Q <sub>g</sub>	N-Channel I <sub>D</sub> = 6.0A, V <sub>DS</sub> = 10V, V <sub>GS</sub> = 4.5V	N-Ch		18	27	nC
Gate-to-Source Charge	Q <sub>gs</sub>	P-Channel	N-Ch		2.2	3.3	
			P-Ch		4.0	6.1	
Gate-to-Drain ("Miller") Charge	Q <sub>gd</sub>	I <sub>D</sub> = -2.9A, V <sub>DS</sub> = -16V, V <sub>GS</sub> = -4.5V	N-Ch		6.2	9.3	
			P-Ch		7.7	12	
Turn-On Delay Time	t <sub>d(on)</sub>	N-Channel V <sub>DD</sub> = 10V, I <sub>D</sub> = 1.0A, R <sub>G</sub> = 6.0 Ω	N-Ch		8.1	12	ns
Rise Time	t <sub>r</sub>	P-Channel R <sub>D</sub> = 10 Ω	N-Ch		17	25	
			P-Ch		40	60	
Turn-Off Delay Time	t <sub>d(off)</sub>	V <sub>DD</sub> = -10V, I <sub>D</sub> = -2.9A, R <sub>G</sub> = 6.0 Ω R <sub>D</sub> = 3.4 Ω	N-Ch		38	57	
			P-Ch		42	63	
Fall Time	t <sub>f</sub>		N-Ch		31	47	
			P-Ch		49	73	
Input Capacitance	C <sub>iss</sub>	N-Channel V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1.0MHz	N-Ch		900		
			P-Ch		780		
Output Capacitance	C <sub>oss</sub>	P-Channel	N-Ch		430		
			P-Ch		470		
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = -15V, f = 1.0MHz	N-Ch		200		
			P-Ch		240		

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■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Continuous Source Current (Body Diode)	Is		N-Ch		2.5	A
			P-Ch		-2.5	
Pulsed Source Current (Body Diode) *2	ISM		N-Ch		26	A
			P-Ch		-21	
Diode Forward Voltage	VSD	TJ = 25°C, Is = 1.7A, VGS = 0V*3	N-Ch	0.72	1.0	V
		TJ = 25°C, Is = -2.9A, VGS = 0V*3	P-Ch	-0.78	-1.0	
Reverse Recovery Time	trr	N-Channel	N-Ch	52	77	ns
		TJ = 25°C, IF = 1.7A, di/dt = 100A/μs*	P-Ch	47	71	
Reverse RecoveryCharge	Qrr	P-Channel	N-Ch	58	86	nC
		TJ=25°C,IF=-2.9A,di/dt=-100A/μs*1	P-Ch	49	73	

\*1 Pulse width ≤ 300 μs; duty cycle ≤ 2%.

\*2 Repetitive rating; pulse width limited by max. junction temperature.

\*3 N-Channel Starting TJ = 25°C, L = 12mH RG = 25 Ω, IAS = 4.1A.

P-Channel Starting TJ = 25°C, L = 35mH RG = 25 Ω, IAS = -2.9A.