# MONOLITHIC DEVICES DELAY LINES, CHIP MONOLITHIC



# **LDH Series**

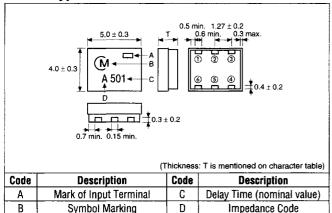
This delay line has been developed by utilizing advanced multilayer technology. It is comprised of a copper line and temperature compensated dielectric NPO (0 ± 60ppm/°C) and includes a metal shield. This results in a very small device that is compatible with high frequency applications and SMD chip processing.

## **APPLICATIONS**

- Optical Network Interface
- Super Computer/Workstations

- Multilayer construction results in a small, thin and light package
- Metal shield is built inside chip
- Reflow solderable
- Supplied on tape and reel

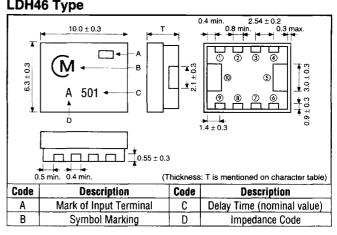
# LDH33 Type



## **TERMINALS: LDH33 Type**

Terminal No.	Function	Terminal No.	Function	
1	IN/OUT	4	GROUND	
2	GROUND	(5)	GROUND	
3	IN/OUT	6	GROUND	

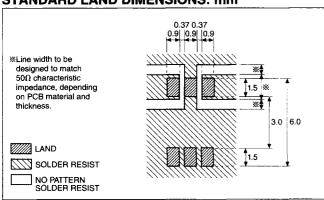
# LDH46 Type



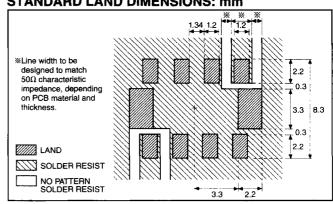
# **TERMINALS: LDH46 Type**

Terminal No.	Function	Terminal No.	Function	
①	IN/OUT	(5)	GROUND	
2	GROUND	6	IN/OUT	
3	GROUND	7	GROUND	
4	GROUND	8	GROUND	
9	GROUND	100	GROUND	

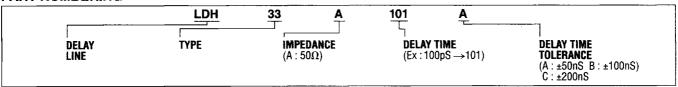
## **LDH33 TYPE** STANDARD LAND DIMENSIONS: mm



# **LDH46 TYPE** STANDARD LAND DIMENSIONS: mm



### PART NUMBERING





# **LDH Series**

#### ELECTRICAL CHARACTER & THICKNESS TABLE-LDH33/LDH46

Part Number	Delay Time (ns)	Thickness (mm) max.	Impedance $^1$ ( $\Omega$ )	Rising Time (ns) max.	Insulation Resistance (MΩ) min.	Rated Current (mA)	Operating Temperature Range (°C)
LDH33 TYPE			<u> </u>		· · —		
LDH33A101A	0.1 ± 0.05			0.15	100	50	-25 to +85
LDH33A201A	0.2 ± 0.05						
LDH33A301A	$0.3 \pm 0.05$	u					
LDH33A401A	0.4 ± 0.05	1.1					
LDH33A501A	0.5 ± 0.05						
LDH33A601B	0.6 ± 0.1						
LDH33A701B	0.7 ± 0.1		50 ± 7	0.3			
LDH33A801B	0.8 ± 0.1	1.5					
LDH33A901B	0.9 ± 0.1						
LDH33A102B	1.0 ± 0.1	1.6					
LDH33A152B	1.5 ± 0.1	2.1	]	0.5			
LDH33A202B	2.0 ± 0.1	2.6					
LDH33A252B	2.5 ± 0.1	3.1					
LDH46 TYPE							
LDH46A302B	3.0 ± 0.1	3.1		0.25 x DT <sup>2</sup>	100	100	-25 to +85
LDH46A402B	4.0 ± 0.1						
LDH46A502B	5.0 ± 0.1	3.7					
LDH46A602C	6.0 ± 0.2	3.1	- 50 ± 10				
LDH46A702C	7.0 ± 0.2	3.7					
LDH46A802C	8.0 ± 0.2						
LDH46A902C	9.0 ± 0.2						
LDH46A103C	10.0 ± 0.2						

<sup>&</sup>lt;sup>1</sup>Impedance is measured at 100MHz.

<sup>&</sup>lt;sup>2</sup>DT stands for Delay Time.

