

HD75173

Quadruple Differential Line Receivers With 3 State Outputs

REJ03D0310-0200Z
 (Previous ADE-205-592 (Z))
 Rev.2.00
 Jul.16.2004

Description

The HD75173 is a quadruple differential line receiver with three state outputs. It is designed to satisfy the requirements of EIA standards RS-422A, RS-423A and several CCITT recommendations. Each receiver features an active high enable and an active low enable common to all four receivers. It also features differential input sensitivity of ± 200 mV.

Features

- Ordering Information

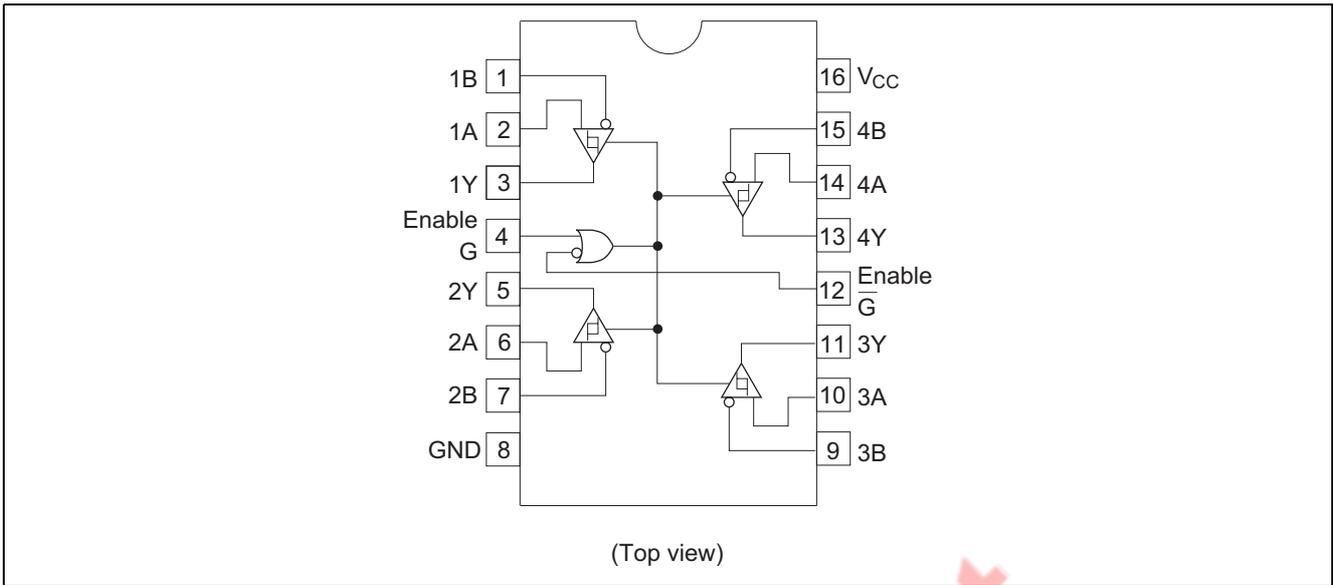
Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD75173P	DILP-16 pin	DP-16E, -16FV	P	—

Function Table

Differential Inputs A-B	Enables		Output Y
	G	\bar{G}	
$V_{ID} \geq 0.2$ V	H	X	H
	X	L	H
-0.2 V $< V_{ID} < 0.2$ V	H	X	?
	X	L	?
$V_{ID} \leq -0.2$ V	H	X	L
	X	L	L
X	L	H	Z

H : High level
 L : Low level
 X : Irrelevant
 ? : Indeterminate
 Z : High impedance

Pin Arrangement



Absolute Maximum Ratings (Ta = 0 to 70°C)

Item	Symbol	Rating	Unit
Supply Voltage	V _{CC}	7	V
Input Voltage, A or B Inputs	V _{IN}	±25	V
Differential Input Voltage*1	V _{ID}	±25	V
Enable Input Voltage	V _{IE}	7	V
Output Current	I _{OL}	50	mA
Power Dissipation (Ta = 25°C)	P _T	1150	mW
Operating temperature range	Topr	0 to 70	°C
Storage Temperature Range	Tstg	-65 to +150	°C

- Notes: 1. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input
2. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	V _{CC}	4.75	5.00	5.25	V
Common Mode Input Voltage	V _{IN}	—	—	±12	V
Differential Input Voltage	V _{ID}	—	—	±12	V
Output Current	V _{OH}	—	—	-400	μA
	V _{OL}	—	—	16	mA
Operating Temperature	Topr	0	—	70	°C

DC Electrical Characteristics (Ta = 0 to 70°C)

Item	Symbol	Min	Typ *1	Max	Unit	Conditions
Differential Input High Threshold Voltage	V _{TH}	—	—	0.2	V	V _O = 2.7 V, I _O = -0.4 mA
Differential Output Low Threshold Voltage	V _{TL}	-0.2*2	—	—	V	V _O = 0.5 V, I _O = 16 mA
Hysteresis	V _{T+} - V _{T-}	—	50	—	mV	
Enable Input Voltage	V _{IH}	2	—	—	V	
	V _{IL}	—	—	0.8		
Enable Input Clamp Voltage	V _{IK}	—	—	-1.5	V	I _I = -18 mA
Output Voltage	V _{OH}	2.7	—	—	V	V _{ID} = 200 mV, I _{OH} = -400 μA
	V _{OL}	—	—	0.45	V	V _{ID} = -200 mV, I _{OL} = 8 mA
		—	—	0.5	V	V _{ID} = -200 mV, I _{OL} = 16 mA
High Impedance State Output Current	V _{OZ}	—	—	-20	μA	V _O = 0.4 V
		—	—	+20		V _O = 2.4 V
Line Input Current	I _I	—	—	1	mA	Other Input at 0 V*4
		—	—	-0.8		
Enable Input Current	I _{IH}	—	—	20	μA	V _{IH} = 2.7 V
	I _{IL}	—	—	-100	μA	V _{IL} = 0.4 V
Input Resistance	r _i	12	—	—	kΩ	
Short Circuit Output Current*3	I _{OS}	-15	—	-85	mA	
Supply Current	I _{CC}	—	—	70	mA	

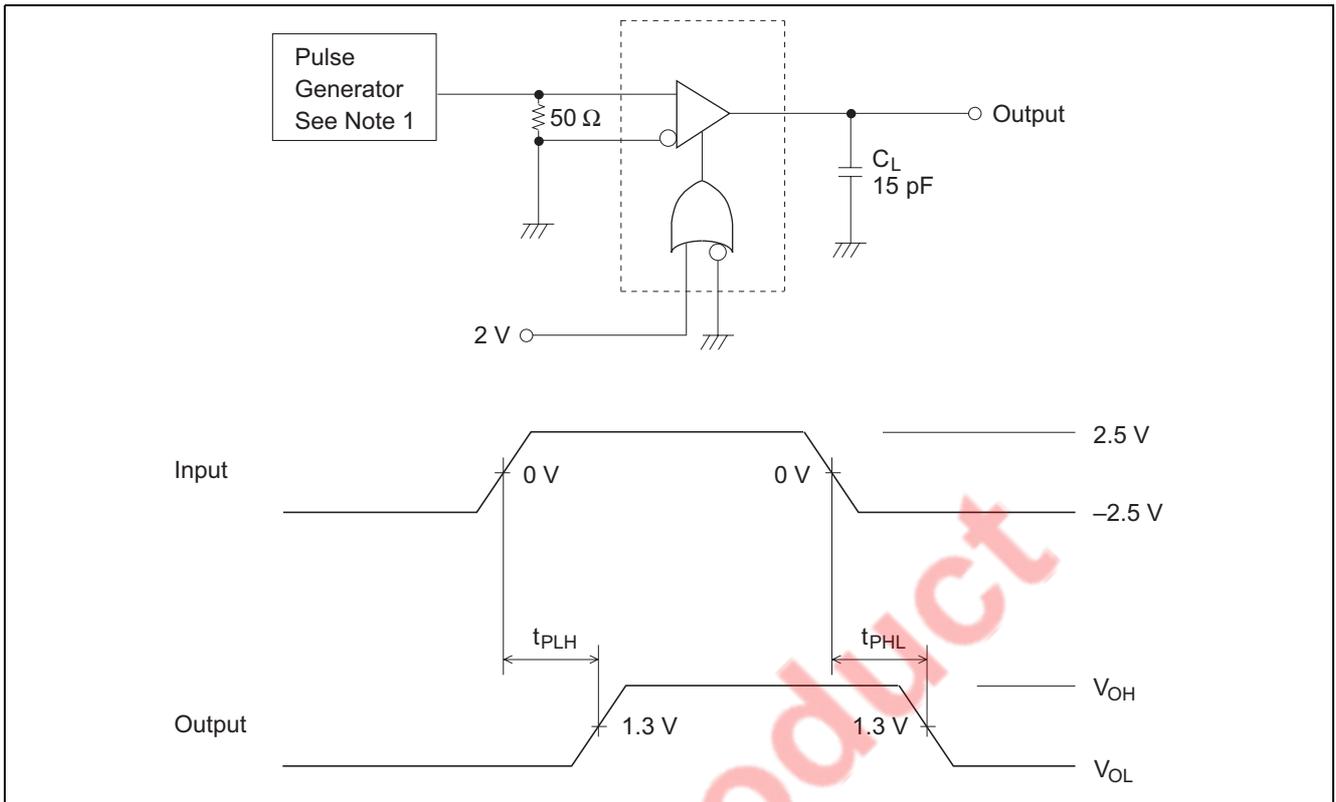
- Notes: 1. All Typical Values are at V_{CC} = 5 V, Ta = 25°C
 2. The algebraic convention is used in this data sheet for threshold voltage levels only.
 3. Not more than one output should be shorted at a time.
 4. Refer to EIA standards RS-422A and RS-423A for exact conditions.

Switching Characteristics (V_{CC} = 5 V, Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Propagation Delay Time	t _{PLH}	—	20	35	ns	C _L = 15 pF
	t _{PHL}	—	22	35		
Output Enable Time	t _{ZH}	—	17	22		C _L = 15 pF
	t _{ZL}	—	20	25		
Output Disable Time	t _{HZ}	—	21	30		C _L = 5 pF
	t _{LZ}	—	30	40		

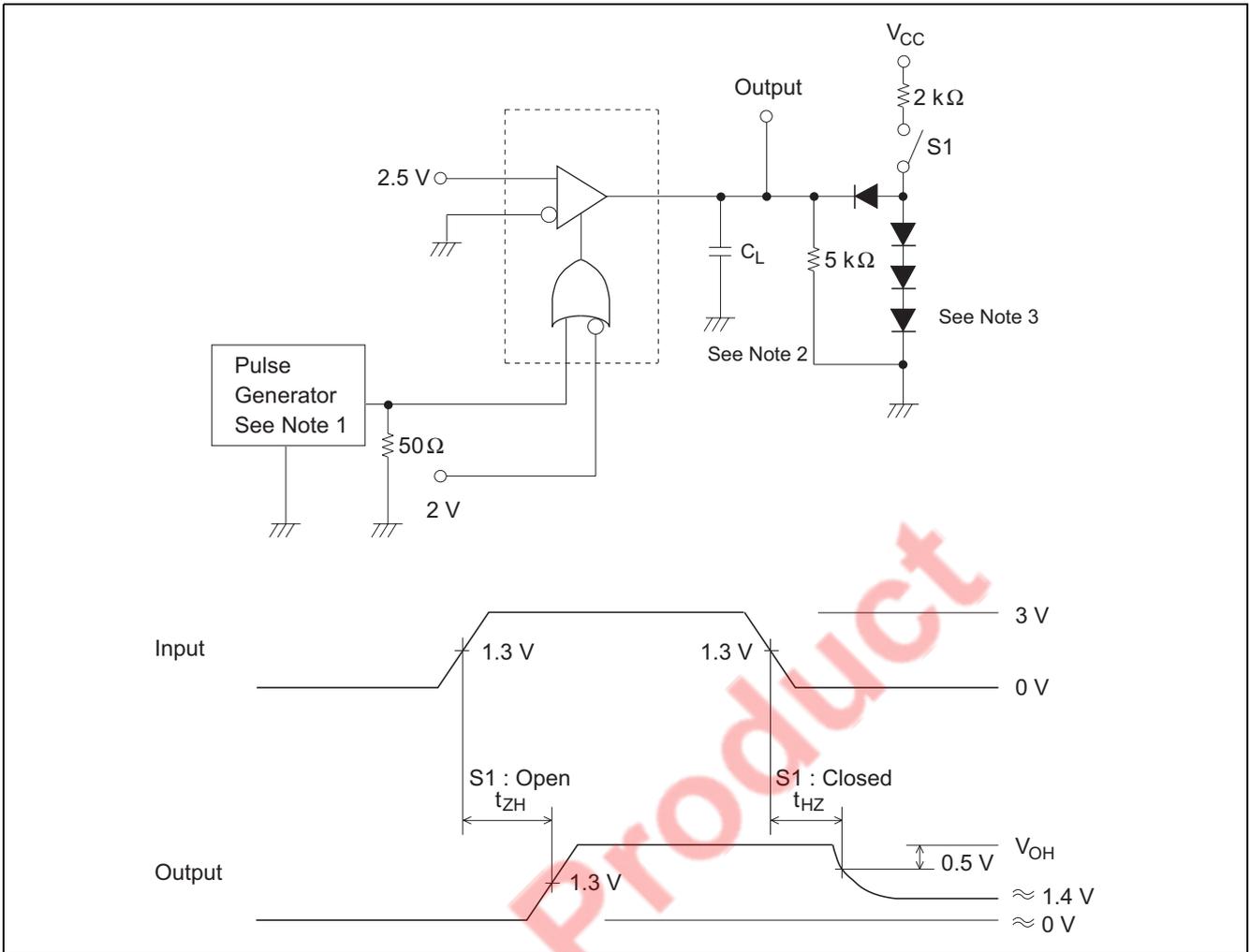
Switching Time Test Method

1. t_{PLH} , t_{PHL}

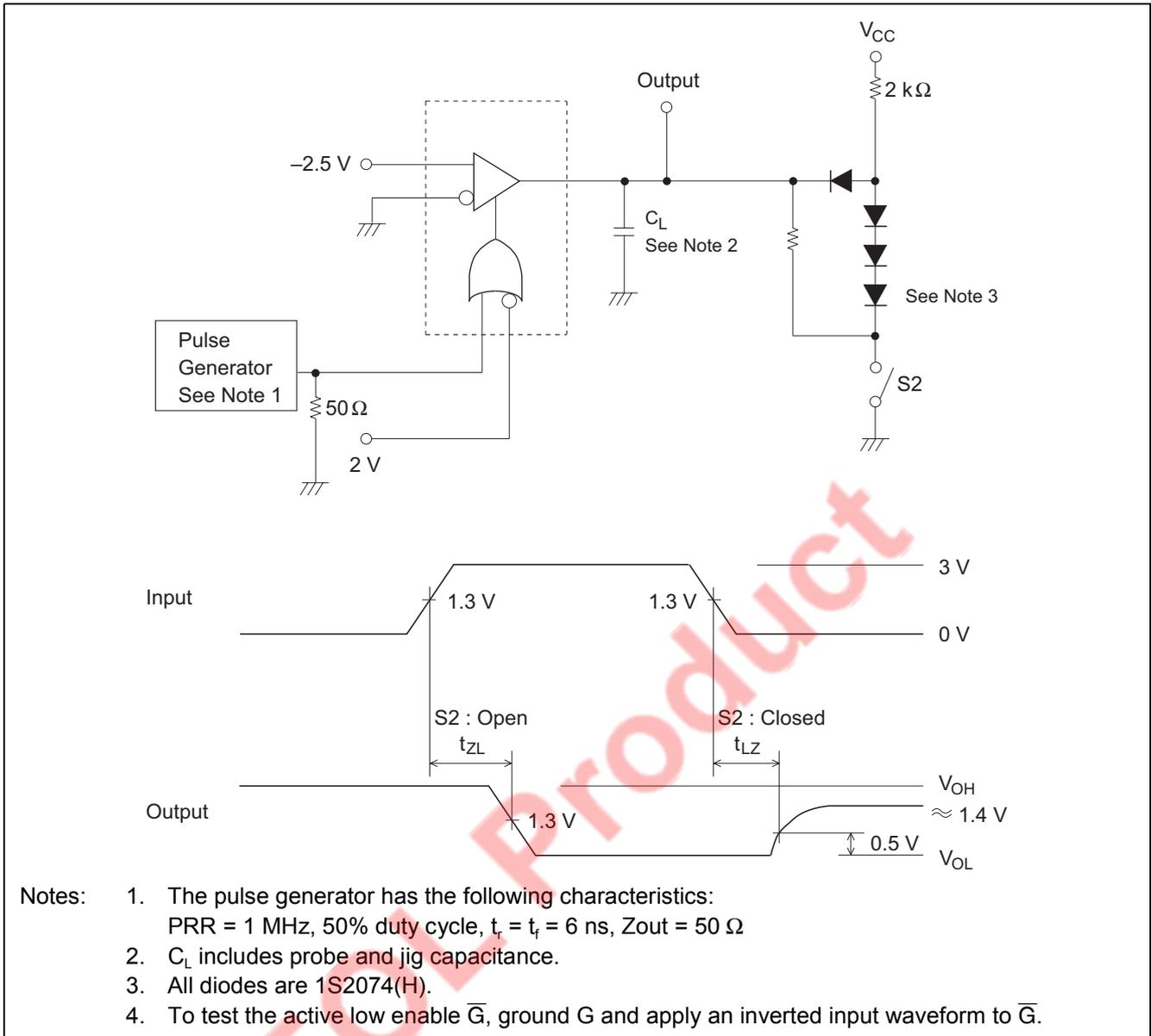


EOL Product

2. t_{ZH} , t_{ZL}

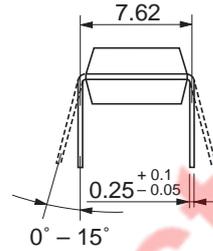
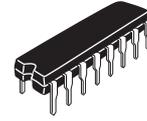
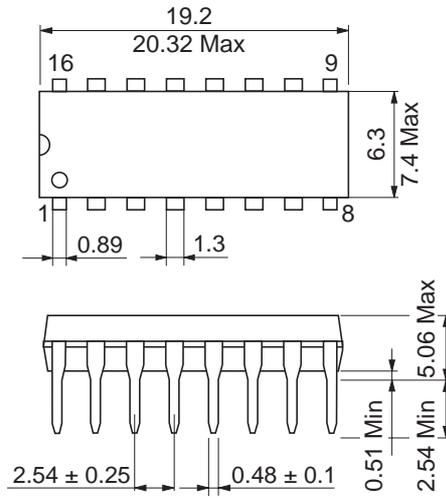


3. t_{ZL} , t_{LZ}



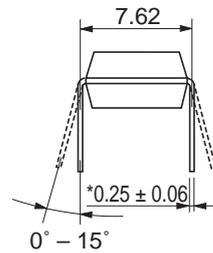
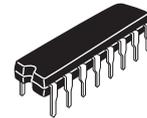
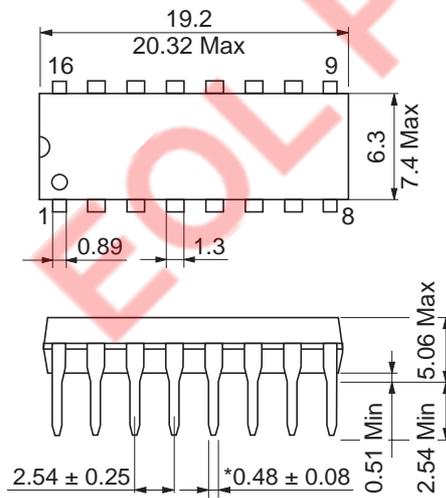
Package Dimensions

As of January, 2003
Unit: mm



Package Code	DP-16E
JEDEC	Conforms
JEITA	Conforms
Mass (reference value)	1.05 g

Unit: mm



*Ni/Pd/AU Plating

Package Code	DP-16FV
JEDEC	Conforms
JEITA	Conforms
Mass (reference value)	1.05 g

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