



A28F800BR-T/B
8-MBIT (512K X 16, 1024K X 8)
SmartVoltage BOOT BLOCK
FLASH MEMORY FAMILY

Automotive

- Intel SmartVoltage Technology
 - 5V or 12V Program/Erase
 - 5V Read Operation
 - Very High Performance Read
 - 80 ns Max. Access Time,
 - 40 ns Max. Output Enable Time
 - Low Power Consumption
 - Maximum 65 mA Read Current at 5V
 - x8/x16-Selectable Input/Output Bus
 - High Performance 16- or 32-bit CPUs
 - Optimized Array Blocking Architecture
 - One 16-KB Protected Boot Block
 - Two 8-KB Parameter Blocks
 - One 96-KB Main Block
 - Seven 128-KB Main Blocks
 - Top or Bottom Boot Locations
 - Hardware-Protection for Boot Block
 - Software EEPROM Emulation with Parameter Blocks
 - Automotive Temperature Operation
 - -40°C to +125°C
 - Extended Cycling Capability
 - 30,000 Block Erase Cycles for Parameter Blocks
 - 1,000 Block Erase Cycles for Main Blocks
 - Automated Word/ByteProgram and Block Erase
 - Industry-Standard Command User Interface
 - Status Registers
 - Erase Suspend Capability
 - SRAM-Compatible Write Interface
 - Automatic Power Savings Feature
 - 1 mA Typical I_{CC} Active Current in Static Operation
 - Reset/Deep Power-Down Input
 - 0.2 μA I_{CC} Typical
 - Provides Reset for Boot Operations
 - Hardware Data Protection Feature
 - Write Lockout during Power Transitions
 - Industry-Standard Surface Mount Packaging
 - 44-Lead PSOP: JEDEC ROM Compatible
 - ETOX™ IV Flash Technology
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