

General Description

The 80N06 is N-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

Features

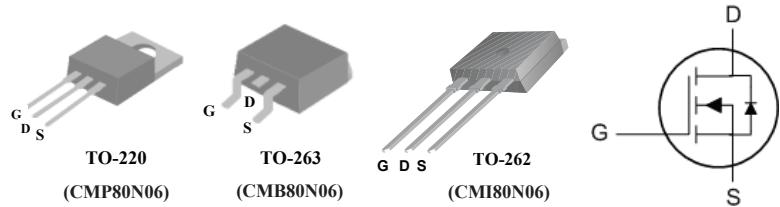
- Simple Drive Requirement
- Fast Switching
- Low On-Resistance

Product Summery

BVDSS	RDSON	ID
60V	7.8mΩ	80A

Applications

- Motor Control
- DC-DC converters
- General Purpose Power Amplifier

TO220/263/262 Pin Configuration**Absolute Maximum Ratings**

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current ¹	80	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current ¹	50	A
I_{DM}	Pulsed Drain Current ²	250	A
EAS	Single Pulse Avalanche Energy ³	405	mJ
I_{AS}	Avalanche Current	80	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation	260	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Value	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	0.9	°C/W

N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=80\text{A}$	---	6.5	7.8	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=80\text{A}$	---	8.2	14	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$	---	---	1	μA
		$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	100	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=40\text{A}$	---	50	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.5	---	Ω
Q_g	Total Gate Charge	$I_D=80\text{A}$	---	76	---	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=30\text{V}$	---	24	---	
Q_{gd}	Gate-Drain Charge	$V_{\text{GS}}=0$ to 10V	---	35	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DS}}=30\text{V}$	---	45	---	ns
T_r	Rise Time	$I_D=80\text{A}$	---	160	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time	$R_G=3.3\Omega$	---	95	---	
T_f	Fall Time	$V_{\text{GS}}=10\text{V}$	---	68	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3800	---	pF
C_{oss}	Output Capacitance		---	815	---	
C_{rss}	Reverse Transfer Capacitance		---	300	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ¹	$V_G=V_D=0\text{V}$, Force Current	---	---	80	A
I_{SM}	Pulsed Source Current ²		---	---	250	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=80\text{ A}$, $T_J=25^\circ\text{C}$	---	---	1.5	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=24\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.13\text{mH}$, $I_{\text{AS}}=80\text{A}$