

UNISONIC TECHNOLOGIES CO., LTD

100N02

Preliminary

100A, 15V N-CHANNEL POWER TRENCH MOSFET

DESCRIPTION

The UTC **100N02** is an N-channel Power Trench MOSFET, it uses UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and high switching speed.

The UTC **100N02** is generally applied in synchronous Rectification or DC to DC convertor.

FEATURES

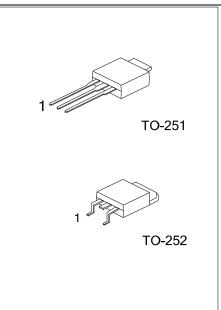
- * R_{DS(ON)}<12m Ω V_{GS}=10V, I_D =55A
- * Low Gate Charge (Typical 46nC)
- * High Switching Speed
- * High Power and Current Handling Capability

ORDERING INFORMATION

Ordering Number		Deekeese	Pin Assignment			Decking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
100N20L-TM3-T	100N20G-TM3-T	TO-251	G	D	S	Tube	
100N20L-TN3-T	100N20G-TN3-T	TO-252	G	D	S	Tube	
100N20L-TN3-R	100N20G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source

100N20L-TN3-R	(1) R: Tape Reel, T: Tube
(2)Package Type	(2) TN3: TO-252, TM3: TO-251
(3)Lead Free	(3) G: Halogen Free, L: Lead Free



ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	15	V	
Gate-Source Voltage		V _{GSS}	±8	V	
Drain Current	Continuous	ID	100	А	
	Pulsed	I _{DM}	400	А	
Avalanche Energy	Single Pulsed	E _{AS}	12	mJ	
Power Dissipation		PD	54	W	
Junction Temperature		TJ	+150	°C	
Storage Temperature Range		T _{STG}	-55~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ _{JA}	62.5	°C/W	
Junction to Case	θ _{JC}	2.3	°C/W	

ELECTRICAL CHARACTERISTICS

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PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
Drain-Source Breakdown Voltage		I _D =250μΑ, V _{GS} =0V	15			V
Drain-Source Leakage Current		V _{DS} =15V			1	μA
orward	I _{GSS}	V _{GS} =+8V			±100	nA
Reverse		V _{GS} =-8V			±100	nA
-						
Gate Threshold Voltage		I _D =250μA	0.5		1.2	V
Static Drain-Source On-State Resistance		V _{GS} =4.5V, I _D =55A			12	mΩ
Input Capacitance		V _{GS} =0V, V _{DS} =20V, f=1.0MHz		3565		рF
Output Capacitance				1310		рF
Reverse Transfer Capacitance				395		рF
Total Gate Charge		V _{GS} =10V, V _{DD} =12V, I _D =0.3A,		46	60	nC
Gate to Source Charge				6.9		nC
Gate to Drain Charge		IG-100μΑ		9.8		nC
Turn-ON Delay Time				9		ns
Rise Time		V _{DD} =10V, I _D =0.16A, R _G =25Ω,		106		ns
Turn-OFF Delay Time		V _{GS} =0~10V		53		ns
Fall-Time				41		ns
SS AND C	HARACTERI	STICS				
ige	V _{SD}	I _S =55A			1.3	V
	stance	Igss Reverse VGS(TH) stance RDS(ON) CISS COSS CRSS QG QGD tD(ON) tR tD(OFF) tF SS AND CHARACTERIS	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $



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