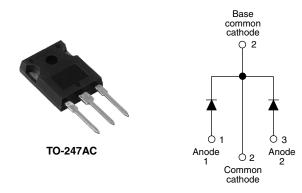


**Vishay Semiconductors** 

# Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY								
Package	TO-247AC							
I <sub>F(AV)</sub>	2 x 30 A							
V <sub>R</sub>	100 V							
V <sub>F</sub> at I <sub>F</sub>	0.64 V							
I <sub>RM</sub> max.	25 mA at 125 °C							
T <sub>J</sub> max.	175 °C							
Diode variation	Common cathode							
E <sub>AS</sub>	15 mJ							

### FEATURES

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- · Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### DESCRIPTION

The VS-63CPQ100... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	60	А						
V <sub>RRM</sub>		100	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2200	А						
V <sub>F</sub>	30 Apk, T <sub>J</sub> = 125 °C (per leg)	0.64	V						
TJ	Range	- 55 to 175	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-63CPQ100PbF	VS-63CPQ100-N3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>	100	100	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	100	v				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS				
Maximum average per leg		50 % duty cycle at $T_{C}$ = 153 °C, rectangular waveform		30					
See fig. 5 per device	I <sub>F(AV)</sub>	$30\%$ duty cycle at $T_{\rm C} = 133$ C	60	А					
Maximum peak one cycle non-repetitive surge current per leg	1	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	2200	A				
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	410					
Non-repetitive avalanche energy per leg	valanche energy per leg $E_{AS}$ $T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 30 \text{ mH}$		15	mJ					
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zer Frequency limited by T <sub>J</sub> maxim		1	А				

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ELECTRICAL SPECIFICATIO	NS					
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		30 A	T <sub>.1</sub> = 25 °C	0.77		
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	60 A	1j=25 C	0.92	V	
See fig. 1	VFM (")	30 A	0.64	v		
		60 A	T <sub>J</sub> = 125 °C	0.76		
Maximum reverse leakage current per leg	I <sub>BM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V - Poted V	0.3	mA	
See fig. 2	IRM (")	T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	25		
Threshold voltage	V <sub>F(TO)</sub>	T T maximum	·	0.38	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		5.75	mΩ	
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1300	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	Measured lead to lead 5 mm from package body			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

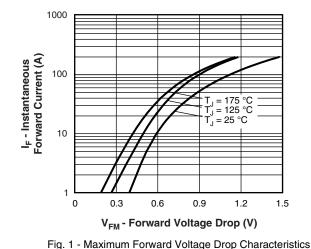
#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS				
Maximum junction and storage temperature range	1	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C				
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	0.8					
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	0.4	°C/W				
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25					
				6	g				
Approximate weight				0.21	oz.				
Mounting torque	minimum			6 (5)	kgf ⋅ cm				
Mounting torque —	maximum			12 (10)	(lbf · in)				
Marking device			Case style TO-247AC (JEDEC)	63CP	Q100				



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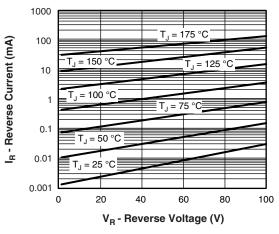


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

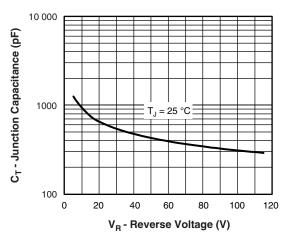


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

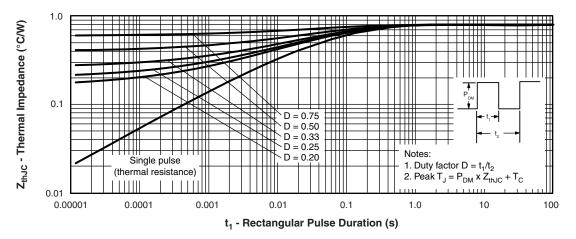
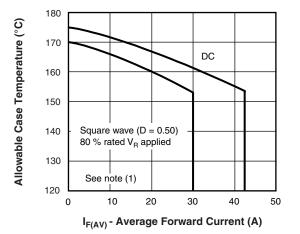
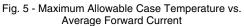


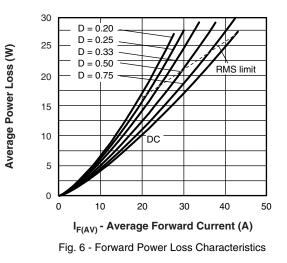
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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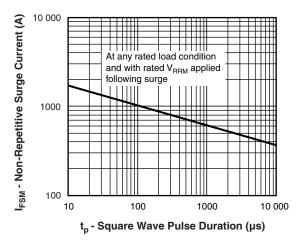


Fig. 7 - Maximum Non-Repetitive Surge Current

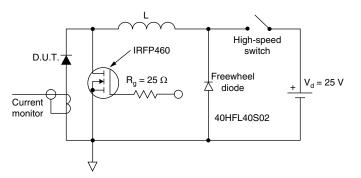


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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### Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	vs-	63	С	Р	Q	100	PbF
		2	3	4	5	6	7
	1 -   2 -   3 -   4 -   5 -   6 -   7 -	Cur Circ C = Pac P = Sch Volt Env • F	rent ratii uit confi Commo kage: TO-247 ottky "Q age coo ironmer bF = Le	" series le ntal digit ead (Pb)	.) i: de -free and	d RoHS	
		• -	N3 = Ha	logen-fr	ee, RoH	IS comp	oliant, a

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-63CPQ100PbF	25	500	Antistatic plastic tube						
VS-63CPQ100-N3	25	500	Antistatic plastic tube						

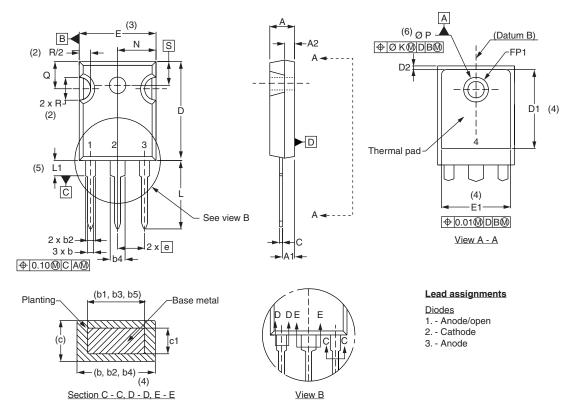
LINKS TO RELATED DOCUMENTS								
Dimensions		www.vishay.com/doc?95223						
Port marking information	TO-247AC PbF	www.vishay.com/doc?95226						
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007						

## **Outline Dimensions**





### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			e	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			FK	2.	54	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.37	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ΦP	3.56	3.66	0.14	0.144	
с	0.38	0.86	0.015	0.034			Φ <b>P1</b>	-	6.98	-	0.275	
c1	0.38	0.76	0.015	0.030			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	1.78	0.216	
D1	13.08	_	0.515	-	4		S	5.51	BSC	0.217	BSC	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

(6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC outline TO-247 with exception of dimension c

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