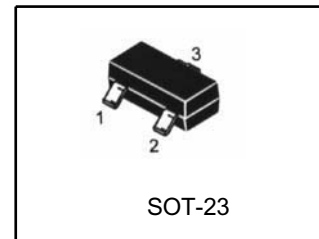


Programmable Precision Reference

LR431ATSLT1G

DESCRIPTION

The LRC LR431 is a three-terminal adjustable regulator with a guaranteed thermal stability over applicable temperature ranges. The output voltage may be set to any value between V_{ref} (approximately 2.5V) and 36V with two external resistors. It provides very wide applications, including shunt regulator, series regulator, switching regulator, voltage reference and others.



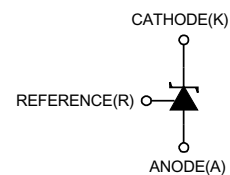
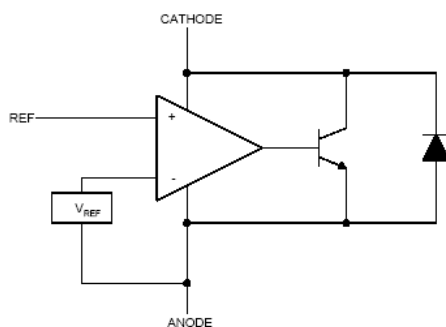
SOT-23 1: Ref; 2: Cathode; 3: Anode

FEATURES

- Programmable output Voltage to 36V.
- Low dynamic output impedance 0.2Ω
- Sink current capability of 1 to 100mA.
- Equivalent full-range temperature coefficient of $50\text{ppm}/^\circ\text{C}$ typical for operation over full rated operating temperature range.

Pb-Free package is available

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

PARAMETER	SYMBOL	VALUE	UNIT
Cathode Voltage	V _K A	36	V
Cathode Current Range(Continuous)	I _K A	-100 ~ +150	mA
Reference Input Current Range	I _{ref}	-0.05 ~ +10	mA
Operating Junction Temperature	T _j	150	°C
Operating Ambient Temperature	T _{opr}	-40 ~ +125	°C
Storage Temperature Temperature	T _{stg}	-65 ~ +150	°C

RECOMMENDED OPERATING CONDITIONS

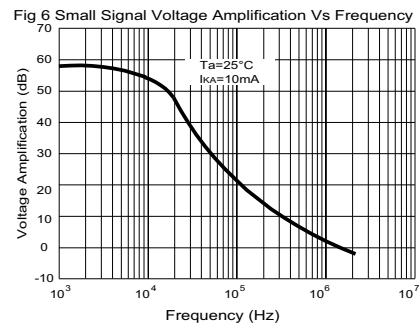
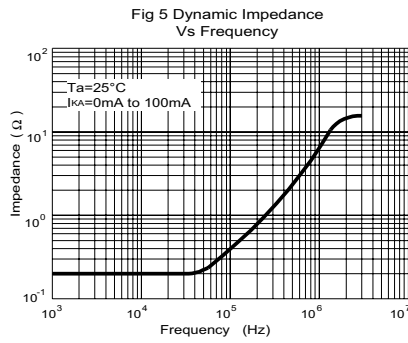
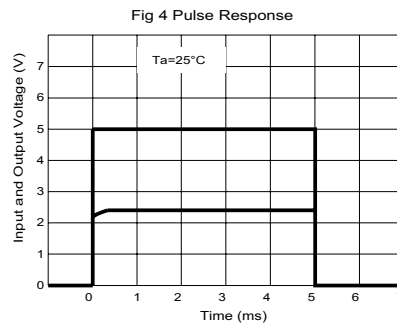
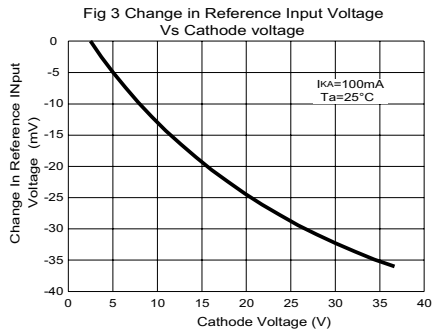
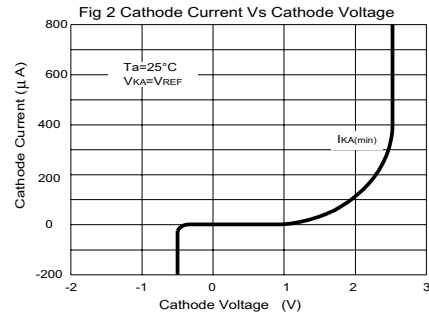
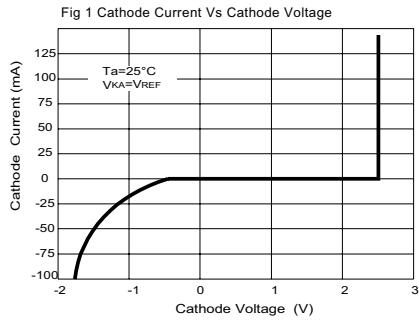
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Cathode Voltage	V _K A	V _{REF}		36	V
Cathode Current	I _K A	0.5		100	mA

ELECTRICAL CHARACTERISTICS (T_a=25°C, unless otherwise specified)

Characteristic		Symbol	Test conditions	MIN	TYP	MAX	UNIT	
Reference Input Voltage 1	0.5%	V _{ref}	V _K A=V _{REF} , I _K A=10mA	2.488	2.50	2.512	V	
	1%			2.475	2.50	2.525		
	2%			2.450	2.50	2.550		
Reference Input Voltage 2*	0.5%	V _{ref}	V _K A=V _{REF} , I _K A=10mA	2.483	2.495	2.507	V	
	1%			2.470	2.495	2.520		
	2%			2.445	2.495	2.545		
Deviation of reference Input Voltage Over temperature		ΔV _{ref}	V _K A=V _{REF} , I _K A=10mA T _{MIN} ≤ T _A ≤ T _{MAX}		4.5	25	mV	
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage		ΔV _{ref} /ΔV _K A	I _K A=10mA	ΔV _K A=10V~V _{REF}		-1.0	-2.7	mV/V
				ΔV _K A=36V~10V		-0.5	-2.0	
Reference Input Current		I _{ref}	I _K A=10mA, R ₁ =10kΩ, R ₂ =∞		1	2	μA	
Deviation of Reference Input Current Over Full Temperature Range		ΔI _{ref} /ΔT	I _K A=10mA, R ₁ =10kΩ, R ₂ =∞, T _A =full Temperature		0.2	0.4	μA	
Minimum cathode current for regulation		I _K A(min)	V _K A=V _{REF}		0.3	0.5	mA	
Off-state cathode Current		I _K A(OFF)	V _K A=36V, V _{REF} =0		0.05	0.5	μA	
Dynamic Impedance		Z _K A	V _K A=V _{REF} , I _K A=1 to 100mA f ≤ 1.0kHz		0.15	0.5	Ω	

*** CLASSIFICATION OF V_{ref} AND PACKAGE**

Type	Rank	Range(V)	Marking	Package	T _{opr}
LR431ATSLT1G	0.5%	2.488~2.512	RAS	SOT-23	-40~+125 °C
LR431BTSLS1G	1%	2.475~2.525	RBS	SOT-23	-40~+125 °C
LR431APTSLS1G	0.5%	2.483~2.507		SOT-23	-40~+125 °C
LR431BPTSLS1G	1%	2.470~2.520		SOT-23	-40~+125 °C

TYPICAL PERFORMANCE CHARACTERISTICS


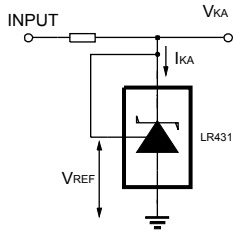
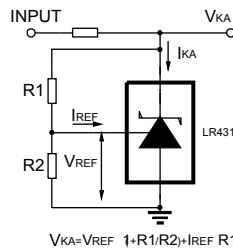
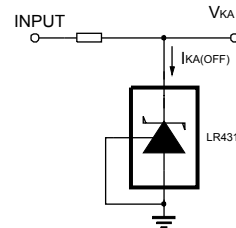
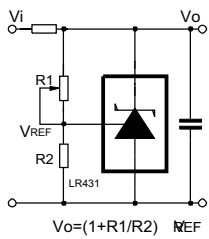
TEST CIRCUIT

 Fig 7 Test Circuit For $V_{KA}=V_{REF}$

 Fig 8 Test Circuit for $V_{KA} \geq V_{REF}$

 Fig 9 Test Circuit For $I_{KA(OFF)}$
APPLICATION CIRCUIT


Fig 10 Shutdown Regulator

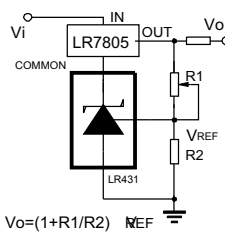


Fig 11 Output Control of a Three-Terminal Fixed Regulator

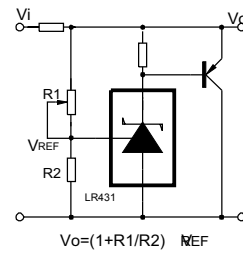


Fig 12 Higher-current Shunt Regulator

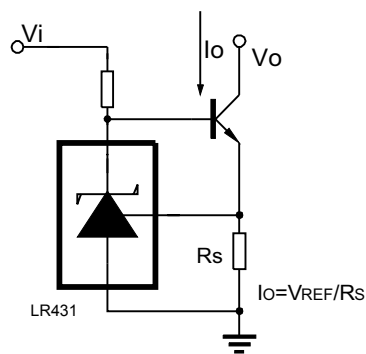


Fig 13 Constant-current Sink

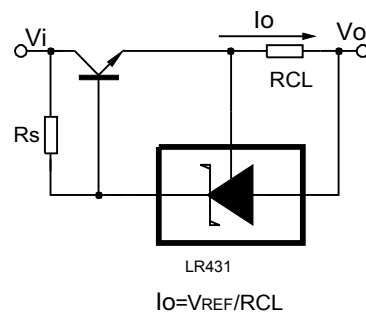
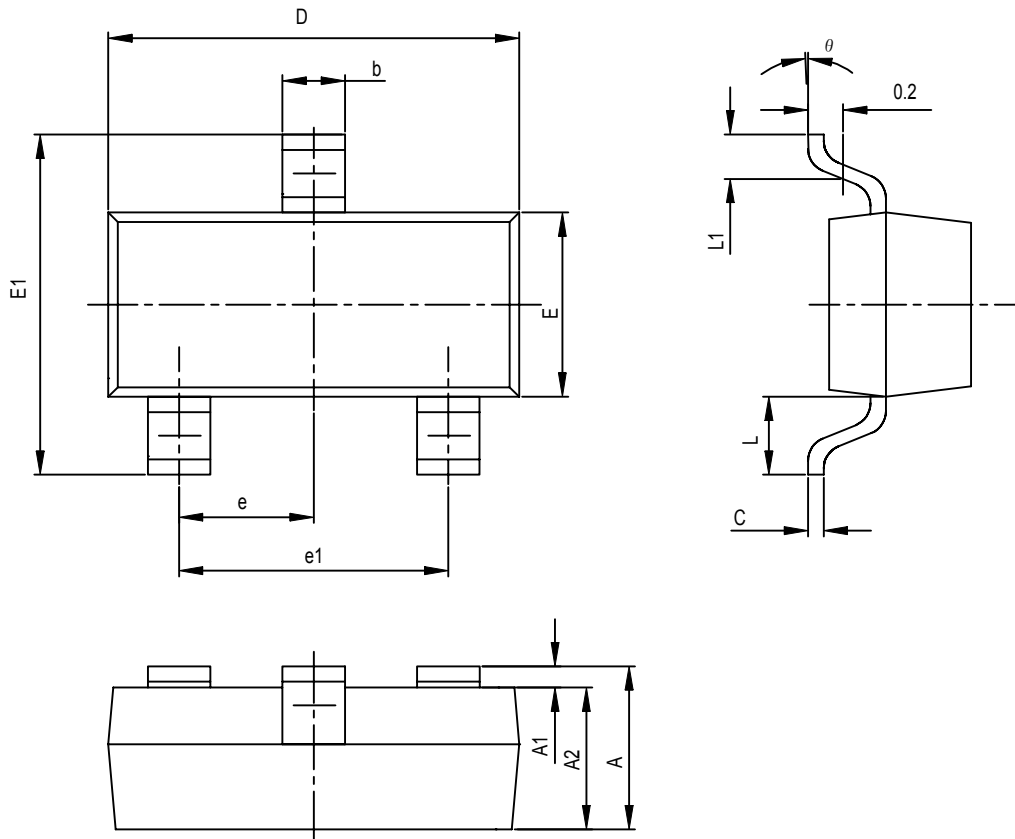


Fig 14 Current Limiting or Current Source

SOT-23 PACKAGE OUTLINE DIMENSIONS


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.100	0.110	0.118
E	1.200	1.610	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950TPY		0.037TPY	
e1	1.800	2.000	0.071	0.079
L	0.550REF		0.022REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°