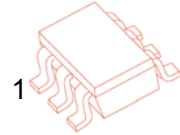


Digital transistors(built-in resistors)

SOT-363



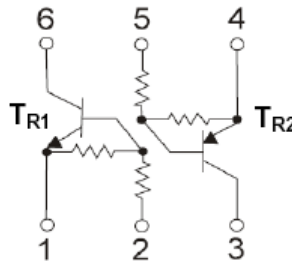
UMD15N DIGITAL TRANSISTOR (NPN+PNP)

FEATURES

- Both the DTC143E chip and DTA143E chip in a package
- Mounting possible with SOT-363 automatic mounting machines
- Transistor elements are independent, eliminating interference
- Mounting cost and area be cut in half

Marking: D15

Equivalent circuit



T_{R1} Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Value	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10~30	V
Output current	I _O	100	mA
	I _{C(MAX)}	100	
Power dissipation	Pd	150	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55~150	°C

T_{R1} Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input voltage	V _{I(off)}	0.5			V	V _{CC} =5V, I _O =100μA
	V _{I(on)}			3		V _O =0.3V, I _O =20mA
Output voltage	V _{O(on)}			0.3	V	I _O /I _I =10mA/0.5mA
Input current	I _I			1.8	mA	V _I =5V
Output current	I _{O(off)}			0.5	μA	V _{CC} =50V, V _I =0
DC current gain	G _I	20				V _O =5V, I _O =10mA
Input resistance	R ₁	3.29		6.11	KΩ	-
Resistance ratio	R ₂ /R ₁	0.8		1.2		-
Transition frequency	f _T		250		MHz	V _O =10V, I _O =5mA, f=100MHz

T_{R2} Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Value	Unit
Supply voltage	V _{CC}	-50	V
Input voltage	V _{IN}	-30~10	V
Output current	I _O	-100	mA
	I _{C(MAX)}	-100	
Power dissipation	P _d	150	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55~150	°C

T_{R2} Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	V _{I(off)}	-0.5			V	V _{CC} =-5V, I _O =-100μA
	V _{I(on)}			-3		V _O =-0.3V, I _O =-20mA
Output voltage	V _{O(on)}			-0.3	V	I _O /I _I =-10mA/-0.5mA
Input current	I _I			-1.8	mA	V _I =-5V
Output current	I _{O(off)}			-0.5	μA	V _{CC} =-50V, V _I =0
DC current gain	G _I	30				V _O =-5V, I _O =-10mA
Input resistance	R ₁	3.29		6.11	KΩ	-
Resistance ratio	R ₂ /R ₁	0.8		1.2		-
Transition frequency	f _T		250		MHz	V _O =-10V, I _O =-5mA, f=100MHz