

# μPD5753T7G

SiGe/CMOS Integrated Circuit

4 × 2 IF Switch Matrix with Tone/Voltage Controller

R09DS0014EJ0100

Rev.1.00

Feb 22, 2011

## FEATURES

- 4 independent IF channels, integral switching to channel input to either channel output
- 4 × 2 switch matrix with integrated switch control - Tone/Voltage  
- Switch's Enable/Disable function is linked with POLA input voltage level
- Switch's Enable condition :  $V_{POLA} > 9.5 \text{ V}$
- Frequency range :  $f = 250 \text{ MHz to } 2150 \text{ MHz}$
- High isolation :  $ISL_{D/U} = 33 \text{ dB TYP. @Worst mode}$
- Insertion loss :  $L_{INS} = 7 \text{ dB TYP. @ } Z_S = Z_L = 50 \Omega$
- Insertion loss flatness :  $\Delta L_{INS} = 1.0 \text{ dB TYP.}$
- 20-pin 4 × 4 mm square micro lead package ( 20-pin plastic QFN (0.5 mm pitch))

## APPLICATIONS

- DBS IF switching
- Multiswitch, Switch box
- 4 × 2 switching application for microwave signal

## ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPD5753T7G-E1	μPD5753T7G-E1-A	20-pin plastic QFN (0.5 mm pitch) (Pb-Free)	D5753	<ul style="list-style-type: none"> <li>• Embossed tape 12 mm wide</li> <li>• Pin 6 to 10 face the perforation side of the tape</li> <li>• Qty 5 kpcs/reel</li> <li>• Dry packing specification (MSL 3 Equivalent)</li> </ul>

**Remark** To order evaluation samples, please contact your nearby sales office.

Part number for sample order: μPD5753T7G

### CAUTION

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	Ratings	Unit
Supply Voltage	V <sub>DD</sub>	+4.0	V
Logic mode Control Voltage (MO and M1)	V <sub>MO</sub> , V <sub>M1</sub>	+4.0	V
Power Dissipation <sup>Note</sup>	P <sub>D</sub>	325	mW
Storage Temperature	T <sub>stg</sub>	-55 to +125	°C
Operating Ambient Temperature	T <sub>A</sub>	-40 to +85	°C
Input Power	P <sub>in</sub>	+15	dBm
POLA Control Input Voltage (POLA1 and POLA2)	V <sub>POLA</sub>	+25	V
TONE Signal Input Voltage	V <sub>TONE</sub>	1	V <sub>p-p</sub>

Note: Mounted on double-sided copper-clad 50 × 50 × 0.51 mm laminates PWB, T<sub>A</sub> = +85°C

**RECOMMENDED OPERATING RANGE (T<sub>A</sub> = +25°C, unless otherwise specified)**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>DD</sub>	+3.0	+3.3	+3.6	V
Operating Ambient Temperature	T <sub>A</sub>	-40	+25	+85	°C
POLA Control Input Voltage	V <sub>POLA</sub>	0	-	21	V
TONE Signal Frequency	f <sub>TONE</sub>	18	22	26	kHz
TONE Signal Input Voltage	V <sub>TONE</sub>	0.4	0.6	0.8	V <sub>p-p</sub>

**ELECTRICAL CHARACTERISTICS**

(T<sub>A</sub> = +25°C, V<sub>DD</sub> = +3.3 V, Z<sub>S</sub> = Z<sub>L</sub> = 50 Ω for each port, Worst mode, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Supply Current	I <sub>DD</sub>	Non-RF, Non control Signal/Voltage	-	1.9	3.0	mA
Insertion Loss 1	L <sub>INS1</sub>	P <sub>in</sub> = 0 dBm, f = 0.95 GHz	-	6.5	8.5	dB
Insertion Loss 2	L <sub>INS2</sub>	P <sub>in</sub> = 0 dBm, f = 2.15 GHz	-	7.5	9.5	dB
Isolation D/U Ratio 2 <sup>Note</sup>	ISL <sub>D/U2</sub>	P <sub>in</sub> = 0 dBm, f = 2.15 GHz	28	33	-	dB
Output Return Loss 1	RL <sub>out1</sub>	P <sub>in</sub> = 0 dBm, f = 0.95 GHz	15	25	-	dB
Output Return Loss 2	RL <sub>out2</sub>	P <sub>in</sub> = 0 dBm, f = 2.15 GHz	10	13	-	dB
POLA Control Threshold Voltage, Channel Selection	V <sub>th_POLA</sub>	OFF to ON	14	14.75	15.5	V
TONE Signal Threshold Voltage, Channel Selection	V <sub>th_TONE</sub>	f <sub>TONE</sub> = 22 kHz, Duty Cycle = 50%, pulse wave, OFF to ON	0.1	0.15	0.35	V <sub>p-p</sub>

Note: Isolation D/U (Desire/U<sub>n</sub>-desire) ratio = |(Signal Leakage (off-state)) - (Insertion loss (on-state))| at Worst mode

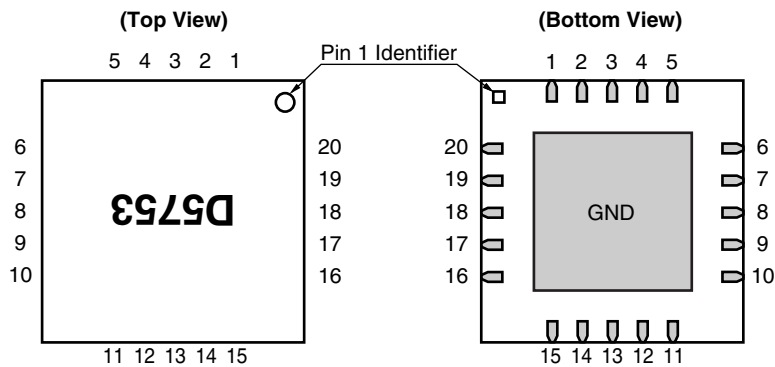
**STANDARD CHARACTERISTICS FOR REFERENCE**

( $T_A = +25^\circ\text{C}$ ,  $V_{DD} = +3.3\text{ V}$ ,  $Z_S = Z_L = 50\ \Omega$  for each port, Worst mode, unless otherwise specified)

Parameter	Symbol	Test Conditions	Reference Value	Unit
Insertion Loss Flatness	$\Delta L_{INS}$	$ L_{INS1} - L_{INS2} $	1.0	dB
Isolation D/U Ratio 1 <sup>Note</sup>	ISL <sub>D/U1</sub>	$P_{in} = 0\text{ dBm}$ , $f = 0.95\text{ GHz}$	40	dB
Input Return Loss 1	RL <sub>in1</sub>	$P_{in} = 0\text{ dBm}$ , $f = 0.95\text{ GHz}$	20	dB
Input Return Loss 2	RL <sub>in2</sub>	$P_{in} = 0\text{ dBm}$ , $f = 2.15\text{ GHz}$	14	dB
POLA Control Current	I <sub>POLA</sub>	$V_{POLA} = 21\text{ V}$	230	μA
POLA Switching Time	T <sub>POLA</sub>	$V_{POLA} = 18\text{ V}$ , OFF to ON	0.75	μs
TONE Switching Time	T <sub>TONE</sub>	$f_{TONE} = 22\text{ kHz}$ , Duty Cycle = 50%, pulse wave, $V_{TONE} = 600\text{ mV}_{p-p}$ , OFF to ON	220	μs

Note: Isolation D/U (Desire/Un-desire) ratio = |(Signal Leakage (off-state)) – (Insertion loss (on-state))| at Worst mode

## PIN CONNECTIONS



Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name
1	TONE1	6	IN-B	11	GND	16	OUT2
2	POLA1	7	GND	12	IN-C	17	M1
3	GND	8	GND	13	GND	18	V <sub>DD</sub>
4	IN-A	9	GND	14	POLA2	19	M0
5	GND	10	IN-D	15	TONE2	20	OUT1

**Remark** Heat Sink (Bottom side) : GND

**TRUTH TABLE**

**SWITCHING CONTROL OF OUT1 SIGNAL PATH**

Logic Pattern Select		CONTROL PINS for OUT1		OUT1
M0	M1	POLA1	TONE1	Output Signal
0	0	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-C
		Low	0	IN-D
		High	0	IN-B
		High	22 kHz	IN-A
1	1	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-A
		Low	0	IN-B
		High	0	IN-D
		High	22 kHz	IN-C
0	1	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-D
		Low	0	IN-C
		High	0	IN-B
		High	22 kHz	IN-A
1	0	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-B
		Low	0	IN-A
		High	0	IN-D
		High	22 kHz	IN-C

**SWITCHING CONTROL OF OUT2 SIGNAL PATH**

Logic Pattern Select		CONTROL PINS for OUT2		OUT2
M0	M1	POLA2	TONE2	Output Signal
0	0	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-C
		Low	0	IN-D
		High	0	IN-B
		High	22 kHz	IN-A
1	1	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-A
		Low	0	IN-B
		High	0	IN-D
		High	22 kHz	IN-C
0	1	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-D
		Low	0	IN-C
		High	0	IN-B
		High	22 kHz	IN-A
1	0	No Voltage	22 kHz	None
		No Voltage	0	None
		Low	22 kHz	IN-B
		Low	0	IN-A
		High	0	IN-D
		High	22 kHz	IN-C

**Remarks** M0, M1 : "0" : 0 V dc (Connected to GND line)  
 "1" : V<sub>DD</sub> dc (Connected to V<sub>DD</sub> line)

V<sub>DD</sub> = + 3.3 V dc

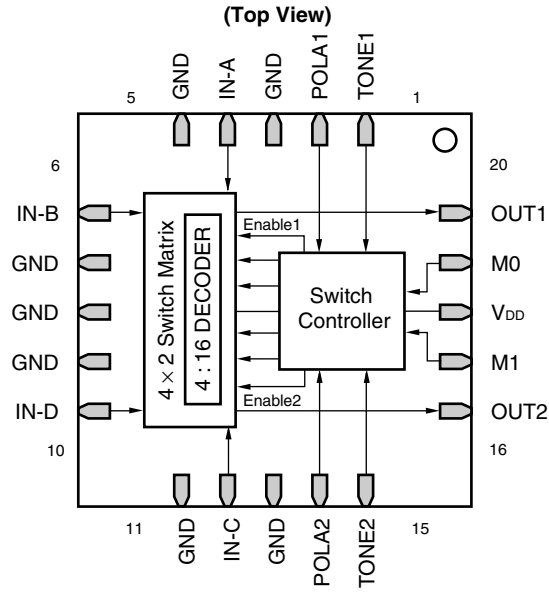
POLA1, 2 : "Low" : 9.5 V to 14 V dc  
 "High" : 15.5 V to 19 V dc

"No Voltage" : 0 V dc (< 5 V dc) or Open

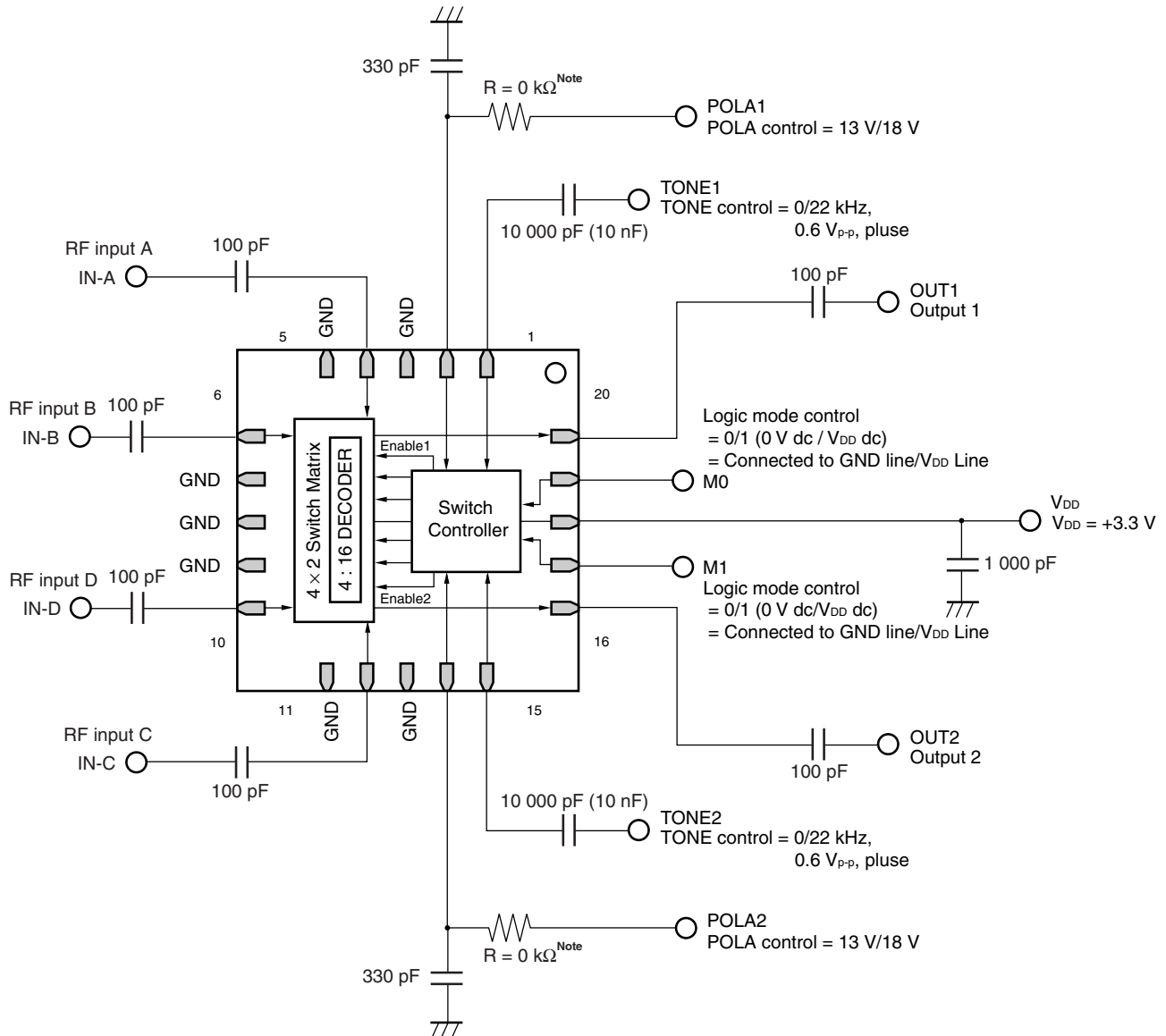
Switch's Enable/Disable function is linked with POLA input voltage level

Switch's Enable condition : V<sub>POLA</sub> > 9.5 V

# FUNCTIONAL DIAGRAM



## EVALUATION CIRCUIT



Note: R = 0 kΩ (at POLA control = 13 V/18 V)  
 = 5.6 kΩ (at POLA control = 14 V/18 V)

**Remarks** Heat Sink (Bottom Side): GND

$Z_S = Z_L = 50 \Omega$

Switch's Enable/Disable function is linked with POLA input voltage level

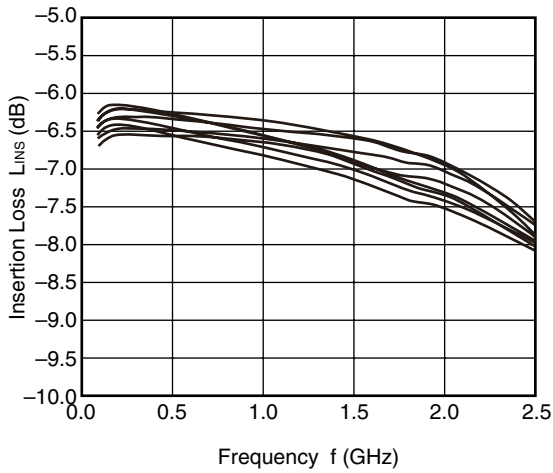
Switch's Enable condition :  $V_{POLA} > 9.5 \text{ V}$

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

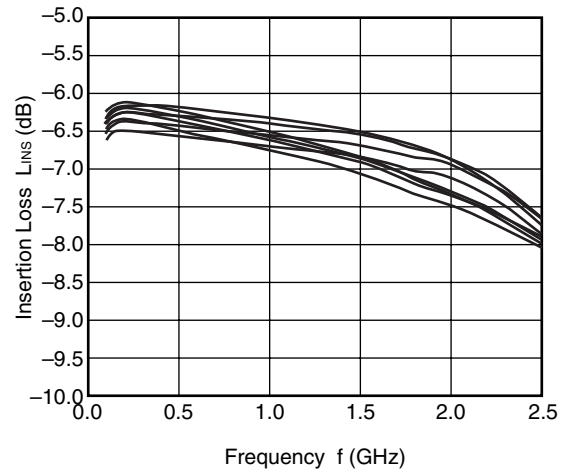
### TYPICAL CHARACTERISTICS

( $T_A = +25^\circ\text{C}$ ,  $V_{DD} = +3.3\text{ V}$ ,  $P_{in} = 0\text{ dBm}$ ,  $Z_S = Z_L = 50\ \Omega$  for each port, unless otherwise specified)

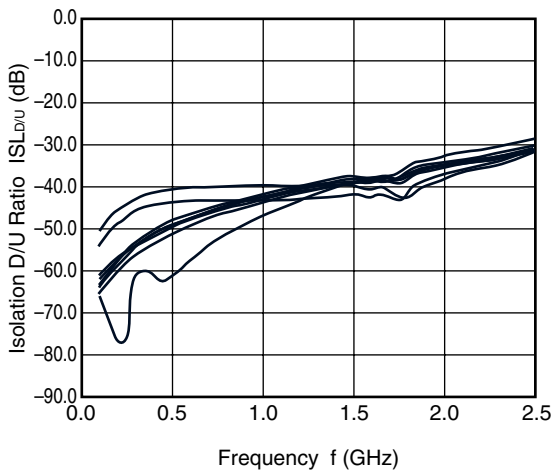
INSERTION LOSS vs. FREQUENCY  
(IN-x - OUT1)



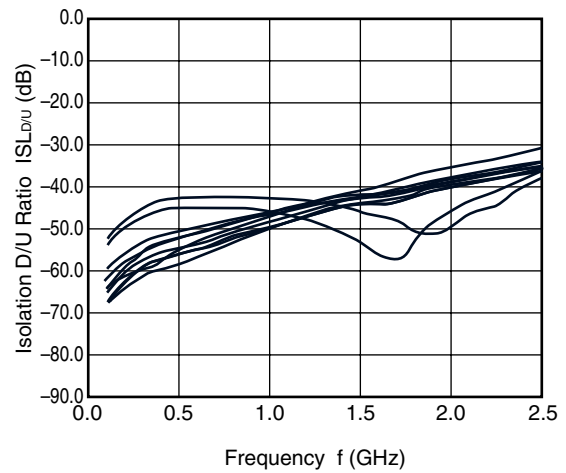
INSERTION LOSS vs. FREQUENCY  
(IN-x - OUT2)



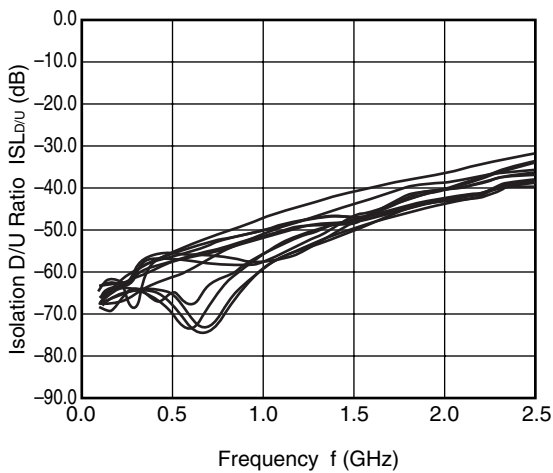
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-A - OUT1)



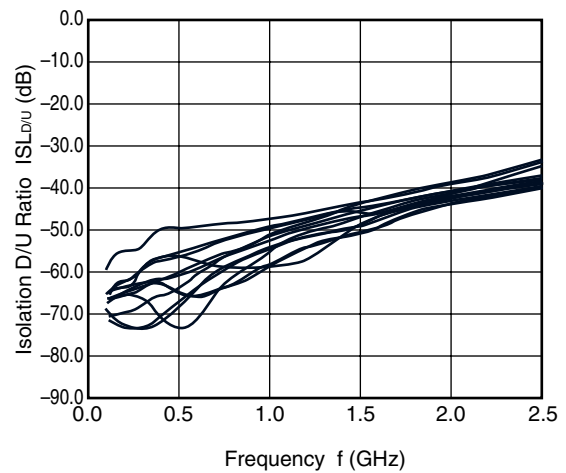
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-B - OUT1)



ISOLATION D/U RATIO vs. FREQUENCY  
(IN-C - OUT1)



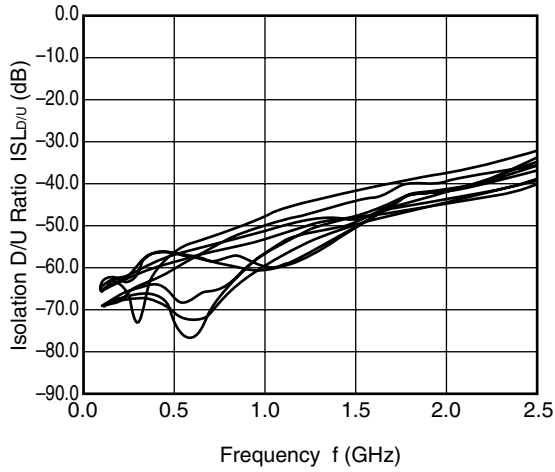
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-D - OUT1)



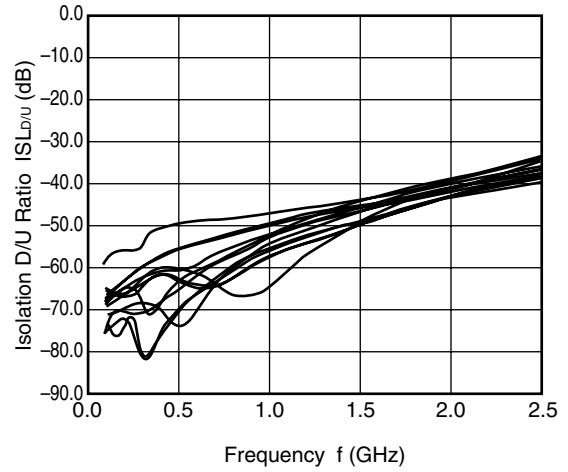
**Remark** The graphs indicate nominal characteristics.



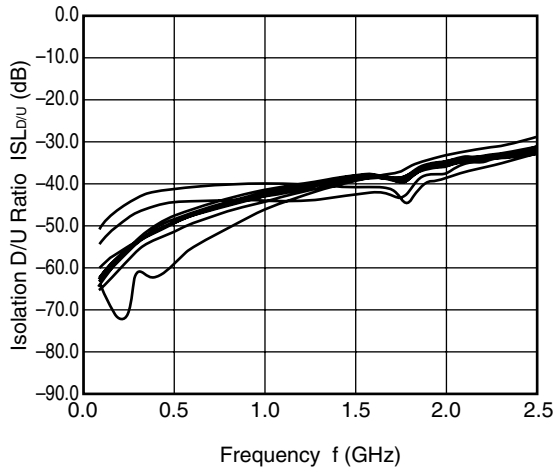
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-A - OUT2)



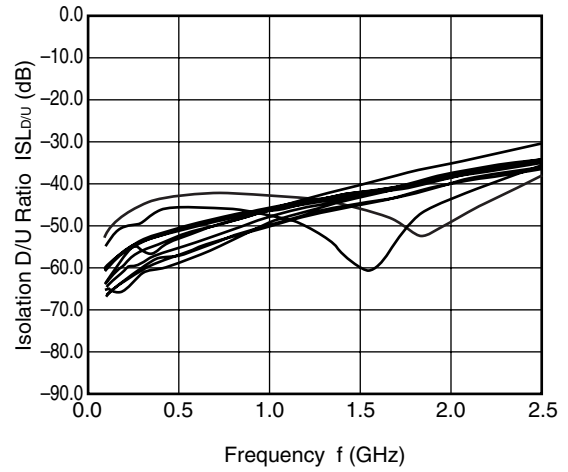
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-B - OUT2)



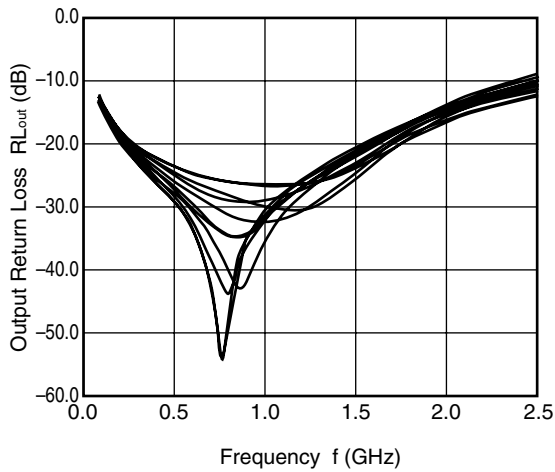
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-C - OUT2)



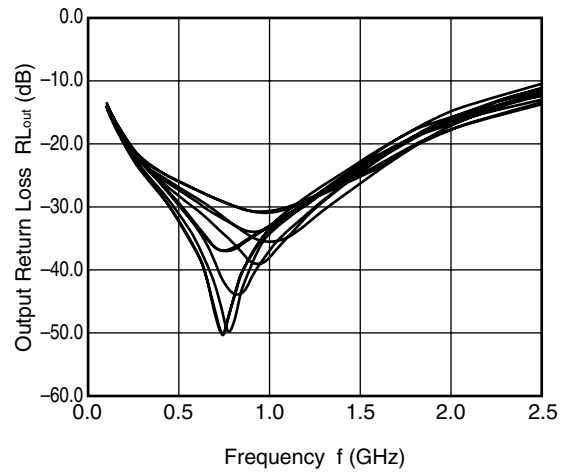
ISOLATION D/U RATIO vs. FREQUENCY  
(IN-D - OUT2)



OUTPUT RETURN LOSS vs. FREQUENCY  
(IN-x - OUT1)

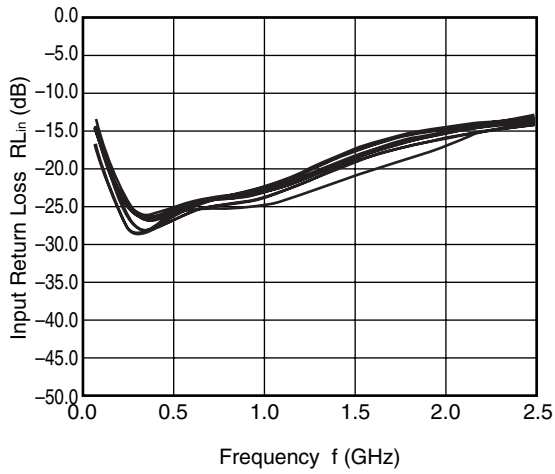


OUTPUT RETURN LOSS vs. FREQUENCY  
(IN-x - OUT2)

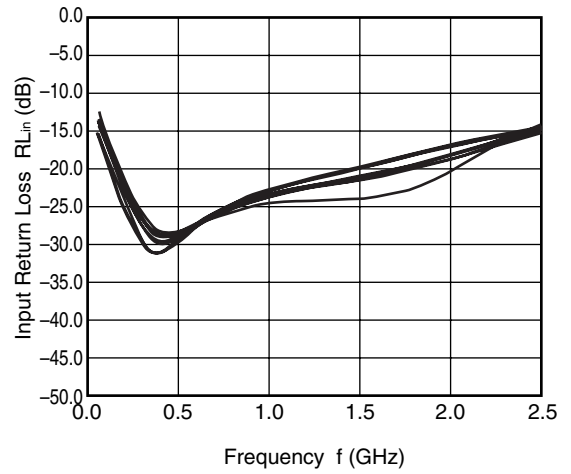


**Remark** The graphs indicate nominal characteristics.

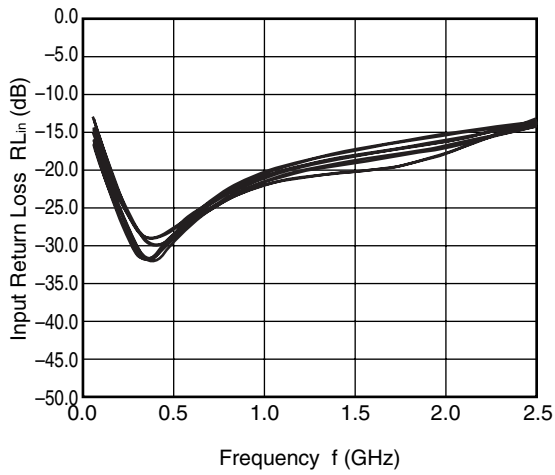
INPUT RETURN LOSS vs. FREQUENCY  
(IN-A - OUTx)



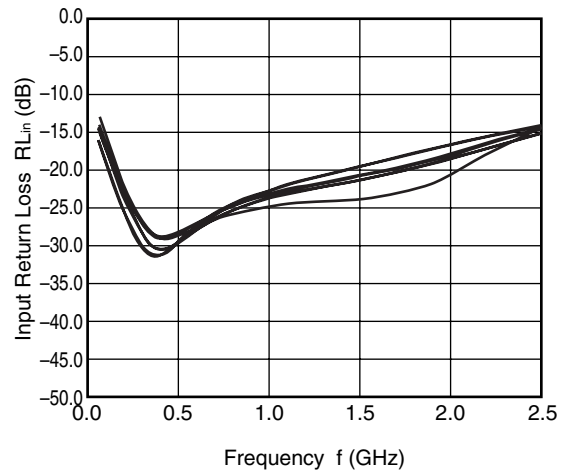
INPUT RETURN LOSS vs. FREQUENCY  
(IN-B - OUTx)



INPUT RETURN LOSS vs. FREQUENCY  
(IN-C - OUTx)



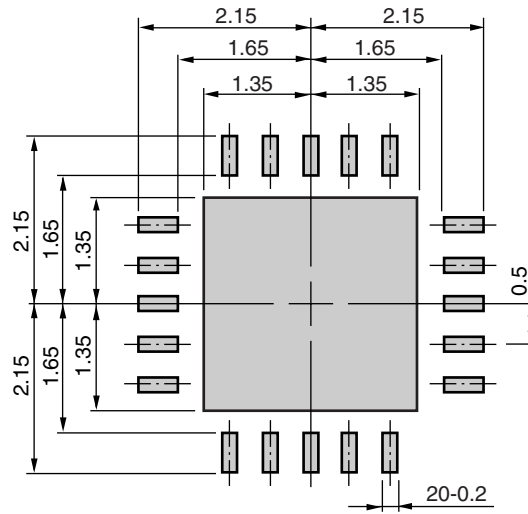
INPUT RETURN LOSS vs. FREQUENCY  
(IN-D - OUTx)



**Remark** The graphs indicate nominal characteristics.

## MOUNTING PAD LAYOUT DIMENSIONS

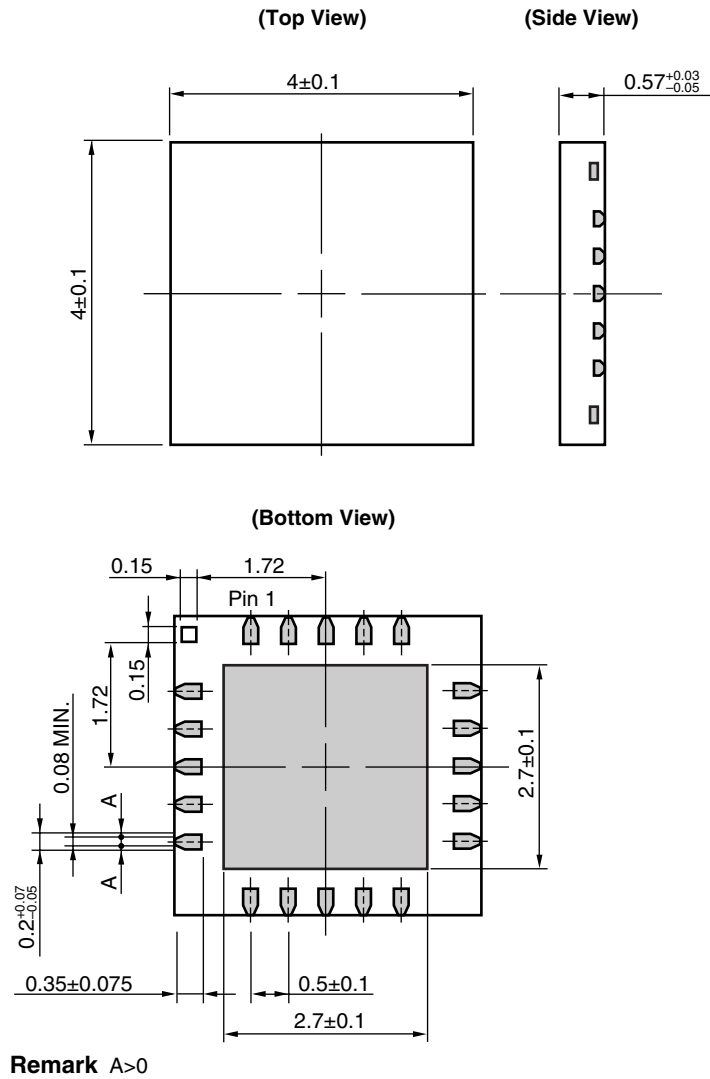
20-PIN 4 × 4 mm SQUARE MICRO LEAD PACKAGE (20-PIN PLASTIC QFN (0.5 mm pitch)) (UNIT: mm)



**Remark** The mounting pad layout in this document is for reference only.

## PACKAGE DIMENSIONS

20-PIN 4 × 4 mm SQUARE MICRO LEAD PACKAGE (20-PIN PLASTIC QFN (0.5 mm pitch)) (UNIT: mm)



## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Partial Heating	Peak temperature (package surface temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

### CAUTION

Do not use different soldering methods together (except for partial heating).

<b>Revision History</b>	<b>μPD5753T7G Data Sheet</b>
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<b>Rev.</b>	<b>Date</b>	<b>Description</b>	
		<b>Page</b>	<b>Summary</b>
1.00	Feb 22, 2011	–	First edition issued

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