

**ROHS** 

HALOGEN

FREE

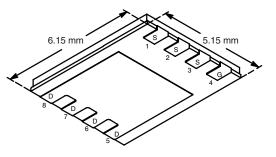
Available

Vishay Siliconix

# N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)		
100	0.034 at V <sub>GS</sub> = 10 V	7.8		
	0.040 at V <sub>GS</sub> = 6.0 V	7.2		

### PowerPAK<sup>®</sup> SO-8



Bottom View

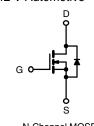
Ordering Information: Si7454DP-T1-E3 (Lead (Pb)-free) Si7454DP-T1-GE3 (Lead (Pb)-free and Halogen-free)

### FEATURES

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFETs
- New Low Thermal Resistance PowerPAK<sup>®</sup> Package with Low 1.07 mm Profile
- PWM Optimized for Fast Switching
- 100 % R<sub>g</sub> Tested

#### **APPLICATIONS**

- Primary Side Switch for High Density DC/DC
- Telecom/Server 48 V, Full-/Half-Bridge DC/DC
- Industrial and 42 V Automotive



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unles	ss otherwise n	oted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	100		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Continuous Drain Current (T <sub>.1</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	7.8	5.0	
Continuous Drain Current $(1) = 150^{\circ}$ C)	T <sub>A</sub> = 85 °C		5.7	3.6	А
Pulsed Drain Current		I <sub>DM</sub>	30		~
Avalanche Current	rent L = 0.1 mH		25		
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mm	E <sub>AS</sub>	31		mJ
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	4.0	1.6	А
Maximum Dawar Dissinctional	T <sub>A</sub> = 25 °C	PD	4.8	1.9	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C	۰D	2.6	1.0	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C
Soldering Recommendations (Peak Temperature) <sup>b, c</sup>			260		

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum hundling to Angleing 18	t ≤ 10 s	R <sub>thJA</sub>	21 26		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	55	65	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.6	2	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. See Solder Profile (<u>www.vishay.com/ppg?73257</u>). The PowerPAK SO-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

Dynamic<sup>b</sup> **Total Gate Charge** Gate-Source Charg Gate-Drain Charge Gate Resistance Turn-On Delay Time

**Rise Time** 

Fall Time

Turn-Off Delay Time

Source-Drain Reverse Recovery

## Vishay Silico

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<b>SPECIFICATIONS</b> $T_J = 25$	°C, unless	otherwise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	2		4	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zana Oata Maltana Duain Ourmant	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$		1		
Zero Gate Voltage Drain Current		$V_{DS}$ = 100 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			20	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5$ V, $V_{GS}$ = 10 V	30			А
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.8 A		0.028	0.034	0
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = 6.0 \text{ V}, \text{ I}_{D} = 7.2 \text{ A}$		0.032	0.040	Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 7.8 A		25		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = 4 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic <sup>b</sup>						
Total Gate Charge	Qg			24	30	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 50 V, $V_{GS}$ = 10 V, $I_{D}$ = 7.8 A		7.6		nC
Gate-Drain Charge	Q <sub>gd</sub>			5.4		
Gate Resistance	Rg		0.5	1.25	2.2	Ω

Time Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

 $V_{DD}$  = 50 V,  $R_L$  = 50  $\Omega$  $I_D \cong 1.0 \text{ A}, V_{GEN} = 10 \text{ V}, \text{ R}_a = 6 \Omega$ 

 $I_F = 4 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$ 

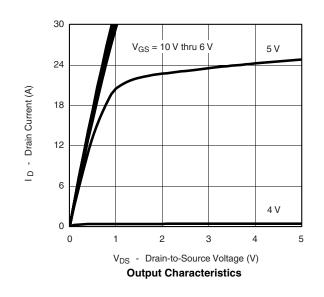
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

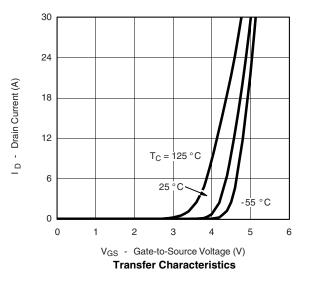
t<sub>d(on)</sub> t<sub>r</sub>

t<sub>d(off)</sub>

t<sub>f</sub>

t<sub>rr</sub>





16

10

35

20

50

30

20

70

40

80

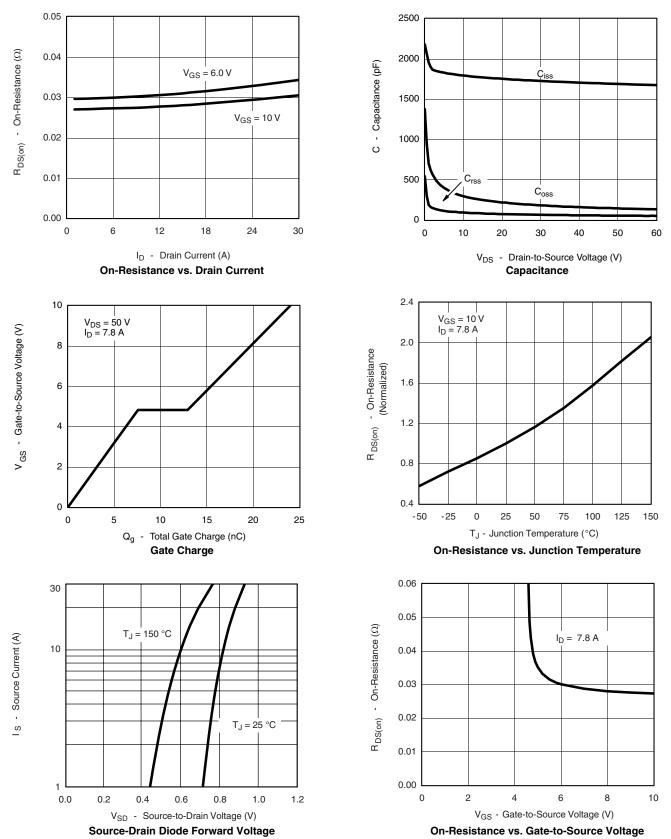
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# Si7454DP

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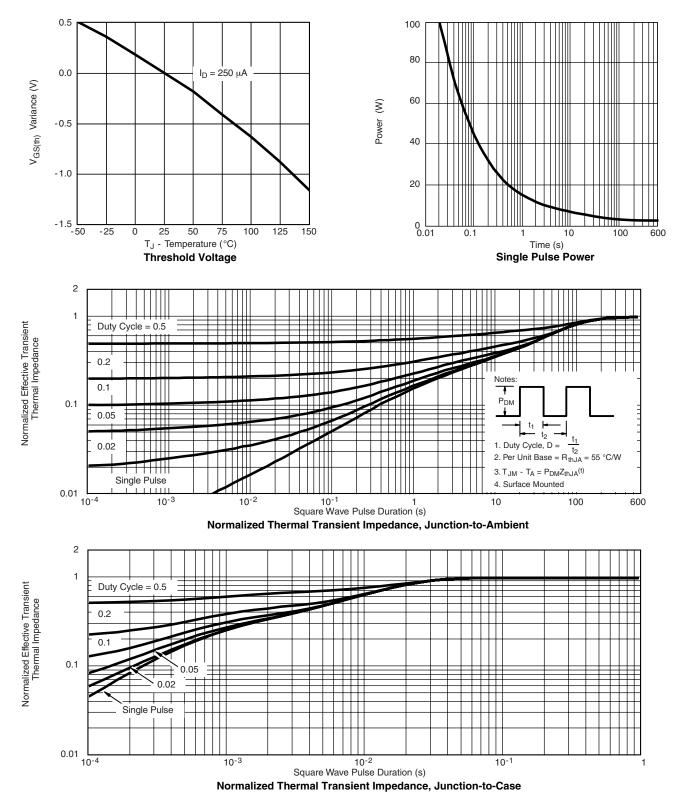


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### Vishay Siliconix



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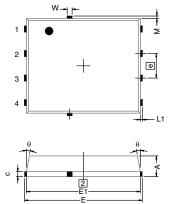


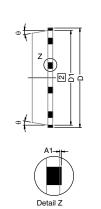
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?71618">www.vishay.com/ppg?71618</a>.

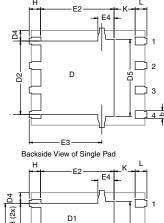


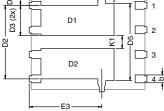
**Vishay Siliconix** 

# PowerPAK<sup>®</sup> SO-8, (Single/Dual)









Backside View of Dual Pad

Notes

1. Inch will govern.

2 Dimensions exclusive of mold gate burrs.

3. Dimensions exclusive of mold flash and cutting burrs.

DIM.	MILLIMETERS			INCHES			
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
А	0.97	1.04	1.12	0.038	0.041	0.044	
A1		-	0.05	0	-	0.002	
b	0.33	0.41	0.51	0.013	0.016	0.020	
С	0.23	0.28	0.33	0.009	0.011	0.013	
D	5.05	5.15	5.26	0.199	0.203	0.207	
D1	4.80	4.90	5.00	0.189	0.193	0.197	
D2	3.56	3.76	3.91	0.140	0.148	0.154	
D3	1.32	1.50	1.68	0.052	0.059	0.066	
D4	0.57 typ.				0.0225 typ.		
D5		3.98 typ.			0.157 typ.		
E	6.05	6.15	6.25	0.238	0.242	0.246	
E1	5.79	5.89	5.99	0.228	0.232	0.236	
E2 (for AL product)	3.30	3.48	3.66	0.130	0.137	0.144	
E2 (for other product)	3.48	3.66	3.84	0.137	0.144	0.151	
E3	3.68	3.78	3.91	0.145	0.149	0.154	
E4 (for AL product)		0.58 typ.		0.023 typ.			
E4 (for other product)		0.75 typ.		0.030 typ.			
е	1.27 BSC			0.050 BSC			
K (for AL product)	1.45 typ.			0.057 typ.			
K (for other product)	1.27 typ.			0.050 typ.			
K1	0.56	-	-	0.022	-	-	
Н	0.51	0.61	0.71	0.020	0.024	0.028	
L	0.51	0.61	0.71	0.020	0.024	0.028	
L1	0.06	0.13	0.20	0.002	0.005	0.008	
θ	0°	-	12°	0°	-	12°	
W	0.15	0.25	0.36	0.006	0.010	0.014	
М	0.125 typ.			0.005 typ.			

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# Application Note 826

Vishay Siliconix

### RECOMMENDED MINIMUM PADS FOR PowerPAK® SO-8 Single



Recommended Minimum Pads Dimensions in Inches/(mm)

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