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The technical content of this austriamicrosystems datasheet is still valid.

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AS1901, AS1902, AS1903 Ultra Low-Power µP Supervisory Circuit

1 General Description

The AS1901/2/3 family is an ultra low-power supervisory circuit device.

The device can be used to monitor the supply voltage of digital systems and microprocessors and initiate a reset when the voltage goes below a predefined threshold. The duration of the reset is 5/20/100/500ms (typ.) after the supply voltage has risen above the threshold.

The AS1901/2/3 exhibit excellent reliability and can reduce application costs by eliminating all external components.

The device is available with different output drivers:

- The AS1901 has a push-pull driver with an active low reset.
- The AS1902 uses the same output stage as the AS1901, but has an active high reset.
- The AS1903 has an open drain output with active low reset.

All devices operate down to a voltage of 1V.

The reset thresholds are factory-trimmable between 2.2V and 3.08V in steps of approximately 100mV.

Each device of the AS1901/2/3 family is offered with four time-out periods of 5/20/100/500ms.

The extremely low current consumption of only 230nA (typ) at 3.3V makes the device ideal for use in portable applications.

All devices are available in a 3-pin SOT23 package.

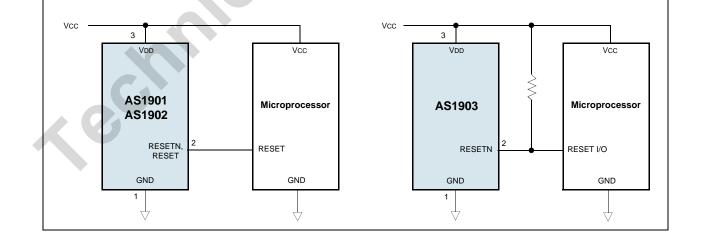
Figure 1. Functional Block Diagrams

2 Key Features

- Low 230nA (typ) Supply Current
- Precision Monitoring of 2.5-, 3-, and 3.3V-Power Supplies
- Supply Voltage Range: 1.0 to 3.6V
- Reset Threshold Range: 2.2 to 3.1V
- Available in 3 Versions:
 - AS1901 Push Pull RESETN
 - AS1902 Push Pull RESET
 - AS1903 Open Drain RESETN
- 4 Time-Out Periods Ranging from 5 to 500ms
- Detection Voltage Accuracy: ±1.5%
- Temperature Range: -40 to +85°C
- Package: SOT23-3

3 Applications

The devices are ideal for computers, intelligent instruments, controllers, critical microprocessor and microcontroller, power monitoring and portable/batterypowered equipment.



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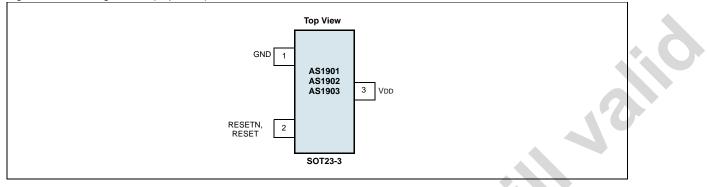
Data Sheet - Pinout

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4 Pinout

Pin Assignments

Figure 2. Pin Assignments (Top View)



Pin Descriptions

Table 1. Pin Descriptions

	010110				
Pin Number AS1901/AS1903 AS1902		Pin Name	Description		
		Pin Name			
1	1	GND	Ground		
2	-	RESETN	Active-low reset output. RESETN remains low while VDD i below the reset threshold and for tTP after VDD rises above the reset threshold. RESETN is open-drain on the AS1900 and push-pull on the AS1901.		
-	2	RESET	Active-high reset output. RESET remains high while VDD i below the reset threshold and for tTP after VDD rises above the reset threshold.		
3	3	Vdd	Supply voltage.		

Data Sheet - Absolute Maximum Ratings

5 Absolute Maximum Ratings

Stresses beyond those listed in Table 2 may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in Section 6 Electrical Characteristics is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package-Body Peak Temperature +260 °C <i>IPC/JEDEC J-STD-020C "Moisture/Reflow</i>	Parameter	Limits	Unit	Notes
Input Current (VDD)20mAOutput Current (RESET, RESETN)20mARate of Rise (VDD)100V/µsOperating Temperature Range (TA)-40 to +85°CStorage Temperature Range-65 to +150°CPackage-Body Peak Temperature+260°CPackage-Body Peak Temperature+260°C	VDD to GND	-0.3 to +5	V	
Output Current (RESET, RESETN) 20 mA Rate of Rise (VDD) 100 V/µs Operating Temperature Range (TA) -40 to +85 °C Storage Temperature Range -65 to +150 °C Package-Body Peak Temperature +260 °C The reflow peak soldering temperature (bod temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermeticical sensitivity Classification for non-hermetical senstrule for thermatical sensitivity Classific	RESET/RESETN to GND	-0.3 to VDD + 0.3	V	
Rate of Rise (VDD) 100 V/µs Operating Temperature Range (TA) -40 to +85 °C Storage Temperature Range -65 to +150 °C Package-Body Peak Temperature +260 °C The reflow peak soldering temperature (bod temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic	Input Current (VDD)	20	mA	
Operating Temperature Range (TA) -40 to +85 °C Storage Temperature Range -65 to +150 °C Package-Body Peak Temperature +260 °C The reflow peak soldering temperature (bod temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic	Output Current (RESET, RESETN)	20	mA	
Storage Temperature Range -65 to +150 °C Package-Body Peak Temperature +260 °C The reflow peak soldering temperature (bod temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic	Rate of Rise (VDD)	100	V/µs	
Package-Body Peak Temperature +260 °C The reflow peak soldering temperature (body temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic	Operating Temperature Range (TA)	-40 to +85	°C	
Package-Body Peak Temperature +260 °C <i>IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic</i>	Storage Temperature Range	-65 to +150	°C	
	Package-Body Peak Temperature	+260	°C	The reflow peak soldering temperature (body temperature) specified is in accordance with IPC/JEDEC J-STD-020C "Moisture/Reflow Sensitivity Classification for non-hermetic Solid State Surface Mount Devices"

Table 2. Absolute Maximum Ratings (TA = 25°C Unless Otherwise Noted)

Data Sheet - Electrical Characteristics

6 Electrical Characteristics

VDD = Full Range; TA = -40 to +85℃; Unless Otherwise Specified

Table 3. Electrical Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
VDD Range	Vdd	$TA = 0 \text{ to } +70^{\circ}C$	1.0		3.6	V
VDD Kange		TA = -40 to +85°C	1.2		3.6	v
Supply Current (AS1901/2/3)	Idd	VDD = 3.3V, No Load		230	430	nA
Reset Threshold Voltage ¹	Vтн	TA = +25°C	Vтн - 1.5%	Vтн	Vтн + 1.5%	V
Reset miesnoù vollage	VIII	TA = -40 to +85°C	Vтн - 2.5%	Vтн	Vтн + 2.5%	
VDD to Reset Delay ²	tRD	VDD = VTH to (VTH - 100mV)		20	50	μs
Reset Active Time-Out Period ³	tтр		ttp - 40%	tTP	ttp + 60%	ms
RESETN Output Voltage (AS1901/3)	Vol	ISINK = 1.2mA, VDD = 2.1V, Reset Asserted			0.4	V
	VOL	ISINK = 400µA, VDD = 1.2V, Reset Asserted			0.4	V
RESETN Output Voltage (AS1901)	Vон	ISOURCE = 1.2mA, VDD = 3.2V	0.8 x Vdd			V
	Vон	ISOURCE = 500µA, VDD = 2.1V, Reset Asserted	0.8 x VDD			V
RESET Output Voltage (AS1902)	VOH	ISOURCE = 100µA, VDD = 1.2V, Reset Asserted	0.8 x Vdd			V
	Vol	ISINK = 1.2mA, VDD = 3.2V, Reset Not Asserted			0.4	V
RESET Threshold Hysteresis	VHYST			10		mV
Open-Drain RESETN Output Leakage Current (AS1903)	ILEAK				0.1	μA

Notes:

¹ See Table 6, "Coding of Factory-Trimmed Reset Threshold Voltages," on page 6.

² Guaranteed by design.

³ See Table 4, "Coding of Factory-trimmed Reset Active Time-Out Period," on page 6.

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Data Sheet - Typical Operating Characteristics

7 Typical Operating Characteristics

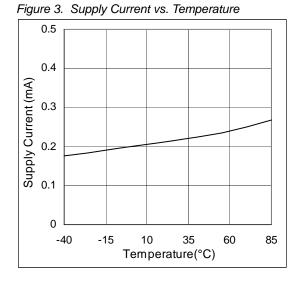
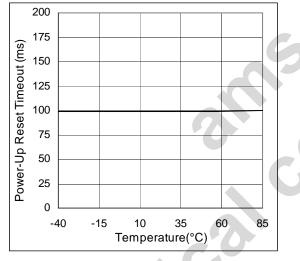
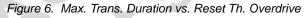


Figure 5. Power-Up Reset Timeout vs. Temperature



200 $V_{OD} = 10mV$ 175 150 Reset Delay (µs) 125 100 75 50 25 0 35 60 -40 -15 10 85 Temperature(°C)



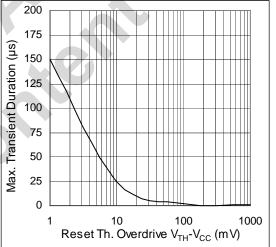


Figure 4. Power Down Reset Delay vs. Temp.

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Data Sheet - Detailed Description

8 Detailed Description

Interfacing to Microprocessors with Bi-Directional Reset Pins

The device has an open drain RESETN output, which enables easy interfacing to microprocessors (μ P) with bi-directional reset pins, such as the Motorola 68HC11. The RESETN pin of the microcontroller (μ C) can be connected directly to the μ P supervisor's RESETN output with a single pull-up resistor (see Figure 1 on page 1).

Negative-Going VDD Transients

The device is optimized to ignore short-duration, negative-going VDD transients (glitches) in order to avoid incorrect resets.

In the graph Maximum Transient Duration vs. Reset Threshold Overdrive (page 6), the conditions are shown, for which the reset pulses are not generated. In the graph the maximum pulse width that a negative VDD transient may have when a reset signal is generated. As the amplitude of the transient increases, the maximum allowable pulse width decreases.

Options

Device	Suffix	tTP in Milliseconds			
Device	Juliix	Min	Тур	Max	
AS190x_xx	A	3	5	8	
AS190x_xx	В	12	20	32	
AS190x_xx	С	60	100	160	
AS190x_xx	D	300	500	800	

Table 4. Coding of Factory-trimmed Reset Active Time-Out Period

Table 5. Output Variants

Device	Output Functionality
AS1901xxx	Active Low (RESETN)
AS1902xxx	Active High (RESET)
AS1903xxx	Open Drain (RESETN)

Table 6. Coding of Factory-Trimmed Reset Threshold Voltages

		Reset Threshold Voltage, VTH in V					
Device Suffix			TA = +25°C	TA = -40 to +85°C			
		Min	Тур	Max	Min	Мах	
AS190xx	22	2.167	2.2	2.233	2.145	2.255	
AS190xx	23	2.285	2.32	2.355	2.262	2.378	
AS190xx	24	2.364	2.4	2.436	2.340	2.460	
AS190xx	25	2.463	2.5	2.538	2.438	2.563	
AS190xx	26	2.591	2.63	2.669	2.564	2.696	
AS190xx	27	2.660	2.7	2.741	2.633	2.768	
AS190xx	28	2.758	2.8	2.842	2.730	2.870	
AS190xx_	29	2.886	2.93	2.974	2.857	3.003	
AS190xx	30	2.955	3.0	3.045	2.925	3.075	
AS190xx	31	3.034	3.08	3.126	3.003	3.157	

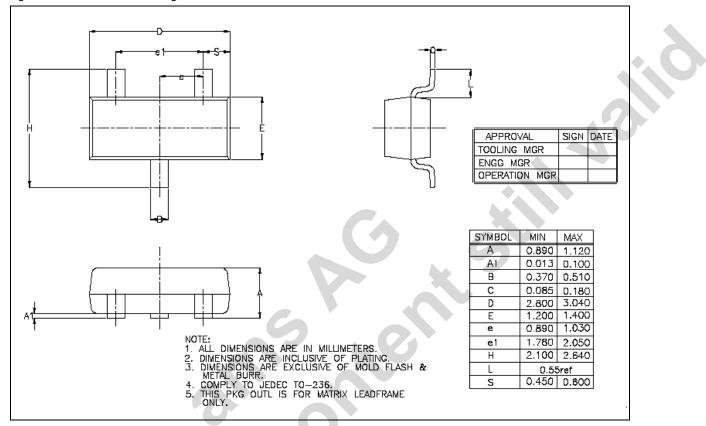
Data Sheet - Package Drawings and Markings



9 Package Drawings and Markings

The device is available in a 3-pin SOT-23 package.

Figure 7. 3-Pin SOT-23 Package.



Data Sheet - Ordering Information

10 Ordering Information

The device is available in the following standard versions.

Standard Part	Threshold	Duration	Marking
AS1901C23	2.32	100ms	ASBA
AS1901C26	2.63	100ms	ASBD
AS1901C31	3.08	100ms	ASBI
AS1902C23	2.32	100ms	ASBK
AS1902C26	2.63	100ms	ASBN
AS1902C31	3.08	100ms	ASBS
AS1903C23	2.32	100ms	ASBU
AS1903C26	2.63	100ms	ASBX
AS1903C31	3.08	100ms	ASB3

Non-standard versions require a minimum order of 30,000 units. Contact austriamicrosystems for availability of non-standard versions.

Data Sheet

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