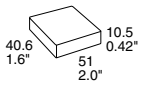
15 Watt DC-DC Converters

IMX 15 Series
IMY 15 Series

Wide input voltage ranges up to 150 V DC
1 or 2 outputs up to 48 V DC
1500...4000 V DC I/O electric strength test



- Extremely wide input voltage ranges
- Electrical isolation, also between outputs
- Emissions below EN 55022, level B
- Immunity to IEC/EN 61000-4-2,-3,-4,-5 and -6
- High efficiency (typ. 87%)
- Input undervoltage lock-out
- Shut down input, output voltages adjustable
- Flex power: Flexible load distribution on outputs
- Outputs no-load, overload and short-circuit proof
- Operating ambient temperature up to –40...85°C
- Thermal protection
- 2" × 1.6" case with 10.5 mm profile
- Supplementary insulation: 20/40 IMX 15 types
- Double or reinforced insulation: 110 IMY 15 types

Safety according to IEC/EN 60950, UL 1950

LGA c UL us Approvals pending CE[1]

[1] For 110 IMY 15 types

Summary

The IMX 15 and IMY 15 series of board mountable 15 Watt DC-DC converters has been designed according to the latest industry requirements and standards. The converters are particularly suitable for use in mobile or stationary applications in transport, railways, industry, or telecommunication where variable input voltages or high transient voltages are prevalent.

Covering a total input voltage range from 8.4 up to 150 V with 3 different types, the units are available with single, dual and electrically isolated double outputs from 3.3 up to 48 V externally adjustable, with flexible load distribution on dual and double output units. A shut down input allows remote converter on-off. Features include consistently high efficiency over the entire input voltage range, high reliability and excellent dynamic response to load and line changes.

The converters are designed and built according to the international safety standards IEC/EN 60950, UL 1950, CAN/CSA C22.2 No.950-95. LGA, UL and cUL approvals are in progress. The 20 IMX 15 and 40 IMX 15 types provide supplementary insulation. Connected to a secondary circuit the 40 IMX 15 types provide SELV outputs even if the bus voltage at the converter input exceeds the SELV-limit of 60 V DC. The 110 IMY 15 types provide double insulation and are CE marked. They may be connected to e.g. a rectified 110 V AC source without any further isolation barrier.

The circuit comprises of integrated planar magnetics and all components are automatically assembled and solidly soldered onto a single PCB without any wire connection. Magnetic feedback ensures maximum reliability and repeatability in the control loop over all operating conditions. Careful considerations of possible thermal stresses ensure the absence of hot spots providing long life in environments where temperature cycles are a reality. The thermal design allows operation at full load up to an ambient temperature of 71°C in free air without using any potting material. For extremely high vibration environments the case has holes for screw mounting. Various options as e.g. extended temperature range –40...85°C or an alternative pinout provide a high level of application specific engineering and design-in flexibility.

Table of Contents

Type Survey and Key Data

Table 1: Type survey

Output 1		Output 2		Output power	Input voltage	Efficiency	Type designation	Options [2]
$U_{o\,nom}$ [V DC]	$I_{o\,nom}$ [1] [A]	$U_{o\,nom}$ [V DC]	$I_{o\,nom}$ [1] [A]	$P_{o\,nom}$ [W]	$U_{i\,min}$...$U_{i\,max}$ [V DC]	η_{typ} [%]		
2.5	4.5	-	-	11.3	8.4...36	84	20 IMX 15-2.5-9RG	-8, i, C, L, Z
2.5	4.5	-	-	11.3	16.8...75 [3]	84	40 IMX 15-2.5-9RG	-8, i, C, L, Z
3.3	4.5	-	-	14.9	8.4...36	84	20 IMX 15-03-9RG	-8, i, C, L, Z
3.3	4.5	-	-	14.9	16.8...75 [3]	85	40 IMX 15-03-9RG	-8, i, C, L, Z
3.3	4.5	-	-	14.9	50...150 [4]	84	110 IMY 15-03-9RG	-8, i, C, L, Z
5.1	3.5	-	-	17.8	8.4...36	88	20 IMX 15-05-9RG	-8, i, C, L, Z
5.1	3.5	-	-	17.8	16.8...75 [3]	88	40 IMX 15-05-9RG	-8, i, C, L, Z
5.1	3.5	-	-	17.8	50...150 [4]	86	110 IMY 15-05-9RG	-8, i, C, L, Z
5.1	2.3	-	-	11.7	8.4...36	85	20 IMX 15-05-9R	-8, i, C, L, Z
5.1	2.5	-	-	12.8	16.8...75 [3]	83	40 IMX 15-05-9R	-8, i, C, L, Z
5.1	2.5	-	-	12.8	50...150 [4]	83	110 IMY 15-05-9R	-8, i, C, L, Z
5.1	1.35	3.3	1.35	11.3	8.4...36	84	20 IMX 15-0503-9R	-8, i, C, L, Z
5.1	1.5	3.3	1.5	12.6	16.8...75 [3]	84	40 IMX 15-0503-9R	-8, i, C, L, Z
5.1	1.5	3.3	1.5	12.6	50...150 [4]	82	110 IMY 15-0503-9R	-8, i, C, L, Z
5	1.3	5	1.3	13.0	8.4...36	86	20 IMX 15-05-05-9	-8, R, K, i, C, L, Z
5	1.4	5	1.4	14.0	16.8...75 [3]	86	40 IMX 15-05-05-9	-8, R, K, i, C, L, Z
5	1.4	5	1.4	14.0	50...150 [4]	86	110 IMY 15-05-05-9	-8, R, i, C, L, Z
12	0.65	12	0.65	15.6	8.4...36	88	20 MX 15-12-12-9	-8, R, K, i, C, L, Z
12	0.7	12	0.7	16.8	16.8...75 [3]	88	40 IMX 15-12-12-9	-8, R, K, i, C, L, Z
12	0.7	12	0.7	16.8	50...150 [4]	87	110 IMY 15-12-12-9	-8, R, i, C, L, Z
15	0.5	15	0.5	15.0	8.4...36	88	20 IMX 15-15-15-9	-8, R, K, i, C, L, Z
15	0.56	15	0.56	16.8	16.8...75 [3]	88	40 IMX 15-15-15-9	-8, R, K, i, C, L, Z
15	0.56	15	0.56	16.8	50...150 [4]	87	110 IMY 15-15-15-9	-8, R, i, C, L, Z
24	0.32	24	0.32	15.4	8.4...36	86	20 IMX 15-24-24-9	-8, R, K, i, C, L, Z
24	0.35	24	0.35	16.8	16.8...75 [3]	86	40 IMX 15-24-24-9	-8, R, K, i, C, L, Z
24	0.35	24	0.35	16.8	50...150 [4]	86	110 IMY 15-24-24-9	-8, R, i, C, L, Z

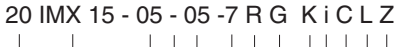
[1] Flexible load distribution on dual and double outputs possible up to 75% of the total output power $P_{o\,nom}$ on one of the 2 outputs. IMX/IMY 15-0503 types have reduced load distribution flexibility; 1.8 A max. on one of the 2 outputs. The other output should not exceed the difference to the total output power $P_{o\,nom}$.

[2] See: *Description of Options.*

[3] Short-time operation down to $U_{i\,min}$ ≥14.4 V possible. P_o reduced to approx. 85% of rated output power.

[4] Short-time operation down to $U_{i\,min}$ ≥43.2 V possible. P_o reduced to approx. 85% of rated output power.

Type Key and Product Marking



Input voltage range U_i
- 8.4...36 V DC 20
- 16.8...75 V DC 40
- 50...150 V DC 110

Series ..... IMX 15, IMY 15

Output voltage type output 1 ..... 2.5, 03, 05, 12, 15, 24

Dash designates double output unit with two independent electrically isolated outputs ..... - [1]

Output voltage type output 2 ..... 03, 05, 12, 15, 24

Operating ambient temperature range T_A
- –40...71°C (standard) ..... -9
- –40...85°C (option) ..... -8 [2]

Options and features:
- R input and magnetic feedback ..... R [2 3]
- Synchronous rectification ..... G
- Alternative pinout ..... K [2 4]
- Inhibit ..... i [2 5]
- Industrial C-pinout C
- Surface mount version with PCB lid L
- Open frame ..... Z

[1] Not applicable with option K or -0503- types. [2] For delivery lead times contact factory. Some types require a minimum order quantity.
[3] Standard features for single output and -0503- types. Optional for double output units. Excludes option K.
[4] Option K excludes option R. [5] Option inhibit excludes shut down and option K.

Examples: 20 IMX 15-05-05-9: DC-DC converter, input voltage range 8.4...36 V, 2 electrically isolated outputs each providing 5 V, 1.3 A.

110 IMY 15-0503-9R: DC-DC converter, input voltage range 50...150 V, 2 outputs with common return providing +5.1 V, 1.5 A and +3.3 V, 1.5 A. Unit fitted with magnetic feedback for tight output voltage regulation.

Product Marking

Basic type designation, output voltages and currents, applicable safety approval and recognition marks, Power-One patent nos. and company logo. Date code and serial no.

Functional Description

The IMX/IMY 15 series of DC-DC converters are magnetic feed-back controlled flyback converters using current mode PWM (Pulse Width Modulation). The -05- and -0503- output voltage versions as well as all double output versions fitted with option R feature an active magnetic feedback loop via a pulse transformer which results in very tight regulation of the output voltage (see fig.: *Block diagram, single output types, -0503- types and double output types with option R*). The output voltages of these versions can be adjusted via the R input. The R input is referenced to the secondary side and allows for programming of the output voltages in the range of approximately 80 to 105% of $U_{o\,nom}$ using either an external resistor or an external voltage source.

The voltage regulation on the dual and double output versions without option R is achieved with a passive transformer feedback from the main transformer (see fig.: *Block diagram, for double output types*). The output voltages can be adjusted via the Trim input. The Trim input is referenced to the primary side of the converter and allows for programming of the output voltages in the range 100 to 105% of $U_{o\,nom}$ by an external resistor or within 75 to 105% using an external voltage source. The load regulation output characteristic allows for paralleling of one or several double output units with equal output voltages.

Current limitation is provided by the primary circuit, thus limiting the total output power of double output types. The shut down input allows remote converter on/off.

Overtemperature protection will shut down the unit in excessive overload conditions with automatic restart approximately every 50 to 60 ms.

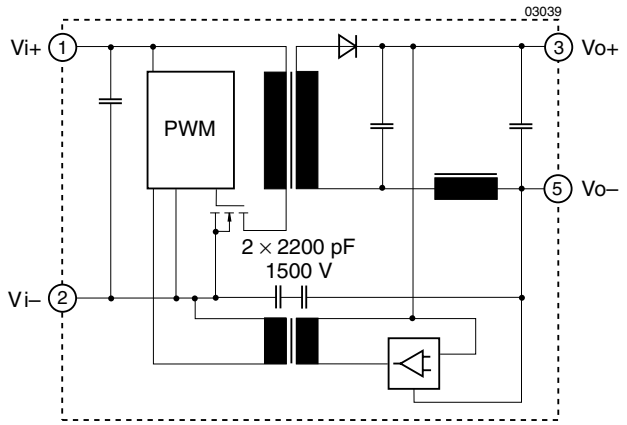


Fig. 1
Block diagram, single output types

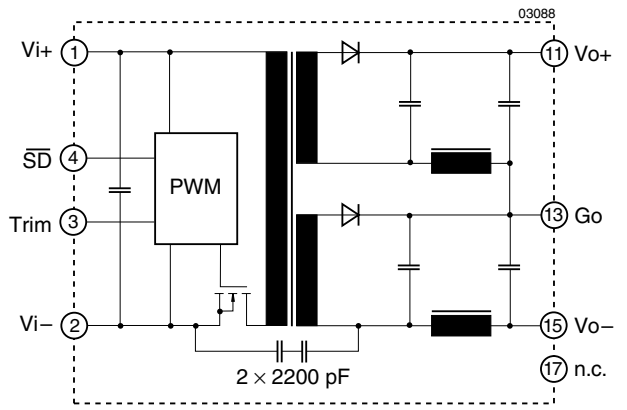


Fig. 2
Block diagram, dual output types, alternative K-pinout.

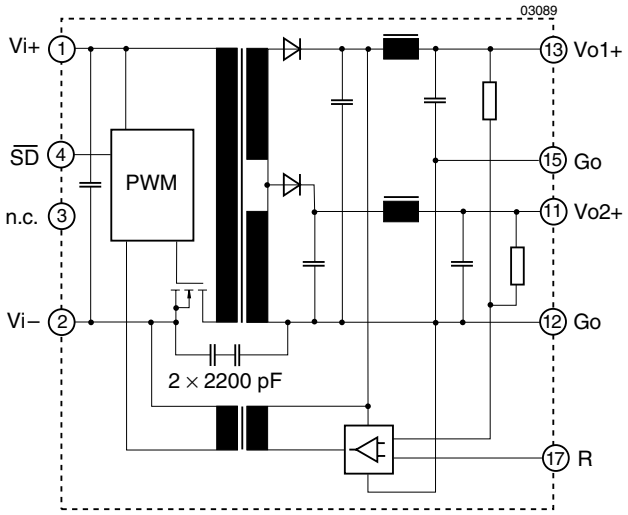


Fig. 3
Block diagram, -0503- types.

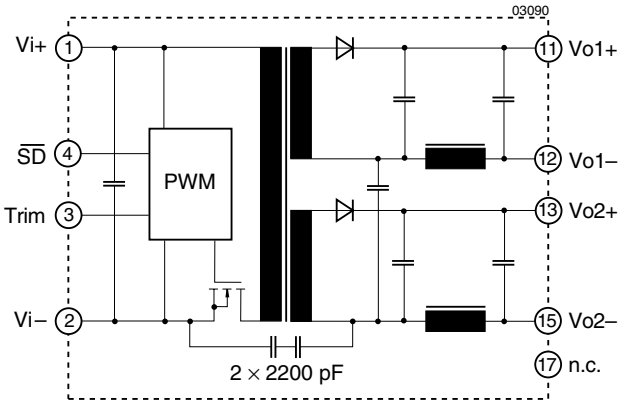


Fig. 4
Block diagram, double output types, standard pinout.

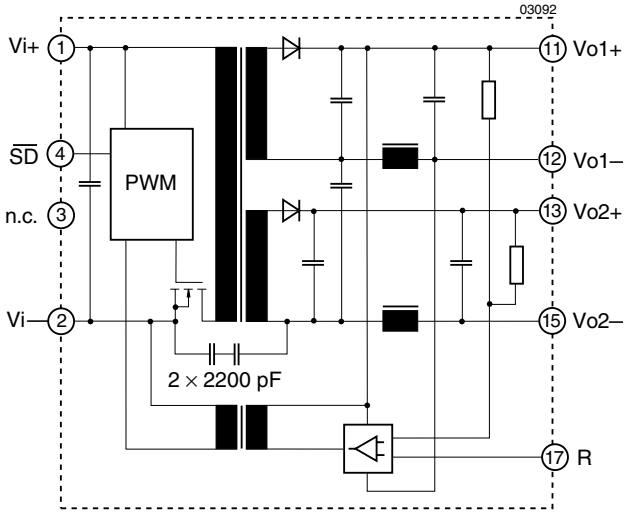


Fig. 5
Block diagram, double output types with option R.

Electrical Input Data

General conditions:
– T_A = 25 °C, unless T_C is specified.
– Shut down pin left open circuit (not connected).
– Trim or R input not connected.

Table 2: Input Data

Input				20 IMX			40 IMX			110 IMY			
Characteristics			Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
U_i	Input voltage range [1]		$T_{A\,min}$...$T_{A\,max}$ I_o = 0...$I_{o\,nom}$	8.4 [5]		36	16.8 [5 6]		75	50 [5 7]		150	V DC
$U_{i\,nom}$	Nominal input voltage				20			40			110		
$U_{i\,sur}$	Repetitive surge voltage		Abs. max input (3 s)			40		[9]	100			168	
$t_{start\,up}$	Converter start-up time [2]	Switch on	Worst case condition at $U_{i\,min}$ and full load		0.25	0.5		0.25	0.5		0.25	0.5	s
		$\overline{SD}$ high				0.1			0.1			0.1	
t_{rise}	Rise time [2]		$U_{i\,nom}$ resist load		5			5			5		ms
			$I_{o\,nom}$ capac. load		10	20		10	20		10	20	
$I_{i\,o}$	No load input current		I_o = 0, $U_{i\,min}$...$U_{i\,max}$			40			20			10	mA
I_{irr}	Reflected ripple current		I_o = 0...$I_{o\,nom}$			30			30			20	
$I_{inr\,p}$	Inrush peak current [3]		U_i = $U_{i\,nom}$			8			9			10	A
C_i	Input capacitance		for surge calculation		1.5			0.75			0.35		µF
$U_{\overline{SD}}$	Shut down voltage		Unit shut down		–10...0.7			–10...0.7			–10...0.7		V DC
			Unit operating		open or 2...20			open or 2...20			open or 2...20		
$R_{\overline{SD}}$	Shut down input resistance		For current calculations		approx. 10			approx. 10			approx. 10		kΩ
$I_{\overline{SD}}$	Input current if unit shut down		$U_{i\,min}$...$U_{i\,max}$			6			3			1	mA
f_s	Switching frequency		$U_{i\,min}$...$U_{i\,max}$, I_o = 0...$I_{o\,nom}$		approx. 300			approx. 300			approx. 300		kHz
$u_{i\,RFI}$	Input RFI level conducted		EN 55022 [4]		B [8]			B			B		

[1] $U_{i\,min}$ will not be as stated if U_o is increased above $U_{o\,nom}$ by use of the R or the Trin input. If the output voltage is set to a higher value, $U_{i\,min}$ will be proportionately increased.
[2] Measured with resistive and max. admissible capacitive load.
[3] Source impedance according to prETS 300132-2, version 4.3.
[4] Measured with a lead length of 0.1 m, leads twisted. Double output units with both outputs in parallel.
[5] Input undervoltage lock-out at typ. 80% of $U_{i\,min}$.
[6] Short time operation down to $U_{i\,min}$ >14.4 V possible. P_o reduced to approx. 85% of $P_{o\,nom}$.
[7] Short time operation down to $U_{i\,min}$ >43.2 V possible. P_o reduced to approx. 85% of $P_{o\,nom}$.
[8] 20 IMX 15 types require 4.7 µF/50 V capacitance accross the input (Polyester, Philips 372 series).
[9] See: *Transient ETR 283 (19Pfl1), Electromagnetic Immunity.*

Inrush current

The inrush current has been kept as low as possible by choosing a very small input capacitance. A series resistor may be installed in the input line to further reduce this current.

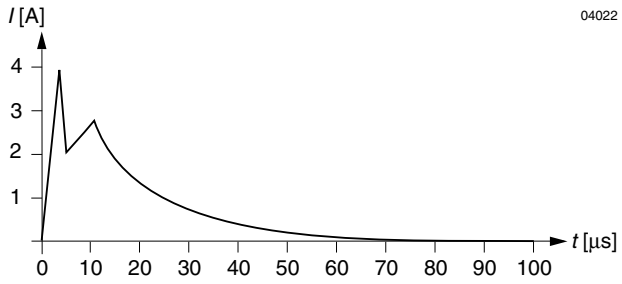


Fig. 6
Typical inrush current at $U_{i\,nom}$, $P_{o\,nom}$ versus time (40 IMX 15). Source impedance according to prETS 300132-2, version 4.3 at $U_{i\,nom}$.

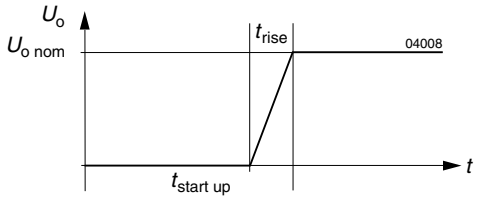


Fig. 7
Converter start-up and rise time

Input Undervoltage Lockout

A special feature of these units is the accurate undervoltage lockout protection which protects the units (and system) from large currents caused by operation at low voltages. This ensures easier start-up in distributed power systems.

Table 3: Turn on and turn off voltage

Type	Turn On	Turn Off	Units
20 IMX	7.5...8	7...7.5	V
40 IMX	12.5...13.5	12...13	
110 IMY	40...42.5	38...40.5	

Reverse Polarity Protection

The built-in suppressor diode also provides for reverse polarity protection at the input by conducting current in the reverse direction. An external fuse is required to limit this current.

Table 4: Recommended external fuses

Converter type	Fuse type
20 IMX 15	F4.0A
40 IMX 15	F2.0A
110 IMY 15	F1.0A

Input Transient Voltage Protection

A built-in suppressor diode provides effective protection against input transients which may be caused for example by short-circuits accross the input lines where the network inductance may cause high energy pulses.

Table 5: Built-in transient voltage suppressor

Type	Breakdown voltage $V_{Br\,nom}$ [V]	Peak power at 1 ms P_p [W]	Peak pulse current I_{pp} [A]
20 IMX 15	40	1500	22
40 IMX 15	100	1500	9.7
110 IMY 15	168	600	0.5

For very high energy transients as for example to achieve IEC/EN 61000-4-5 or ETR 283 (19 Pfl1) compliance (as per table: *Electromagnetic Immunity*) an external inductor and capacitor are required. The components should have similar characteristics as listed in table: *Components for external circuitry for IEC/EN 61000-4-5, level 2 or ETR 283 (19Pfl1) compliance*.

Note: The suppressor diode D is only necessary for 20 IMX 15 types.

Table 6: Components for external circuitry for IEC/EN 61000-4-5, level 2 or ETR 283 (19Pfl1) compliance.

Type	Inductor (L)	Capacitor (C)	Diode (D)
20 IMX	68 µH, 2.7 A	330 µF, 63 V	1.5 k E47A
40 IMX	220 µH, 1.3 A	2 × 100 µF, 100 V	-
110 IMY	330 µH, 0.43 A	2 × 100 µF, 200 V	-

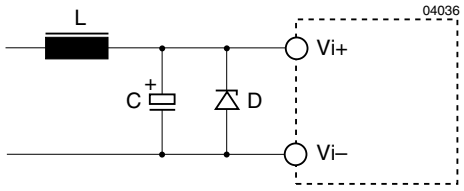


Fig. 8
Example for external circuitry to comply with IEC/EN 61000-4-5 or ETR 283 (19Pfl1); the diode D is only necessary for 20 IMX 15 types.

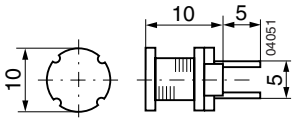


Fig. 9
Dimensions of inductor L for 110 IMY 15 types (e.g. TOKO 494LYF-0098K).

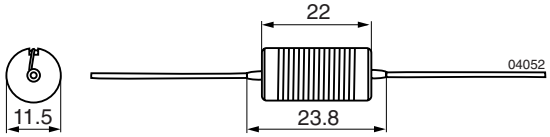


Fig. 10
Dimensions of inductor L for 20 IMX 15 and 40 IMX 15 types (e.g. Coil Craft, PCH-45 series).

Electrical Output Data

General conditions:
– T_A = 25 °C, unless T_C is specified
– Shutdown pin left open circuit (not connected)
– R input not connected

Table 7a: Output data for single output units and -0503- types.

Output				**2.5 V (RG)**			**3.3 V (RG)**			**5.1 V (RG)**			
Characteristics	Conditions			min	typ	max	min	typ	max	min	typ	max	Unit
U_{o1} U_{o2}	Output voltage		$U_{i\,nom}$ I_o = 0.5 $I_{o\,nom}$	3.27		3.33	3.25		3.35	5.05		5.15	V DC
$I_{o\,nom}$	Output current [1]	20 IMX	$U_{i\,min}$...$U_{i\,max}$		4.5			4.5			3.5		A
		40 IMX/110 IMY			4.5			4.5			3.5		
I_{o1L} I_{o2L}	Current limit [2, 4]	20 IMX	$U_{i\,nom}$, T_C = 25 °C U_{o1} – 93% U_{nom}		6.0			6.0			4.6		
I_{o1L} I_{o2L}		40 IMX/110 IMY			6.0			6.0			4.6		
ΔU_o	Line/load regulation		$U_{i\,min}$...$U_{i\,max}$, I_o = (0.01...1) • $I_{o\,nom}$			±0.5			±0.5			±0.5	%
$u_{o1/2}$	Output voltage noise		$U_{i\,min}$...$U_{i\,max}$ I_o = $I_{o\,nom}$			100 [5]			100 [5]			100 [5]	mV_{pp}
						60 [6]			60 [6]			60 [6]	
$U_{o\,L}$	Output overvoltage limit. [7]			115		130	115		130	115		130	%
$C_{o\,ext}$	Admissible capacitive load					4000			4000			4000	µF
$u_{o\,d}$	Dynamic load regulation	Voltage deviat.	$U_{i\,nom}$ $I_{o\,nom} \leftrightarrow {}^1/_2\, I_{o\,nom}$ IEC/EN 61204		±250			±250			±250		mV
t_d		Recovery time				1			1			1	ms
α_{Uo}	Temperature coefficient $\Delta U_o/\Delta T_C$		$U_{i\,min}$...$U_{i\,max}$ I_o = 0...$I_{o\,max}$			±0.02			±0.02			±0.02	%/K

[1] Flexible load distribution: 20 IMX 15-0503-7; 1.6 A max. and 40 IMX 15/110 IMY 15-0503-7 types; 1.8 A max. on one of the 2 outputs, the other output should not be loaded such that the total output power exceeds $P_{o\,nom}$ according to table: *Type survey*.
[2] The current limit is primary side controlled. In the event of a sustained overload condition the thermal protection may cause the unit to shutdown (restart on cool-down).
[3] For -0503- types: total capacitance, of both outputs.
[4] Conditions for specified output. Other output loaded with constant current I_o = 0.5 $I_{o\,nom}$.
[5] BW = 20 MHz
[6] Measured with a probe according to EN 61204
[7] The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control

Table 7b: Output data for single output units and -0503- types.

Output					**5.1 V**			**5.1/3.3 V**			
Characteristics		Conditions			min	typ	max	min	typ	max	Unit
U_{o1} U_{o2}	Output voltage			$U_{i\,nom}$ $I_o = 0.5\ I_{o\,nom}$	5.05		5.15	5.0 3.13		5.12 3.46	V DC
$I_{o\,nom}$	Output current [1]	20 IMX		$U_{i\,min}...U_{i\,max}$		2.3			2×1.35		A
		40 IMX/110 IMY				2.5			2×1.5		
I_{o1L} I_{o2L}	Current limit [2, 4]	20 IMX		$U_{i\,nom}$, $T_C = 25\,°C$ $U_{o1} - 93\%\ U_{nom}$		3.2			2.7 3.8		
I_{o1L} I_{o2L}		40 IMX/110 IMY				3.6		4.0	2.9		
ΔU_o	Line/load regulation			$U_{i\,min}...U_{i\,max}$, $I_o = (0.01...1) \bullet I_{o\,nom}$			±0.5				%
			5.1 V	$U_{i\,nom}$ $I_o = (0.1...1)\ I_{o\,nom}$						+3, –5	
			3.3 V							±4.5	
$u_{o1/2}$	Output voltage noise			$U_{i\,min}...U_{i\,max}$ [5] $I_o = I_{o\,nom}$ [6]			70 40			80 40	mV_{pp}
$U_{o\,L}$	Output overvoltage limit. [7]				115		130	115		130	%
$C_{o\,ext}$	Admissible capacitive load						4000			total: 4000 [3]	µF
$u_{o\,d}$	Dynamic load regulation	Voltage deviat.		$U_{i\,nom}$ $I_{o\,nom} \leftrightarrow {}^1/_2\ I_{o\,nom}$ IEC/EN 61204		±250			±150		mV
t_d		Recovery time					1			1	ms
α_{Uo}	Temperature coefficient $\Delta U_o/\Delta T_C$			$U_{i\,min}...U_{i\,max}$ $I_o = 0...I_{o\,max}$			±0.02			±0.02	%/K

[1] Flexible load distribution: 20 IMX 15-0503-7; 1.6 A max. and 40 IMX 15/110 IMY 15-0503-7 types; 1.8 A max. on one of the 2 outputs, the other output should not be loaded such that the total output power exceeds $P_{o\,nom}$ according to table: *Type survey*.
[2] The current limit is primary side controlled. In the event of a sustained overload condition the thermal protection may cause the unit to shutdown (restart on cool-down).
[3] For -0503- types: total capacitance, of both outputs.
[4] Conditions for specified output. Other output loaded with constant current $I_o = 0.5\ I_{o\,nom}$.
[5] BW = 20 MHz
[6] Measured with a probe according to EN 61204
[7] The overvoltage protection is via a primary side second regulation loop. It is not tracking with R control

Table 7c: Output data for dual and double output units.

Output				**2 × 5 V**			**2 × 12 V**			**2 × 15 V**			**2 × 24 V**			
Characteristics			Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	Unit
U_{o1} U_{o2}	Output voltage		$U_{i\,nom}$ I_o = 0.5 $I_{o\,nom}$	4.95 4.94		5.05 5.06	11.90 11.88		12.10 12.12	14.88 14.85		15.12 15.15	23.80 23.75		24.20 24.25	V DC
$I_{o\,nom}$	Output current [1]	20 IMX	$U_{i\,min}$...$U_{i\,max}$		2 × 1.3			2 × 0.65			2 × 0.50			2 × 0.32		A
		40 IMX/110 IMY			2 × 1.4			2 × 0.70			2 × 0.56			2 × 0.35		
I_{oL}	Current limit [2, 4]	20 IMX	$U_{i\,nom}$, T_C = 25 °C		3.0			1.6			1.3			0.85		
		40 IMX/110 IMY	U_o = 93% $U_{o\,nom}$		3.2			1.7			1.4			0.90		
ΔU_{o1}	Line/load regulation	U_{o1}	$U_{i\,min}$...$U_{i\,max}$, $I_{o\,nom}$			±1			±1			±1			±1	%
ΔU_{o2}		U_{o2}	$U_{i\,nom}$ I_o = (0.1...1) $I_{o\,nom}$			±3			±3			±3			±3	
$u_{o1/2}$	Output voltage noise		$U_{i\,min}$...$U_{i\,max}$ [5] I_o = $I_{o\,nom}$ [6]			80 40			120 60			150 70			240 120	mV_{pp}
$U_{o\,L}$	Output overvoltage limit. [7]		Min. load 1%	115		130	115		130	115		130	115		130	%
$C_{o\,ext}$	Admissible capacitive load [3]					4000			680			470			180	µF
$u_{o\,d}$	Dynamic load regulation	Voltage deviat.	$U_{i\,nom}$ $I_{o\,nom}$ ↔ ½ $I_{o\,nom}$		±250			±300			±300			±600		mV
t_d		Recovery time				1			1			1			1	ms
α_{Uo}	Temperature coefficient $\Delta U_o/\Delta T_C$		$U_{i\,min}$...$U_{i\,max}$ I_o = 0...$I_{o\,max}$			±0.02			±0.02			±0.02			±0.02	%/K

[1] Flexible load distribution: With double or dual output units each output is capable of delivering 75% of the total output power. The other output should not be loaded such that the total output power exceeds $P_{o\,nom}$ according to table: *Type survey*.
[2] The current limit is primary side controlled. In the event of a sustained overload condition the thermal protection may cause the unit to shutdown (restart on cool-down).
[3] Measured with both outputs connected in parallel.
[4] Conditions for specified output. Other output loaded with constant current I_o = 0.5 $I_{o\,nom}$.
[5] BW = 20 MHz
[6] Measured with a probe according to EN 61204
[7] The overvoltage protection is via a primary side second regulation loop, not tracking with Trim control.
[8] Minimum load of 10% on output 1 recommended to prevent the two output voltages to drop in unbalanced load conditions.

Thermal Considerations

If a converter, mounted on a PCB, is located in free, quasi-stationary air (convection cooling) at the indicated maximum ambient temperature $T_{A\,max}$ (see table: *Temperature specifications*) and is operated at its nominal input voltage and output power, the case temperature T_C measured at the *Measuring point of case temperature* T_C (see: *Mechanical Data*) will approach the indicated value $T_{C\,max}$ after the warm-up phase. However, the relationship between T_A and T_C depends heavily on the conditions of operation and integration into a system. The thermal conditions are influenced by input voltage, output current, airflow, temperature of surrounding components and surfaces and the properties of the printed circuit board. $T_{A\,max}$ is therefore only an indicative value and under practical operating conditions, the ambient temperature T_A may be higher or lower than this value.

Caution: The case temperature T_C measured at the: *Measuring point of case temperature* T_C (see: *Mechanical Data*) may under no circumstances exceed the specified maximum value. The installer must ensure that under all operating conditions T_C remains within the limits stated in the table: *Temperature specifications.*

Output Overvoltage Protection

The output of single output units as well as -0503- and -05-05- types are protected against overvoltages by a second control loop. In the event of an overvoltage on one of the outputs the unit will shut down and attempt to restart approximately every 50 to 60 ms. Double and dual output units (with exception of the -0503- and -05-05- types) are protected against overvoltages by a Zener diode across the second output. Under worst case conditions the Zener diode will short circuit. Since with double output units both outputs track each other the protection diode is only provided in one of the outputs. The main purpose of this feature is to protect against possible overvoltages which could occur due to a failure in the feedback control circuit. The output overvoltage protection is not designed to withstand externally applied overvoltages.

Overtemperature Protection

The converters are protected from possible overheating by means of an internal temperature monitoring circuit. It shuts down the unit above the internal temperature limit and attempts to automatically restart every 50 to 60 ms. This feature prevents from excessive internal temperature building up which could occur in heavy overload conditions.

Short Circuit Behaviour

The current limit characteristic shuts down the converter whenever a short circuit is applied to its output. It acts self-protecting and automatically recovers after removal of the overload condition (hiccup mode).

Connection in Series

The outputs of one or several single or double output units can be connected in series without any precautions, taking into consideration that the highest output voltage should remain below 42 V to ensure that the output remains SELV.

Connection in Parallel

The outputs of one or several double output units (except -0503- types) with equal nominal output voltage can be connected in parallel. Approximate current sharing between 2 or several units is ensured by their load dependent output characteristic. The two outputs of a double output unit fitted with option R can be connected in parallel to configure a single output with tight regulation at full load.

Typical Performance Curves

General conditions:
– T_A = 25°C, unless T_C is specified.
– Shut down pin left open circuit.
– Trim or R input not connected.

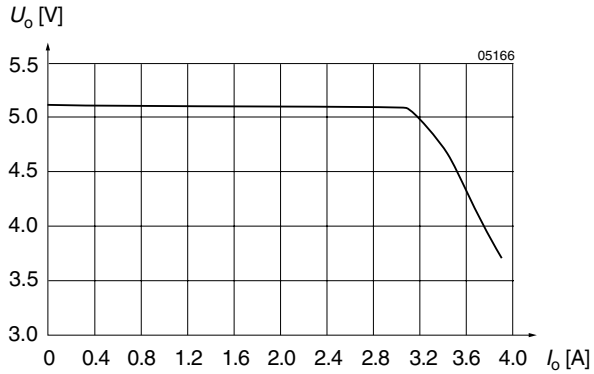


Fig. 11
U_o versus I_o (typ) of units with U_o = 5.1 V. (110 IMY 15-05-7R)

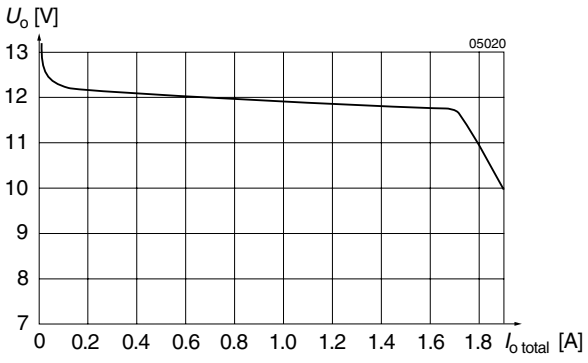


Fig. 12
U_o versus I_o (typ) of double output units (2 × 12 V), with both outputs in parallel. (110 IMY 15-12-12-7)

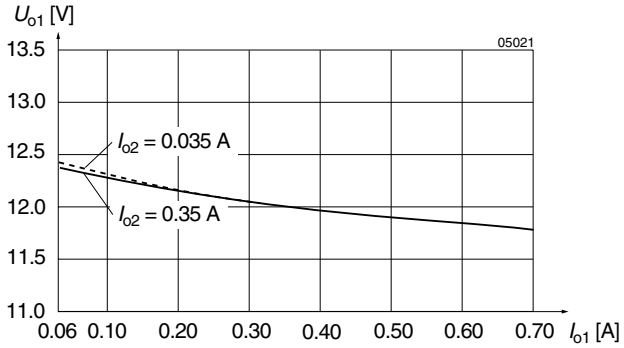


Fig. 13
Cross load regulation U_{o1} versus I_{o1} (typ) for various I_{o2} (2 × 12 V).

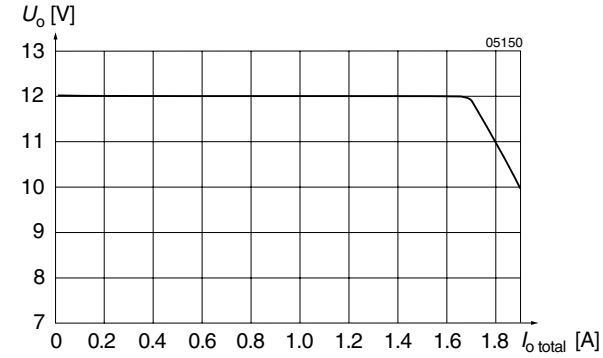


Fig. 14
U_o versus I_o (typ) of double output units with option R (2 × 12 V), with both outputs in parallel.
110 IMY 15-12-12-7R.

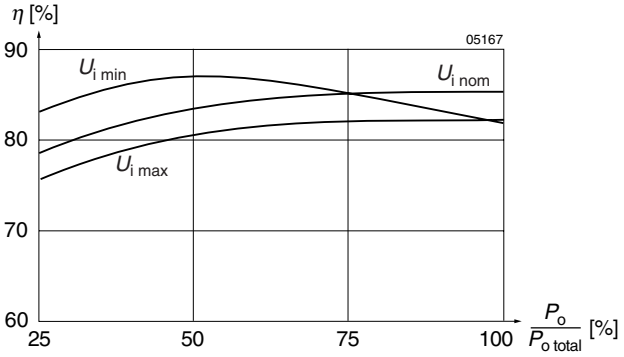


Fig. 15
Efficiency versus input voltage and load. Typical values
40 IMX 15-12-12-7

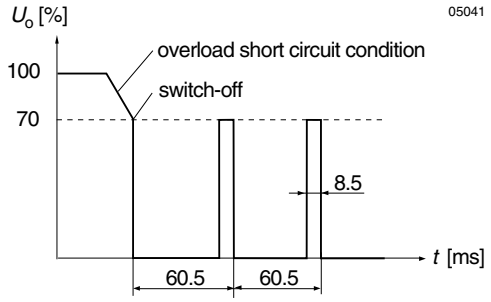


Fig. 16
Overload switch off (hiccup mode), typical values.

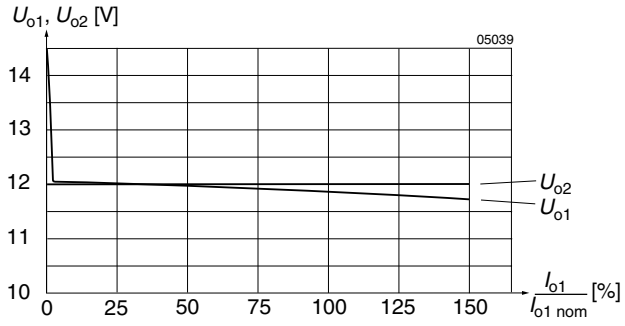


Fig. 17
Flexible load distribution on double outputs (2 × 12 V) with load variation from 0...150% of $P_{o1\,nom}$ on output 1. Output 2 loaded with 50% of $P_{o2\,nom}$.

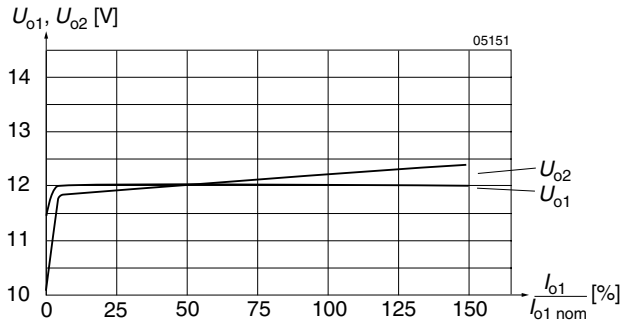


Fig. 18
Flexible load distribution on double outputs with option R (2 × 12 V) with load variation from 0...150% of $P_{o1\,nom}$ on output 1. Output 2 loaded with 50% of $P_{o2\,nom}$.
110 IMY 15-12-12-7R.

Note: Minimum load of approx. 10% on output 1 prevents voltage drop in unballanced load condition.

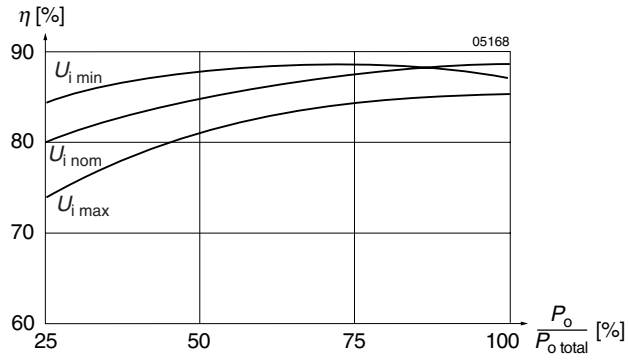


Fig. 19
Efficiency versus input voltage and load. Typical values
110 IMY 15-12-12-7

Auxiliary Functions

Shut Down Function

The outputs of the converters may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the shut down pin. If the shut down function is not required then it should be left open-circuit.

Converter operating: 2.0...20 V
Converter shut down: –10...0.7 V

Adjustable Output Voltage

- R input for single output units using synchronous rectification (G)
- R input for single output units and -0503- types; optional for double output units
- Trim input for double output units

As a standard feature, the single and double output units offer adjustable output voltage(s) by using the control input R or Trim. If the control input is left open-circuit the output voltage is set to $U_{o\,nom}$. For output voltages $U_o > U_{o\,nom}$, the minimum input voltage $U_{i\,min}$ (see: *Electrical Input Data*) increases proportionally to $U_o/U_{o\,nom}$.

Single output units using synchronous rectification (G):

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of an external resistor connected between the R pin and either Vo+ or Vo–.

Note: For the units with synchronous rectification U_o adjustment is different from the standard single output units and -0503- types.

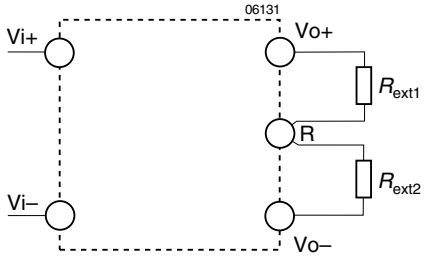


Fig. 20
Output voltage control for single output units synchronous rectification.

Table 8: U_o versus U_{ext} approximate values

$U_{o\,nom}$ [V]	Typ. values of R_{ext1}		Typ. values of R_{ext2}	
	U_o [% of $U_{o\,nom}$]	R_{ext1} [kΩ]	U_o [% of $U_{o\,nom}$]	R_{ext2} [kΩ]
3.3	90	0.47	100	∞
	95	2.7	105	15
	100	∞	110	6.8
5.1	90	3.3	100	∞
	95	8.2	105	9.1
	100	∞	110	3.9-

Single output units, -0503- types and double output units fitted with option R:

The R input is referenced to the secondary side of the converter. Adjustment of the output voltage is possible by means of either an external resistor or a voltage source.

a) Adjustment by means of an external resistor R_{ext}.

Depending upon the value of the required output voltage, the resistor shall be connected.

either: Between the R pin and Vo– to achieve an output voltage adjustment range of approximately $U_o = 80...100\,\%\ U_{o\,nom}$.

$$R_{ext1} \approx 4\ k\Omega \bullet \frac{U_o}{U_{o\,nom} - U_o}$$

or: Between the R pin and Vo+ to achieve an output voltage range of approximately $U_o = 100...105\,\%\ U_{o\,nom}$.

$$R_{ext2} \approx 4\ k\Omega \bullet \frac{(U_o - 2.5V)}{2.5\ V \bullet (U_o/U_{o\,nom} - 1)}$$

b) Adjustment by means of an external voltage U_{ext} between Vo– and R pins.

The control voltage range is 1.96...2.62 V and allows for an adjustment in the range of approximately 80...105% of $U_{o\,nom}$.

$$U_{ext} \approx \frac{U_o \bullet 2.5\ V}{U_{o\,nom}}$$

Attempting to adjust the output below this range will cause the converter to shut down (hiccup mode).

Note: Applying an external control voltage >2.75 V may damage the converter.

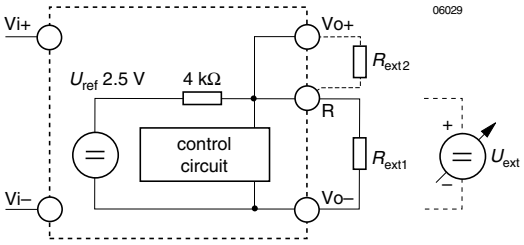


Fig. 21
Output voltage control for single output units, -0503- types and double output units fitted with option R by means of the R input.

Double output units with Trim input:

The Trim input is referenced to the primary side. The figure below shows the circuit topology. Adjustment of the output voltage is possible by means of either an external resistor R_{ext} in the range of 100...105% of $U_{o\,nom}$ or an external voltage source in the range of 75...105% of $U_{o\,nom}$.

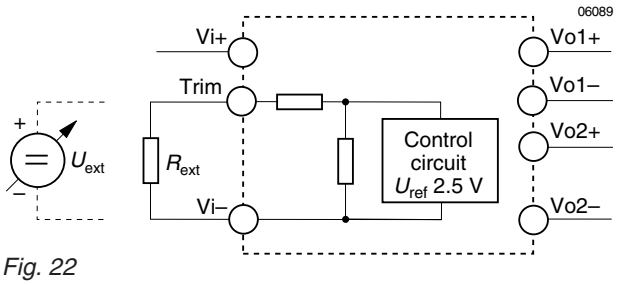


Fig. 22
Output voltage control for double output units by means of the Trim input.

a) Adjustment by means of an external resistor R_{ext}:

Programming of the output voltage by means of an external resistor R_{ext} is possible within a limited range of 100...105% of $U_{o\,nom}$. R_{ext} should be connected between the Trim pin and Vi–. Connection of R_{ext} to Vi+ may damage the converter. The following table indicates suitable resistor values for typical output voltages under nominal conditions ($U_{i\,nom}$, I_o = 0.5 $I_{o\,nom}$), with either paralleled outputs or equal load conditions on each outputs.

Table 9a: R_{ext1} for $U_o > U_{o\,nom}$; approximate values ($U_{i\,nom}$, $I_{o1,\,2}$ = 0.5 $I_{o1/2\,nom}$)

U_o [% $U_{o\,nom}$]	R_{ext} [kΩ]
105...108 (107 typically)	0
105	1.5
104	5.6
103	12
102	27
101	68
100	∞

b) Adjustment by means of an external voltage source U_{ext}.

For external output voltage programming in the range 75...105% of $U_{o\,nom}$ a (0...20 V) source U_{ext} is required, connected to the Trim pin and Vi–. The table below indicates typical U_o versus U_{ext} values under nominal conditions ($U_{i\,nom}$, I_o = 0.5 $I_{o\,nom}$), with either paralleled outputs or equal load conditions on each output. Applying a control voltage >20 V will set the converter into a hiccup mode. Direct paralleling of the Trim pins of units connected in parallel is feasible.

Table 9b: U_o versus U_{ext} for U_o = 75...105% $U_{o\,nom}$; typical values ($U_{i\,nom}$, $I_{o1/2}$ = 0.5 $I_{o1/2\,nom}$)

U_o [% $U_{o\,nom}$]	U_{ext} [V]
≥105	0
102	1.6
95	4.5
85	9
75	13

Electromagnetic Compatibility (EMC)

A suppressor diode together with an input filter form an effective protection against high input transient voltages which typically occur in many installations, but especially in battery driven mobile applications.

Electromagnetic Immunity

Table 10: Immunity type tests

Phenomenon	Standard [1]	Class Level	Coupling mode [2]	Value applied	Waveform	Source Imped.	Test procedure	In oper.	Per- form. [3]
Electrostatic discharge to case	IEC/EN 61000-4-2	2	contact discharge (R pin open)	4000 V_p	1/50 ns	330 Ω	10 positive and 10 negative discharges	yes	B
		3	air discharge (R pin open)	8000 V_p					
Electromagnetic field	IEC/EN 61000-4-3	3	antenna	10 V/m	AM 80% 1 kHz		26…1000 MHz	yes	A
	ENV 50204				PM, 50% duty cycle, 200 Hz resp. frequ.		900 MHz		
Electrical fast transient/burst	IEC/EN 61000-4-4	4	direct +i/–i	4000 V_p	bursts of 5/50 ns 5 kHz rep. rate transients with 15 ms burst duration and a 300 ms period	50 Ω	1 min positive 1 min negative transients per coupling mode	yes	B
Surge	IEC/EN 61000-4-5 [5]	3	+i/–i	2000 V_p	1.2/50 µs	2 Ω	5 pos. and 5 neg. impulses per coupling mode	yes	B
Conducted disturbancies	IEC/EN 61000-4-6	3	+i/–i	10 V_{rms} (140 dBµV)	AM modulated 80%, 1 kHz	50 Ω	0.15...80 MHz 150 Ω	yes	A
Transient	ETR 283 (19 Pfl 1) [4]		+i/–i	150 V_p	0.1/0.3 ms	limited to <100 A	3 positive	yes	B

[1] Related and previous standards are referenced in: *Technical Information: Standards*.
[2] i = input, o = output.
[3] A = normal operation, no deviation from specification, B = temporary deviation from specs. possibe.
[4] For 40 IMX 15 types (additional external components required). Not applicable for 20 IMX 15 types.
[5] External components required.

Electromagnetic Emission

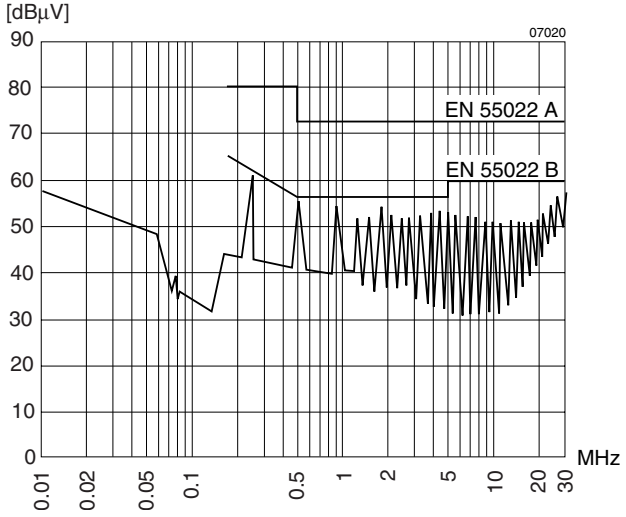


Fig. 23
Typical disturbance voltage (quasi-peak) at the input according to CISPR 11/EN 55011 and CISPR 22/EN 55022, measured at $U_{i\ nom}$ and $I_{o\ nom}$. Output leads 0.1 m, twisted. (40 IMX 15-05-7R)

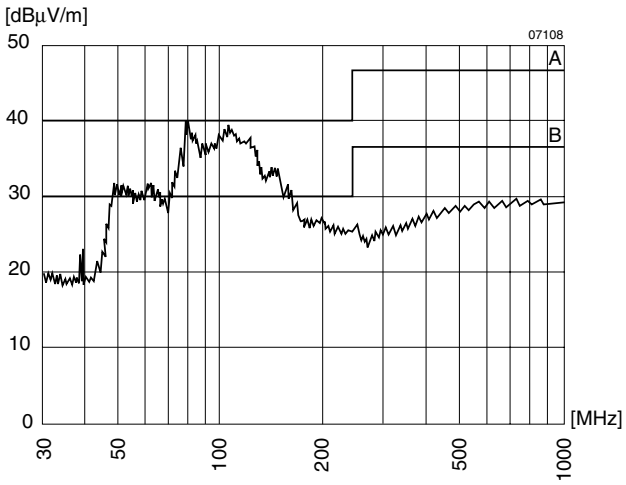


Fig. 24
Typical radio frequency-interference voltage at $U_{i\ nom}$, $I_{o\ nom}$, measured with an antenna (distance 10 m). Output leads 1 m, twisted.

CISPR 22/EN 55022, Level B Radiated

Electromagnetic emission requirements according to EN 55022, class B (radiated emmission) can be achieved by adding an external common mode choke and (for 20 IMX 15 types) an additional capacitor, see: *Input Data*. The filter components should be placed as close as possible to the input of the converter.

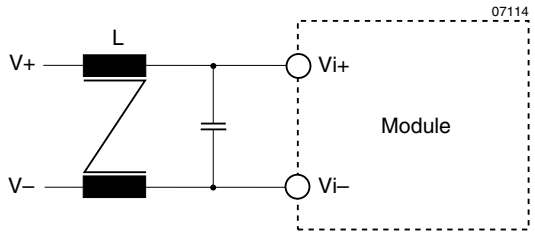


Fig. 25
Example for external circuitry to comply with CISPR22/EN 55022, level B, radiated

Table 11: Input filter components for EN 55022, level B, radiated.

Type	Current compensated choke
20 IMX 15 40 IMX 15 110 IMY 15	1 mH, 2A e.g. Tokin, type SC-02-10GS

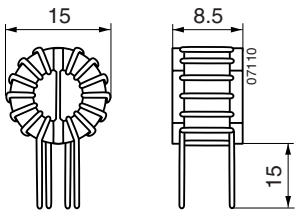


Fig. 26
Choke dimensions

Immunity to Environmental Conditions

Table 12: Temperature specifications, valid for air pressure of 800...1200 hPa (800...1200 mbar)

Temperature			-9		Option -8 [3]		
Characteristics		Conditions	min	max	min	max	Unit
T_A	Ambient temperature [1]	Operational [2]	–40	71	–40	85	°C
T_C	Case temperature		–40	95	–40	105	
T_S	Storage temperature [1]	Non operational	–55	100	–55	105	

[1] MIL-STD-810D section 501.2 and 502.2
[2] See: *Thermal Considerations*
[3] Start up at –55°C

Table 13: MTBF and device hours

MTBF	Ground Benign	Ground Fixed		Ground Mobile
MTBF acc. to MIL-HDBK-217F	T_C = 40°C	T_C = 40°C	T_C = 70°C	T_C = 50°C
110 IMY 15-05-9R	485'000 h	255'000 h	167'000 h	223'000 h

Table 14: Mechanical stress

Test Method		Standard	Test Conditions		Status
Ca	Damp heat steady state	IEC/DIN IEC 60068-2-3 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	40 $^{\pm 2}$ °C 93 $^{+2/-3}$ % 56 days	Unit not operating
Ea	Shock (half-sinusoidal)	IEC/EN/DIN EN 60068-2-27 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	100 g_n = 981 m/s^2 6 ms 18 (3 each direction)	Unit operating
Eb	Bump (half-sinusoidal)	IEC/EN/DIN EN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	40 g_n = 392 m/s^2 6 ms 6000 (1000 each direction)	Unit operating
Fc	Vibration (sinusoidal)	IEC/EN/DIN EN 60068-2-6 MIL-STD-810D section 514.3	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10...60 Hz) 5 g_n = 49 m/s^2 (60...2000 Hz) 10...2000 Hz 7.5 h (2.5 h each axis)	Unit operating
Fda	Random vibration wide band reproducibility high	IEC 60068-2-35	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g_n^2/Hz 20...500 Hz 4.9 $g_{n\ rms}$ 3 h (1 h each axis)	Unit operating
Kb	Salt mist, cyclic (sodium chloride NaCl solution)	IEC/EN/DIN IEC 60068-2-52	Concentration: Duration: Storage: Storage duration: Number of cycles:	5% (30°C) 2 h per cycle 40°C, 93% rel. humidity 22 h per cycle 3	Unit not operating

Mechanical Data

Dimensions in mm. Tolerances ±0.3 mm unless otherwise indicated.

European Projection

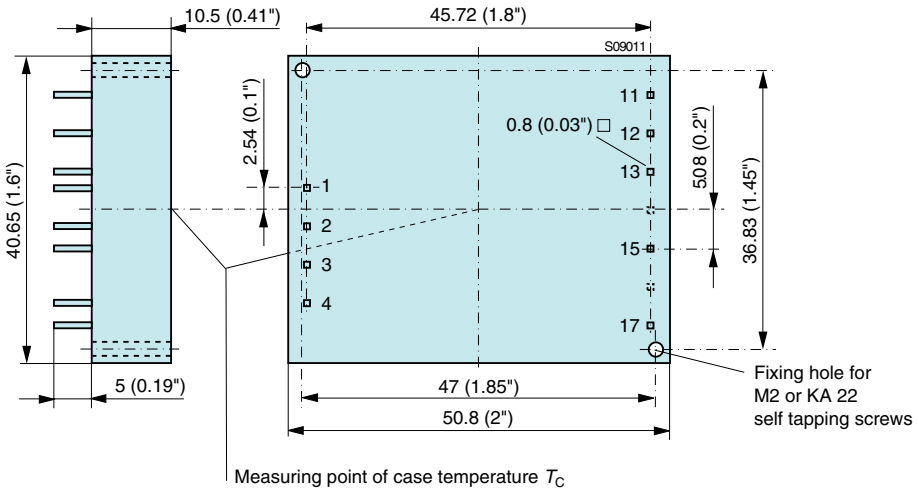


Fig. 27
Case IMX 15, IMY 15
Weight: <35 g

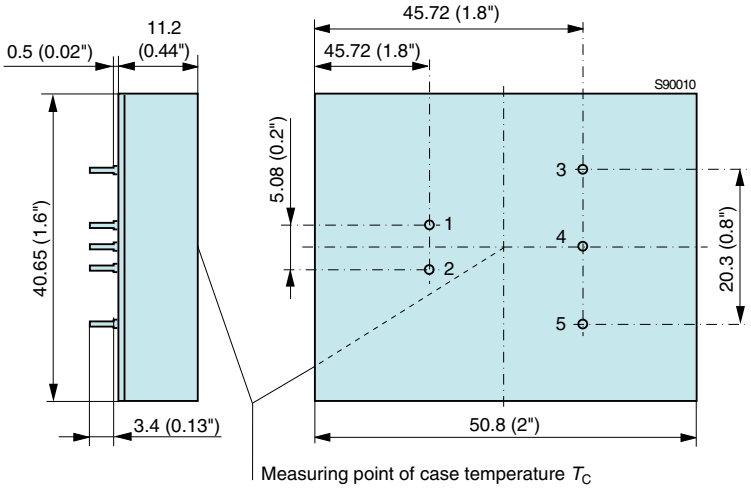


Fig. 28
C pinout (option C)

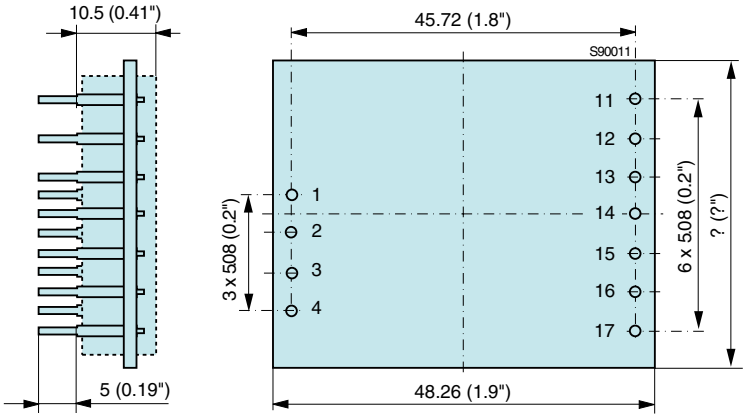


Fig. 29
Open frame (option Z)

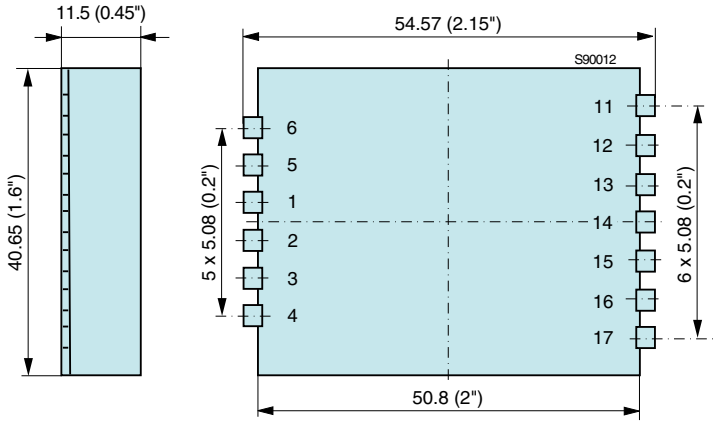


Fig. 30
Surface mount version with PCB lid (option L)

Safety and Installation Instructions

Installation Instructions

Installation of the DC-DC converters must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application.

Connection to the system shall be made via a printed circuit board with hole diameters of 1.4 mm ±0.1 mm for the pins.

The units should be connected to a secondary circuit.

Check for hazardous voltages before altering any connections.

Do not open the module.

Ensure that a unit failure (e.g. by an internal short-circuit) does not result in a hazardous condition. See also: *Safety of operator accessible output circuit.*

Input Fuse

To prevent excessive current flowing through the input supply line in case of a short-circuit across the converter input an external fuse should be installed in a non earthed input supply line. We recommend a fast acting fuse F4.0A for 20 IMX 15 types, F2.0 A for 40 IMX 15 types and a fuse F1.0A for 110 IMY 15 types.

Standards and approvals

All DC-DC converters are pending to be UL recognized according to UL 1950, UL recognized for Canada to CAN/CSA C22.2 No. 950-95 and LGA approved to IEC/EN 60950 standards.

The units have been evaluated for:

- Building in
- Supplementary insulation input to output, based on their maximum input voltage (IMX 15 types)
- Reinforced insulation input to output, based on their maximum input voltage (IMY 15 types)
- The use in a pollution degree 2 environment
- Connecting the input to a secondary circuit which is subject to a maximum transient rating of 1500 V (IMX 15 types)
- Connecting the input to a primary circuit which is subject to a maximum transient rating of 2500 V (IMY 15 types)

After approvals the DC-DC converters are subject to manufacturing surveillance in accordance with the above mentioned UL, CSA, EN and with ISO 9001 standards.

Table 15: Pin allocation

Pin	Standard			Option K	Option C		Option L and Z	
	single	double	-0503-	dual	single	dual	single	double
1	Vi+	Vi+	Vi+	Vi+	Vi+	Vi+	Vi+	Vi+
2	Vi–	Vi–	Vi–	Vi–	Vi–	Vi–	Vi–	Vi–
3	-	Trim	n.c.	-	Vo+	Vo+	n.c.	Trim
4	$\overline{SD}$	$\overline{SD}$	$\overline{SD}$	$\overline{SD}$	-	Go	$\overline{SD}$	$\overline{SD}$
5	-	-	-	-	Vo–	Vo–	n.c.	n.c.
6	-	-	-	-	-	-	n.c.	n.c.
11	-	Vo1+	Vo2+	Vo+	-	-	-	Vo1–
12	-	Vo1–	Go	-	-	-	-	Vo2–
13	Vo+	Vo2+	Vo1+	Go	-	-	Vo+	Vo1+
15	Vo–	Vo2–	Go	Vo–	-	-	Vo–	Vo2–
17	R	n.c./R	R	n.c.	-	-	R	n.c.

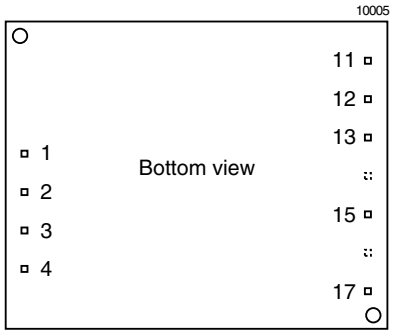


Fig. 31
Pin allocation

Protection Degree

The protection degree of the DC-DC converters is IP 40.

Cleaning Agents

In order to avoid possible damage, any penetration of cleaning fluids should be prevented, since the power supplies are not hermetically sealed.

Isolation

The electric strength test is performed as factory test in accordance with IEC/EN 60950 and UL 1950 and should not be repeated in the field. Power-One will not honour any guarantee claims resulting from electric strength field tests.

Table 16: Electric strength test voltages

Characteristic	Input to output		Output to output	Unit
	IMX 15	IMY 15		
Electric strength test voltage 1 s	1.2	3.0	0.1	kV_{rms}
	1.5	4.0	0.15	kV DC
Insulation resistance at 500 V DC	>100	>100	-	MΩ
Partial discharge extinction voltage	Consult factory		-	kV

Safety of operator accessible output circuit

If the output circuit of a DC-DC converter is operator accessible, it shall be an SELV circuit according to the IEC/EN 60950 related safety standards

The following table shows some possible installation configurations, compliance with which causes the output circuit of the DC-DC converter to be an SELV circuit according to IEC/EN 60950 up to a configured output voltage (sum of nominal voltages if in series or +/– configuration) of 42 V.

However, it is the sole responsibility of the installer to ensure the compliance with the relevant and applicable safety regulations. More information is given in: *Technical Information: Safety*.

Table 17: Insulation concept leading to an SELV output circuit

Conditions	Front end			DC-DC converter		Result
Supply voltage	Minimum required grade of isolation, to be provided by the AC-DC front end, including mains supplied battery charger	Maximum DC output voltage from the front end [1]	Minimum required safety status of the front end output circuit	Type	Measures to achieve the specified safety status of the output circuit	Safety status of the DC-DC converter output circuit
Mains ≤150 V AC	Operational (i.e. there is no need for electrical isolation between the mains supply voltage and the DC-DC converter input voltage)	≤150 V	Primary	IMY 15	Double or reinforced insulation, based on 150 V AC and DC (provided by the DC-DC converter)	SELV circuit
Mains ≤250 V AC	Basic	≤60 V	Earthed SELV circuit [2]	IMX 15 IMY 15	Operational insulation (provided by the DC-DC converter)	
		≤75 V	Hazardous voltage secondary circuit	IMX 15	Input fuse [3] output suppressor diodes [4], and earthed output circuit [2]	Earthed SELV circuit
		≤150 V		IMY 15	Supplementary insulation based on 250 V AC and double or reinforced insulation, based on the maximum rated output voltage from the front end (provided by the DC-DC converter)	SELV circuit
	Double or reinforced	≤60 V	SELV circuit	IMX 15 IMY 15	Operational insulation (provided by the DC-DC converter)	
		≤75 V	Double or reinforced insulated unearthed hazardous voltage secondary circuit [5]	IMX 15	Supplementary insulation based on the maximum rated output voltage from the front end (provided by the DC-DC converter)	
		≤120 V	TNV-2 circuit	IMY 15	Double or reinforced insulation, based on the maximum rated output voltage from the front end (provided by the DC-DC converter)	
		≤150 V	Double or reinforced insulated earthed or unearthed hazardous voltage secondary circuit			

[1] The front end output voltage should match the specified input voltage range of the DC-DC converter.

[2] The earth connection has to be provided by the installer according to the relevant safety standard, e.g. IEC/EN 60950.

[3] The installer shall provide an approved fuse (type with the lowest rating suitable for the application) in a non-earthed input conductor directly at the input of the DC-DC converter (see fig.: *Schematic safety concept*). For UL's purpose, the fuse needs to be UL-listed. See also: *Input Fuse*.

[4] Each suppressor diode should be dimensioned in such a way, that in the case of an insulation fault the diode is able to limit the output voltage to SELV (<60 V) until the input fuse blows (see fig.: *Schematic safety concept*).

[5] Has to be insulated from earth by at least basic insulation according to the relevant safety standard, based on the maximum output voltage from the front end.

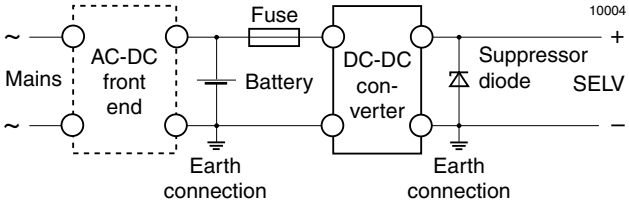


Fig. 32
Schematic safety concept. Use fuse, suppressor diode and earth connection as per table Safety concept leading to an SELV output circuit.

Description of Options

Table 18: Survey of options

Option	Function of option	Characteristic
–8	Extended operational ambient temperature range	T_A = –40...85 °C
R	Input and magnetic feedback	
G	Synchronous rectification	
K	Alternativ pinout	
i	Inhibit	
C	C-pinout	
L	Surface mount version with PCB lid	
Z	Open frame	

Option -8
Extended Temperature Range

Extension of the temperature range from standard –40...85 °C. In the upper temperature range the output power derating below should be observed. The modules will provide the specified output power with free air convection cooling.

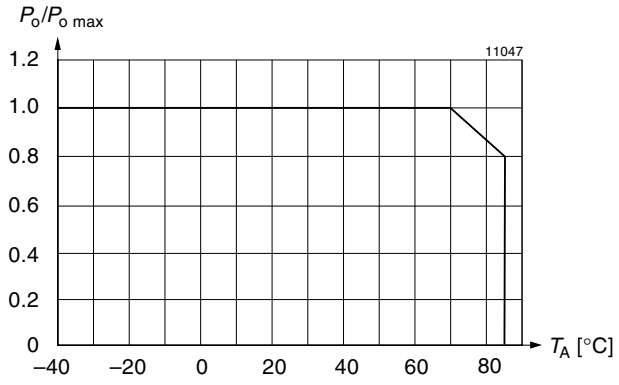


Fig. 33
Maximum allowed output power versus ambient temperature.

Option R
R Input and Magnetic Feedback

R specifies magnetic feedback from the output for closer regulation of the output voltages of double output and single output synchronous rectification units. It is especially suited for applications where the two outputs are either connected in parallel or in series to form one strong and tight regulated output. It enables the adjustment of the output voltages via the R-input on the secondary side by an external resistor or an external voltage source in the range of approximative 80...105% of $U_{o\ nom}$. Option R excludes the Trim input (see also table: *Pin allocation*), option K as well as the possibility to operate several converters with the outputs connected in parallel.

Option K
Alternative Pinout

Option K configures the electrically isolated double outputs to the alternative pinout with outputs connected in series (Vo+/Go/Vo–) and common ground.

However instead of using units with option K, it is recommended to use the standard double output units by providing the printed circuit board with an additional pin hole (for pin 12 of double output units) connected to pin hole 13. This will provide more design-in flexibility since by that both pinouts may be used on the same PCB.

Option i
Inhibit

Excludes shut down and option K.

The output(s) of the converter may be enabled or disabled by means of a logic signal (TTL, CMOS, etc.) applied to the inhibit pin. No output voltage overshoot will occur when the unit is turned on. If the inhibit function is not required the inhibit pin should be connected to Vi– to enable the output (active low logic, fail safe).

Converter operating: –10 V...0.8 V
Converter inhibited
or open inhibit pin: 2.4 V...$U_{i\ max}$

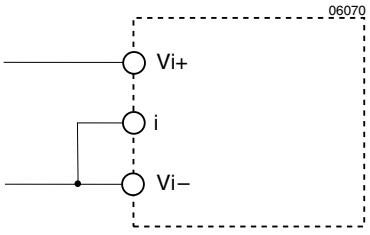


Fig. 34
If the inhibit is not used the inhibit pin should be connected to Vi–