

С

175°C Rated Maximum Junction Temperature Trencher Mosrets Power Mosrets

GO

P-Channel MOSFET

P-Channel 8-V (D-S), 175°C MOSFET

TO-263

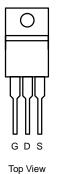
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SUB15P01-52

Н GDS Top View

PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)		
	$0.052 @ V_{GS} = -4.5 V$	-15		
-8	0.070 @ V _{GS} = -2.5 V	-10		
	0.105 @ V _{GS} = -1.8 V	-10.5		





DRAIN connected to TAB

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ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter Drain-Source Voltage		Symbol	Limit	Unit		
		V _{DS}	-8	V		
Gate-Source Voltage		V _{GS}	±8			
Continuous Drain Current $(T_J = 175^{\circ}C)$	$T_C = 25^{\circ}C$		-15			
	$T_{C} = 125^{\circ}C$		-8.7			
Pulsed Drain Current		I _{DM}	-25	A		
Avalanche Current		I _{AR}	-10			
Repetitive Avalanche Energy ^b	L = 0.1 mH	E _{AR}	5	mJ		
Power Dissipation	T _C = 25°C (TO-220AB and TO-263)	_	25 ^d			
	T _A = 25°C (TO-263) ^c	P _D	2.1	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Junction-to-Ambient	PCB Mount (TO-263) ^c	R _{thJA}	58	70		
Junction-to-Case		R _{thJC}	5	6	°C/W	
Junction-to-Lead		R _{thJL}	16	20		

Notes:

Package limited. a.

b. Duty cycle \leq 1%.

When mounted on 1" square PCB (FR-4 material). See SOA curve for voltage derating. c.

d.

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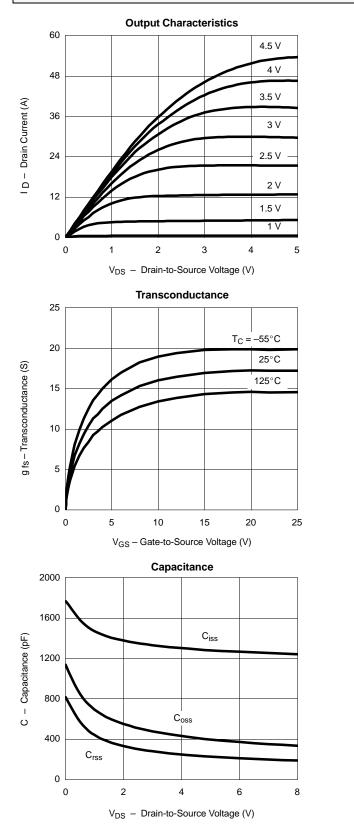
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static				1	1	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = -250 μ A	-8			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, \ I_D = -250 \ \mu A$	-0.45			
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ±8 V			±100	nA
Zero Gate Voltage Drain Current		$V_{DS} = -6.4$ V, $V_{GS} = 0$ V			-1	
	IDSS	$V_{DS} = -6.4 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			-50	μΑ
		$V_{DS} = -6.4 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175^{\circ}\text{C}$			-150	
On-State Drain Current ^a		V_{DS} = -5 V, V_{GS} = -4.5 V	-25			
	I _{D(on)}	V_{DS} = -5 V, V_{GS} = -2.5 V	-10			A
		$V_{GS} = -4.5$ V, $I_D = -10$ A		0.043	0.052	
Drain-Source On-State Resistance ^a		V_{GS} = -4.5 $$ V, I_{D} = $$ -10 A, T_{J} = 125 $^{\circ}C$			0.065	
	^r DS(on)	V_{GS} = -4.5 V, I _D = -10 A, T _J = 175 °C			0.075	Ω
		$V_{GS} = -2.5$ V, $I_D = -5$ A			0.070	
		$V_{GS} = -1.8$ V, $I_D = -2$ A			0.105	
Forward Transconductancea		$V_{DS} = -5 V, I_D = -10 A$		16		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = -4 V, f = 1 MHz		1300		pF
Output Capacitance	C _{oss}			430		
Reversen Transfer Capacitance	C _{rss}			245		
Total Gate Charge ^c	Qg			10.5	15	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = -4 V$, $V_{GS} = -4.5 V$, $I_{D} = -10 A$		1.6		
Gate-Drain Charge ^c	Q _{gd}			2		
Turn-On Delay Time ^c	t _{d(on)}			10	20	
Rise Time ^c	tr	$V_{DD} = -4 \text{ V, } \text{R}_{\text{L}} = 0.22 \Omega$ $\text{I}_{\text{D}} \simeq -15 \text{ A}, \text{ V}_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{G}} = 2.5 \Omega$		16	25	- ns
Turn-Off Delay Time ^c	t _{d(off)}			30	45	
Fall Time ^c	t _f			25	40	
Source-Drain Diode Ratings a	nd Characteristic	s (T _C = 25°C) ^b				
Continuous Current	Is				-15	
Pulsed Current	I _{SM}				-25	A
Forward Voltage ^a	V _{SD}	$I_{F} = -15 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$			-1.5	V
Reverse Recovery Time	t _{rr}			45	75	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = -15 A, di/dt = 100 A/μs		-1	-1.5	Α
Reverse Recovery Charge	Q _{rr}			0.023	0.056	μC

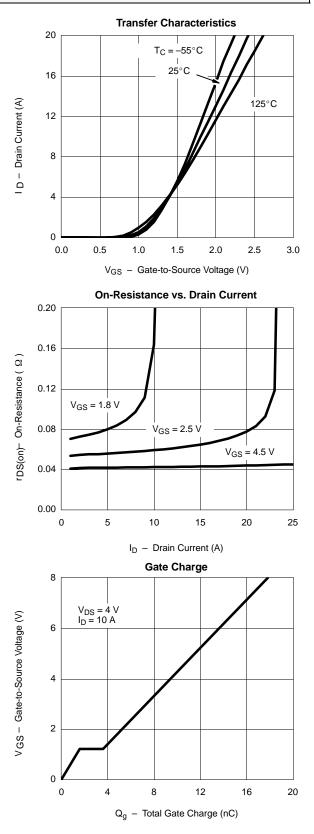
Notes: a. Pulse test; pulse width $\leq 300 \ \mu$ s, duty cycle $\leq 2\%$. b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.



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TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



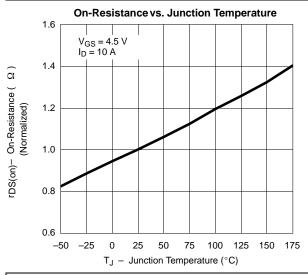


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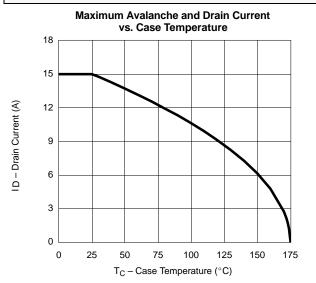
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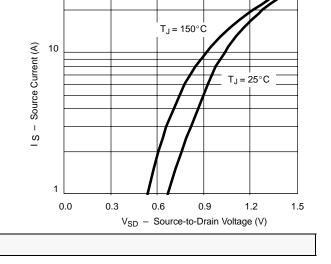


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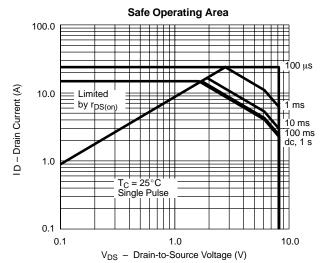


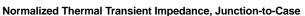


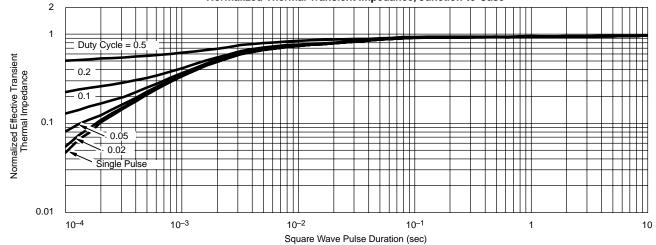


Source-Drain Diode Forward Voltage

30









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